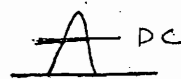
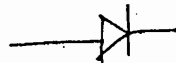
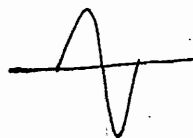


Diodes: Applications:

(1). Rectifiers:



DC
power
supply

(2). Filter :

(3). Voltage Regulator:

(4)*. Clippers

(5)*. Clampers

(6)*. Peak detector

(7). Voltage Multiplier

(8). Diode as a digital logic gate. (AND gate & OR gate)

(9)*. Diode is a analog gate. (sampling gate)

(10). Diode as a varactor diode.

(11)*. Zener diode (Voltage Limiter).

(12). Diode Resistance

(i). Static Resistance

* (ii). Dynamic Resistance :- small s/g analysis of a diode

(13). Diode capacitance

(i). Transition capacitance (C_T)

(ii). Diffusion capacitance (C_D)

BJT

(1). BJT device analysis

(2). BJT Biasing (DC)

(3). Small s/g Amplifiers (voltage amplifiers)

(4). Large s/g Amplifiers (power amplifiers)

(5). Feedback theory

Negative feedback theory (Amplifiers)

Low freq. Analysis

High freq. Analysis

Frequency Response

Positive feedback theory (oscillators)

Integrated theory (op amps):

(1). Multistage Amplifiers

(i). Effect of cascading on Bandwidth.

(ii). Important cascading designs.

(a). Cascode Amplifier (CE-CB)

(OR)

Wide Band Amplifier

(b). Darlington pair / high P/P impedance (CC-CC)

(2). Coupling techniques:

(i). RC coupling

(ii). Direct coupling

(3). Differential Amplifiers

(4). Applications of op-amp

FET / MOSFET:

- (1) FET device
 - (2) FET Biasing
 - (3) FET Amplifiers
- } FET

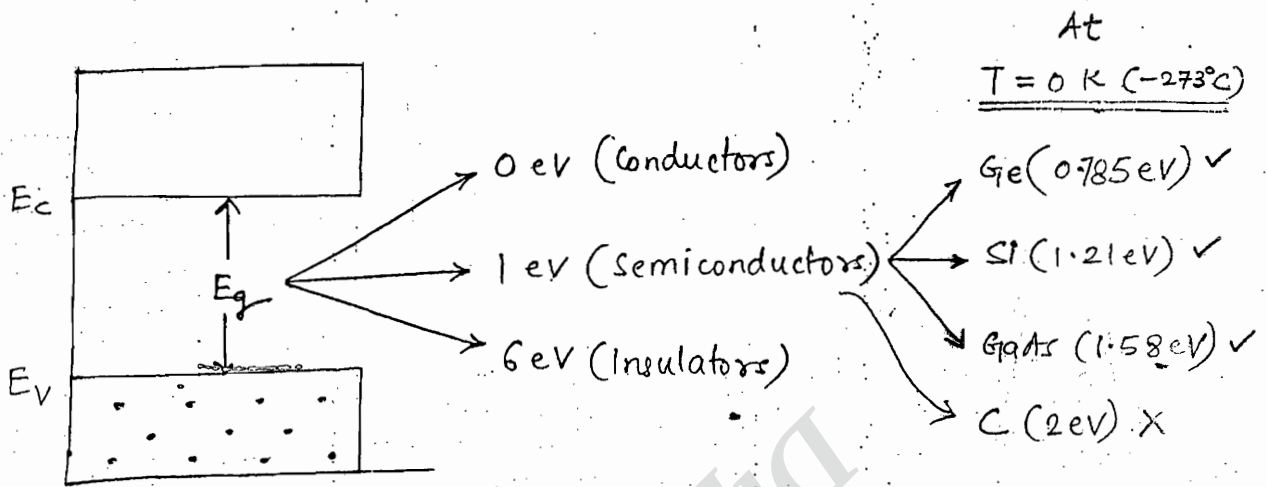
MOSFET

- (1). MOSFET device
- (2). MOSFET Biasing
- (3). MOSFET Amplifiers

Introduction to Electronics:

3

Q. Why 'Si' and 'Ge' are generally preferred compare to GaAs?



At Room Temperature $27^\circ\text{C } (300\text{ K})$:

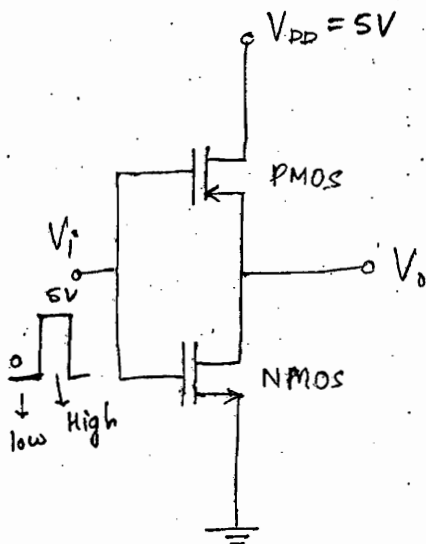
C is a bad semi-conductor.

A good semi-conductor must conduct at room temp.

A. The energy gap value of Si and Ge are less compared to GaAs, we expect more conduction in case of Si & Ge.

Q. Why GaAs is used in present CMOS Technology?

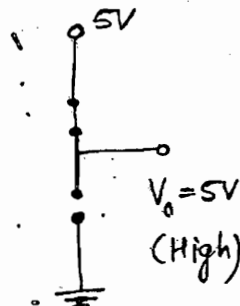
CMOS



$V_i = 0$ (Low)

PMOS $\rightarrow$ ON

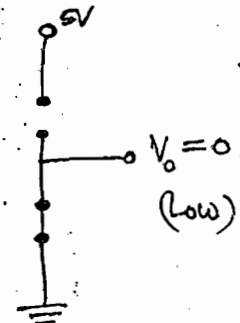
NMOS $\rightarrow$ OFF



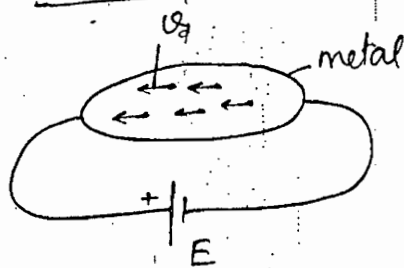
$V_i = 5\text{V}$ (High)

PMOS $\rightarrow$ OFF

NMOS $\rightarrow$ ON



Mobility



$$v_d \propto E$$

$$v_d = \mu E$$

$$\text{Mobility, } \mu = \frac{\text{Drift velocity}}{\text{Electric field}} \quad (\text{m}^2/\text{V}\cdot\text{sec})$$

Mobility of electron (μ_e): (Room temp = 27°C)

$$\mu_e (\text{Si}) = 1300 \text{ cm}^2/\text{V}\cdot\text{sec}$$

$$\mu_e (\text{Ge}) = 3800 \text{ cm}^2/\text{V}\cdot\text{sec}$$

$$\mu_e (\text{GaAs}) = 8500 \text{ cm}^2/\text{V}\cdot\text{sec} \quad \checkmark \quad (\text{High switching speed})$$

Temperature with standing capability

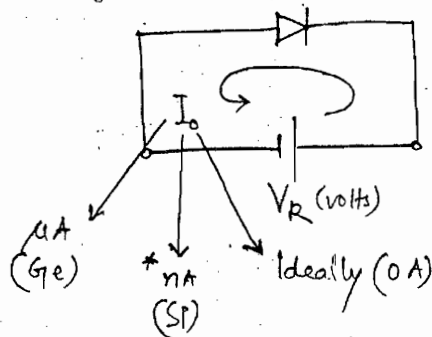
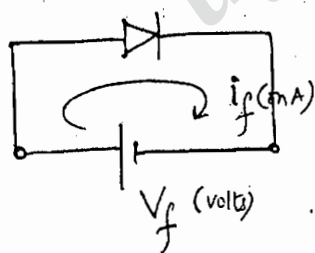
$$T(\text{Ge}) \approx 100^\circ\text{C}$$

$$T(\text{Si}) \approx 200^\circ\text{C}$$

$$* T(\text{GaAs}) \approx 200^\circ\text{C}$$

Q. Why 'Si' is more important than 'Ge'?

(1). $I_0 \rightarrow$ Reverse Saturation (or) Leakage current:



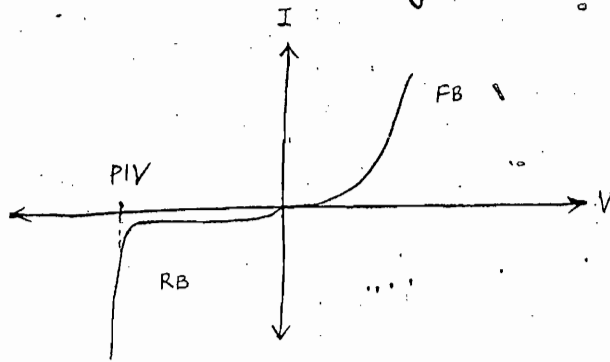
(2). Temperature with standing capability

Power dissipation (Heating effect)

$$T_{\text{Ge}} \approx 100^\circ\text{C}$$

$$* T_{\text{Si}} \approx 200^\circ\text{C}$$

(3). Peak Inverse Voltage (PIV):



* $PIV (Si) \approx 1000 V$

$PIV (Ge) \approx 400 V$

Q. Give the important properties of GaAs?

(1). At high frequency applications GaAs is used (mobility of GaAs is more).

(2). $\mu_e (GaAs) \rightarrow 8,500 \text{ cm}^2/\text{V-sec.}$

$\mu_h (GaAs) \rightarrow 400 \text{ cm}^2/\text{V-sec.}$

for Si:

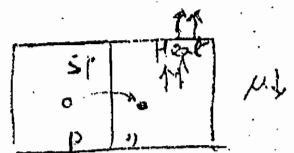
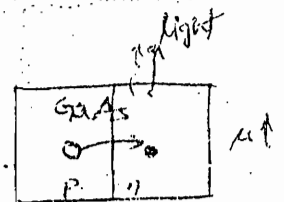
$\mu_e (Si) \rightarrow 1300 \text{ cm}^2/\text{V-sec}$

$\mu_h (Si) \rightarrow 500 \text{ cm}^2/\text{V-sec}$

for Ge:

$\mu_e (Ge) \rightarrow 3,800 \text{ cm}^2/\text{V-sec}$

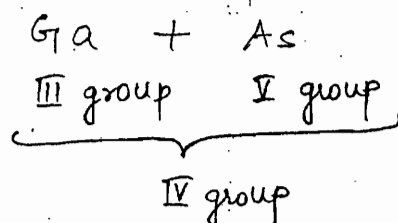
$\mu_h (Ge) \rightarrow 1,800 \text{ cm}^2/\text{V-sec}$



(3). GaAs is a best example for direct band gap.

Si & Ge are best examples for indirect band gap.

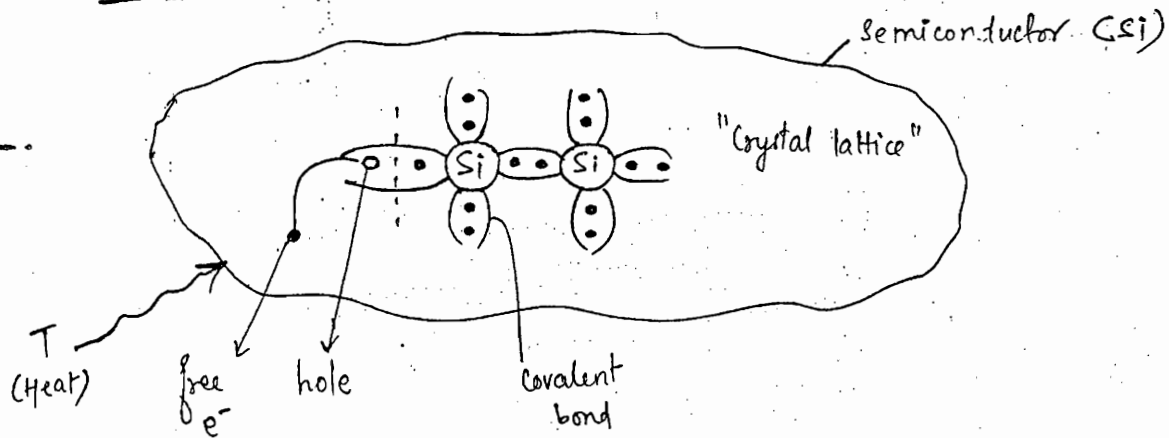
(4). GaAs is a compound semiconductor.



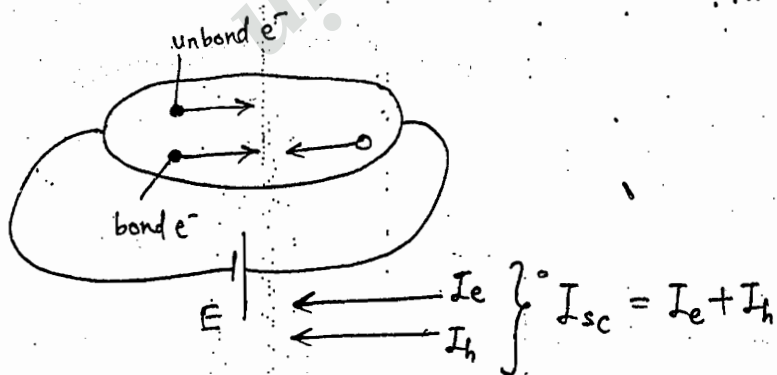
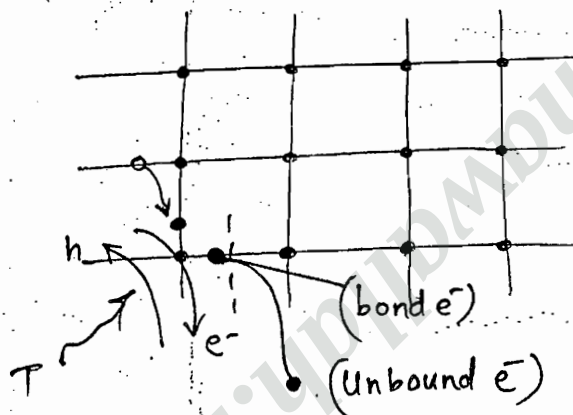
(5). GaAs is used in optics.

Q. Why mobility of $e^- >$ mobility of holes?

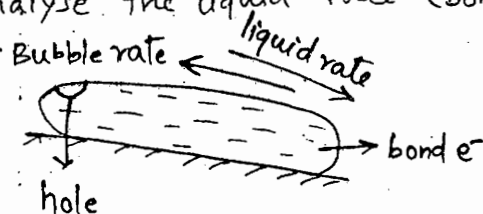
Hole concept:



Crystal Lattice:

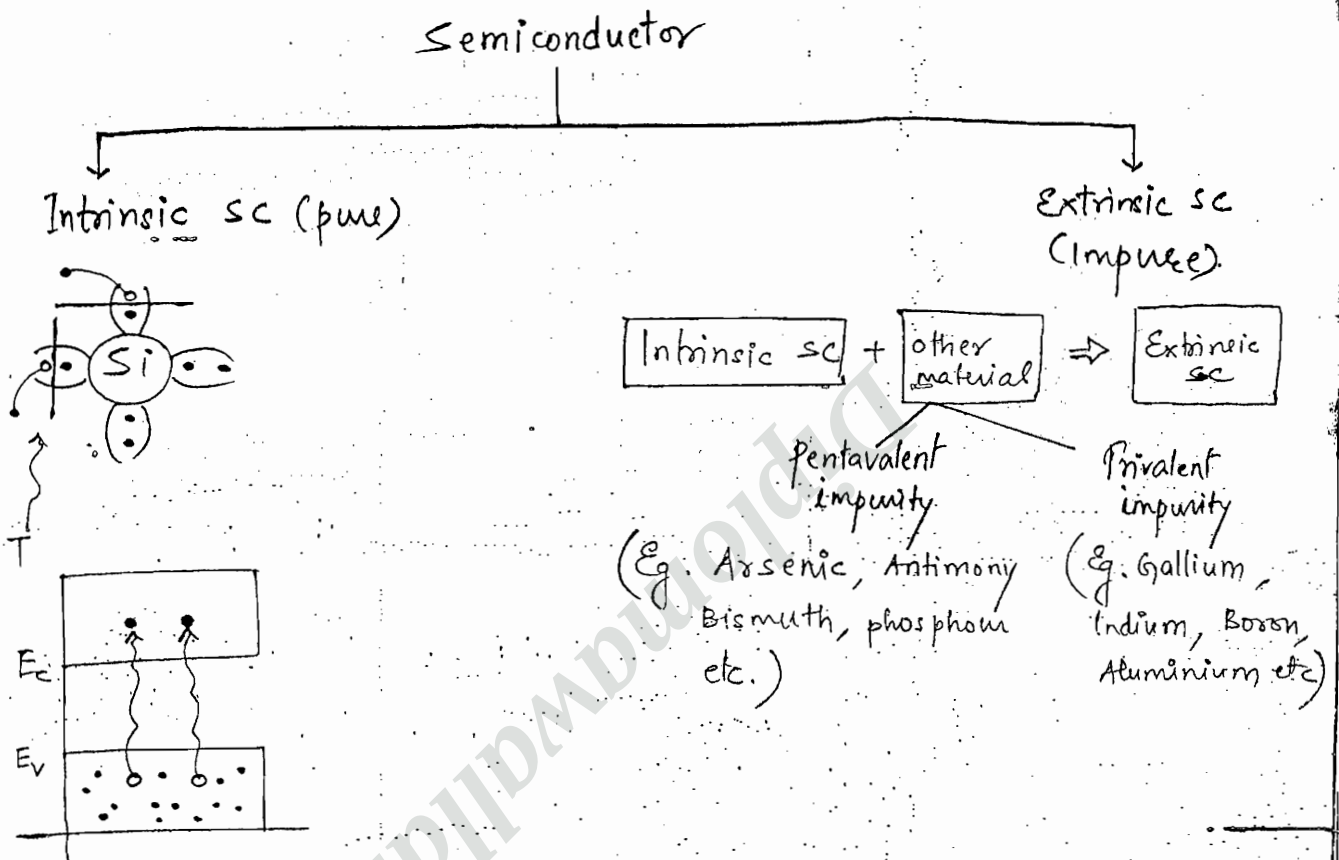


Eg: Glass-Bubble $\rightarrow$ Liquid rate is not easy to analyse, so bubble rate (hole) is easy to analyse the liquid rate (bond E).



the mobility of an unbound e^- (free e^-) is always⁷ greater than ~~than~~ bond e^- (valence electrons).

Q. Give the classification of semiconductor:



$n \rightarrow$ concentration of e^-/cm^3

$p \rightarrow$ conc. of holes $/\text{cm}^3$

$n_i \rightarrow$ Intrinsic conc. $(e-h) / \text{cm}^3$.

$$n_i = p = n$$

$$n_i \propto \text{temp}$$

Si ::

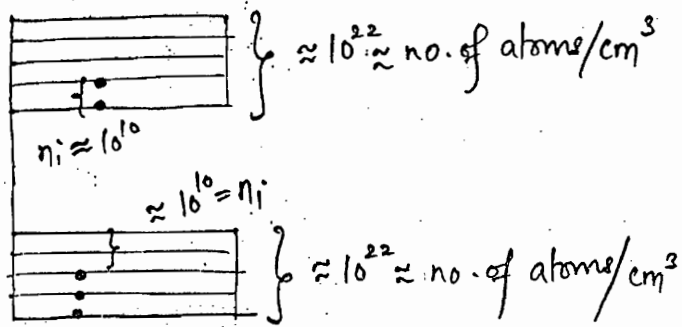
$$n_i (300 \text{ K}) \rightarrow 1.5 \times 10^{10} / \text{cm}^3$$

$$\text{No. of atoms} / \text{cm}^3 \rightarrow 5.0 \times 10^{22} / \text{cm}^3$$

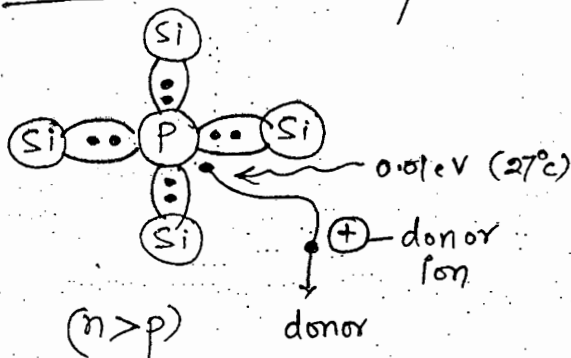
Ge :

$$n_i (300 \text{ K}) \rightarrow 2.5 \times 10^{13} / \text{cm}^3 \text{ (Eg)} \uparrow$$

$$\text{No. of atoms} / \text{cm}^3 \rightarrow 4.4 \times 10^{22} / \text{cm}^3 \text{ (size of Ge)} \uparrow$$



Pentavalent Impurity:

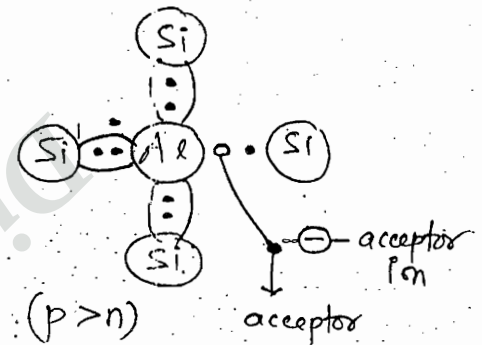


Majority carriers $\rightarrow$ Electrons

Minority carriers $\rightarrow$ holes

N-type Semi-conductor

Trivalent Impurity:



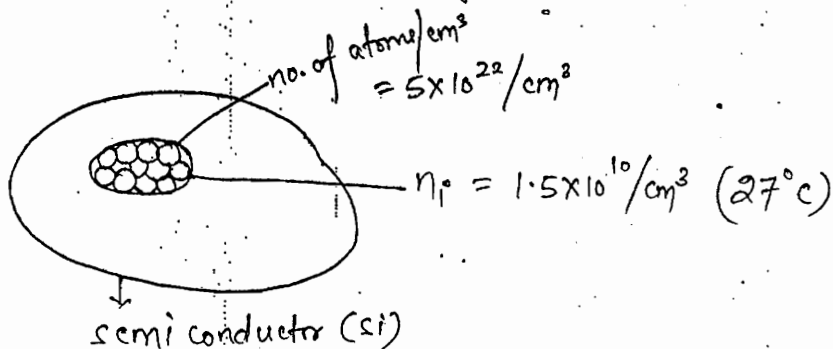
Majority carriers $\rightarrow$ Holes

Minority carriers $\rightarrow$ Electrons

P-type Semi-conductor

* That's why N-type device is always preferred to P-type because electron mobility is higher than hole mobility.

Q. Explain about doping concept in semi conductors?



Doping : $N_D \rightarrow \text{Donor atoms/cm}^3$

(1) Ordinary p-n diodes:

10^8 s.c atoms $\rightarrow$ 1 impurity

[1 'p' pentavalent]

5×10^{22} s.c atoms $\rightarrow N_D$

$$N_D = \frac{5 \times 10^{22}}{10^8} = 5 \times 10^{14} / \text{cm}^3$$

$$N_D > N_i \dots$$

(2) Zener Diode:

10^6 s.c atoms $\rightarrow$ 1 impurity

[1 'p']

5×10^{22} s.c atoms $\rightarrow N_D$

$$N_D = \frac{5 \times 10^{22}}{10^6} = 5 \times 10^{16} / \text{cm}^3$$

$$N_D \gg N_i$$

(3) Tunnel diode:

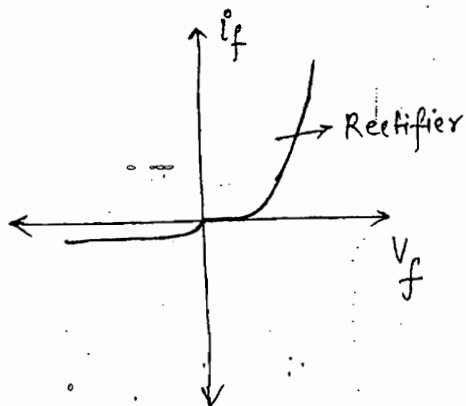
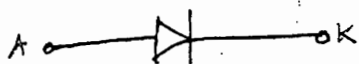
10^3 s.c atoms $\rightarrow$ 1 impurity

5×10^{22} s.c atoms $\rightarrow N_D$

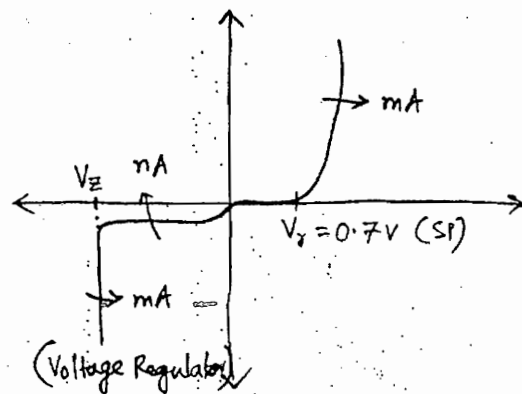
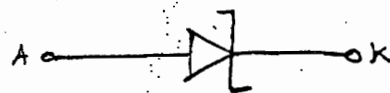
$$N_D = \frac{5 \times 10^{22}}{10^3} = 5 \times 10^{19} / \text{cm}^3$$

$$N_D \gg \gg N_i$$

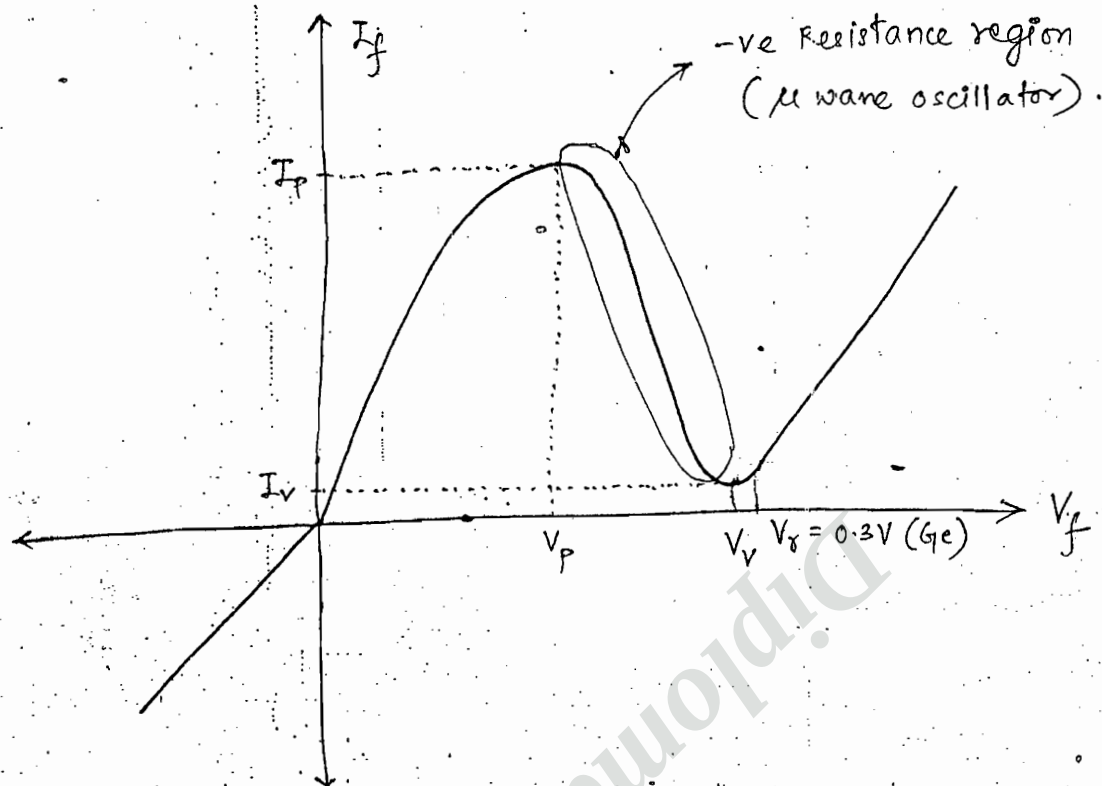
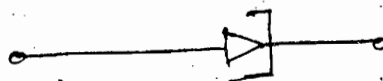
Ordinary p-n diode:



Zener Diode:



Tunnel diode:

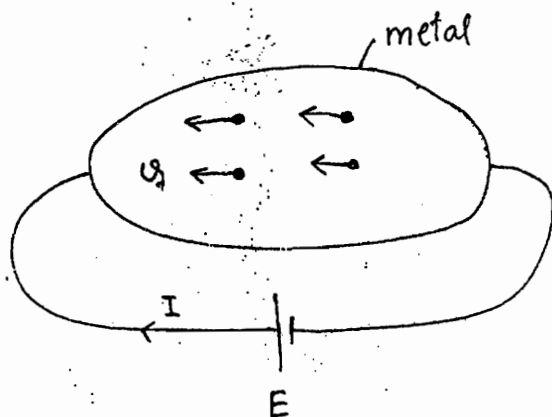


$$R = \frac{dV}{dI} = \frac{V_v - V_p}{I_v - I_p} = \dots -ve.$$

Q. Explain about drift current in Semiconductors?

Drift current

Drift → movement
(greek word)



$$V_d \propto E$$

$$V_d = \mu E$$

$$\mu = \frac{V_d}{E} \cdot m^2/V\text{-sec}$$

(1). "The current is produced due to the drifting of free electrons is called as Drift current".

(2). The current can occur in metals & semi-conductors.¹¹

(3). Drift current mechanism can also be called as "potential gradient".

Current Density: (J)

$$J = \frac{I}{A} \quad A/m^2$$

$$J = \frac{Ne}{t}$$

$$I = \frac{Ne v_d}{L}$$

$$J = \frac{Ne v_d}{AL}$$

$$(\because \eta = N/AL)$$

$$(\because f \rightarrow \text{charge density})$$

$$f = ne$$

$$J = nev_d$$

$$J = f v_d$$

Metals:

$$J = ne \mu E$$

$$(\because v_d = \mu E)$$

$$J = \sigma E$$

$$(\because \sigma = ne \mu)$$

Semi-conductors:

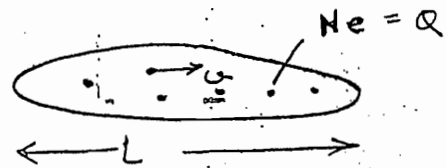
$$J_{sc} = J_n + J_p$$

$$J_n = nq \mu_n E$$

$$J_p = pq \mu_p E$$

$$J_{sc} = \underbrace{(n \mu_n + p \mu_p)}_{\sigma_{s.c.}} \cdot q E$$

$$\therefore \sigma_{s.c.} = (n \mu_n + p \mu_p) q$$



(a) Intrinsic s.c. - ($n=p=n_i$)

$$\sigma_{\text{intrinsic}} = n_i (\mu_n + \mu_p) q$$

(a) Extrinsic s.c. - ($n \neq p$)

(i) N-type: ($n > p$)

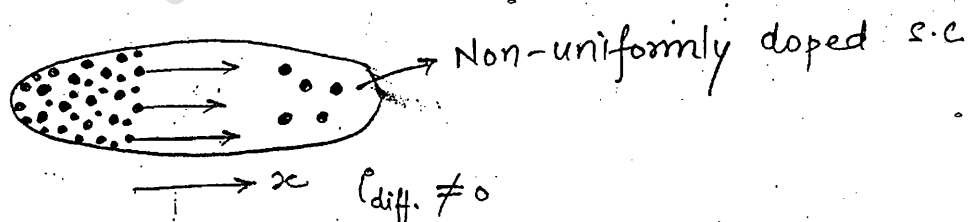
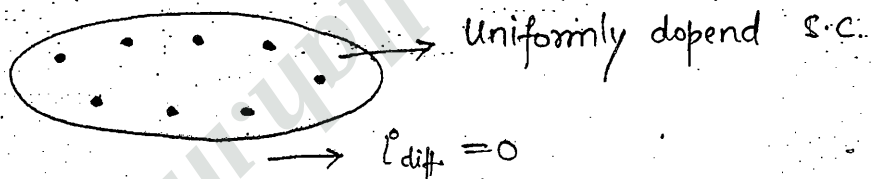
$$\begin{aligned} \sigma_{\text{N-type}} &= n q \mu_n \\ &\approx N_D q \mu_n \end{aligned}$$

(ii) P-type: ($p > n$)

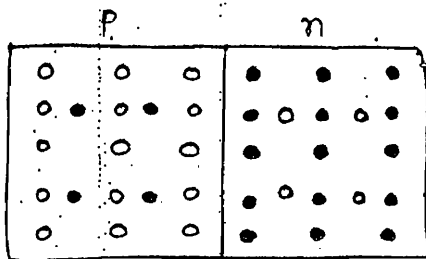
$$\begin{aligned} \sigma_{\text{P-type}} &= p q \mu_p \\ &\approx N_A q \mu_p \end{aligned}$$

Q. Explain about Diffusion current in semi-conductors?

Diffusion current:



Eg.



p-n jxn is a best example for non-uniformly doped s.c.

$$I_n \propto q \frac{dn}{dx}$$

$$I_n = D_n q \frac{dn}{dx}$$

($D_n \rightarrow$ Diffusion constant for e^-)

$$I_n = A q D_n \frac{dn}{dx}$$

$$I_p \propto q \frac{dp}{dx}$$

$$I_p = -D_p q \frac{dp}{dx}$$

($D_p \rightarrow$ Diffusion constant for holes)

$$I_p = -A q D_p \frac{dp}{dx}$$

Diffusion current $\rightarrow$ The rate of change of concentration w.r. to distance x is called as diffusion current.

Diffusion current mechanism can also be called as "concentration gradient".

Analysis:

According to kinetic gas theory -

$$D \propto \mu \Rightarrow \frac{D}{\mu} = V_T = \text{constant}$$

$$D = \mu V_T$$

$V_T \Rightarrow$ Volt equivalent temp. (or) thermal voltage

$$V_T = \frac{KT}{q} \quad \text{where, } K \rightarrow \text{Boltzman constant}$$

$T \rightarrow$ Temperature

In semi-conductors:

$$\frac{D_n}{\mu_n} = \frac{D_p}{\mu_p} = V_T$$

— Einstein Relation

$$V_T = \frac{KT}{q}$$

$$= \frac{T}{q/k}$$

$$\text{As } q = e^- = 1.6 \times 10^{-19} \text{ C}$$

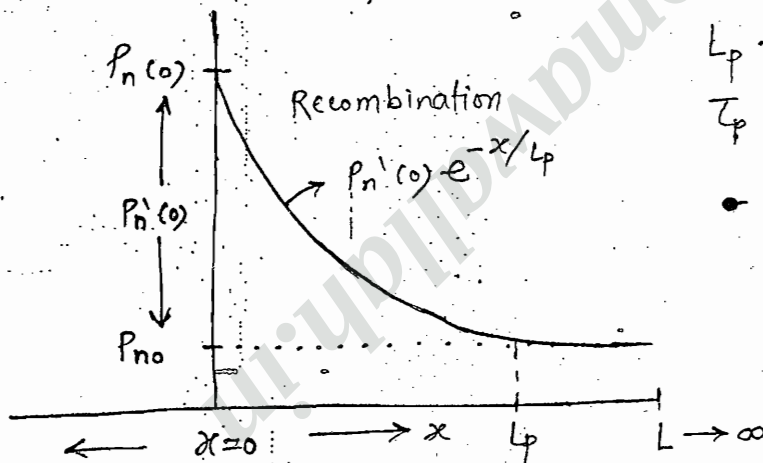
$$K = 1.38 \times 10^{-23} \text{ J/K}$$

$$V_T = \frac{T(K)}{11,600} \text{ volts}$$

Q. - Calculate volt equivalent Temp. at room temp. ?

A. $V_T = \frac{300}{11600} = 25.86 \text{ mV} \approx 26 \text{ mV} / (25 \text{ mV})$

Graphical Analysis:



$L_p \rightarrow$ Diffusion length
 $\tau_p \rightarrow$ Carrier life time

$$L_p^2 = D_p \tau_p$$

Diffusion rate $\propto \frac{1}{\text{Recombination rate}}$

Case - (1)

$$x = 0$$

$$P = P_n'(0) \cdot e^{-0/L_p}$$

$$= P_n'(0) \cdot (1)$$

$$= P_n'(0)$$

Case - (2)

$$x = L_p$$

$$P = P_n'(0) \cdot e^{-L_p/L_p}$$

$$= P_n'(0) \cdot \frac{1}{e}$$

$$= \frac{1}{e} \cdot P_n'(0)$$

Case - (3)

$$x = \infty$$

$$P = P_n'(0) \cdot e^{-\infty/L_p}$$

$$= P_n'(0) \cdot \frac{1}{e^\infty}$$

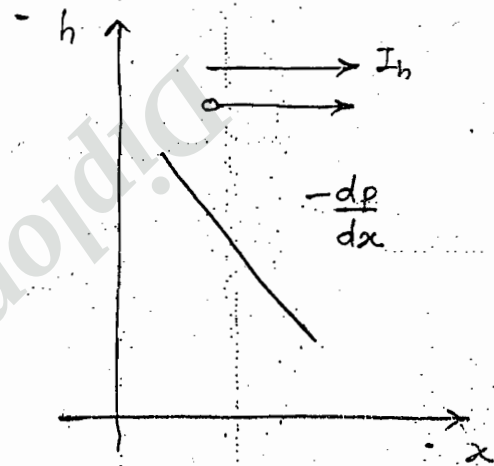
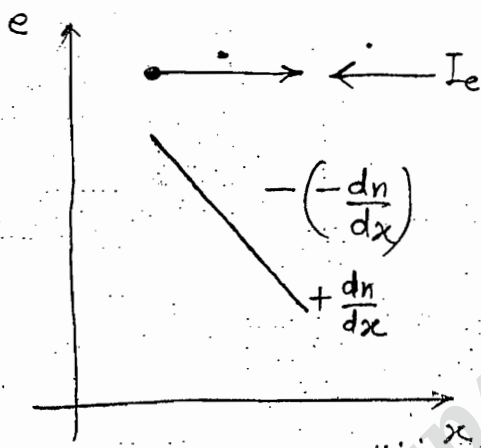
$$= 0$$

(1). When the thickness of material is more, recombination rate will be more, diffusion rate will be less.

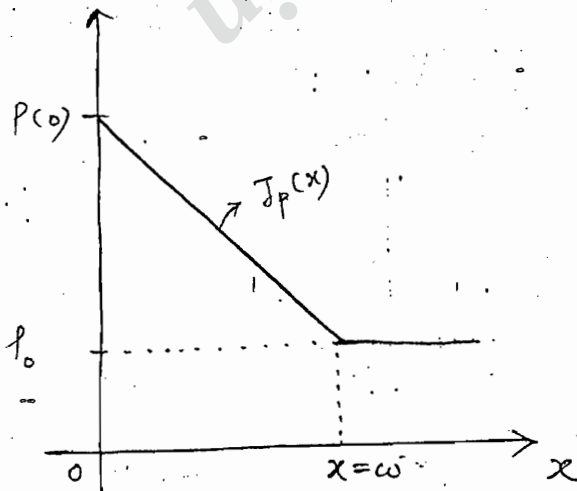
(Non-linear graph)

(2). When the thickness of material is very less, diffusion rate will increase but recombination rate will decrease.

(Graph is linear)

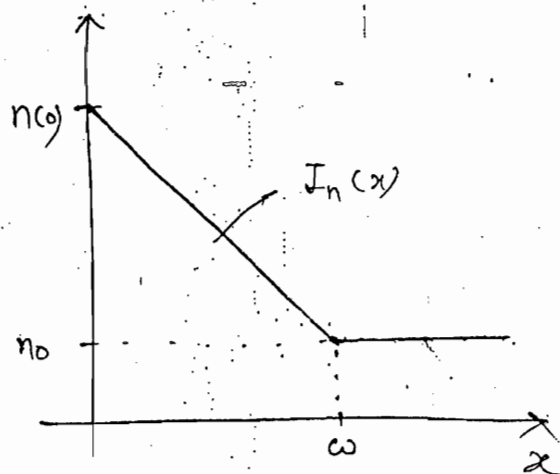


Problems in diffusion current:



$$\begin{aligned}
 J_p(x) &= -q D_p \frac{dp(x)}{dx} \\
 &= -q D_p \frac{P(0) - P_0}{0 - w} \\
 &= \frac{q D_p [P(0) - P_0]}{w}
 \end{aligned}$$

$$\begin{aligned}
 &\begin{aligned} &\longrightarrow \\ &\longrightarrow I_h \end{aligned} \left. \begin{array}{l} \\ \end{array} \right\} +ve
 \end{aligned}$$



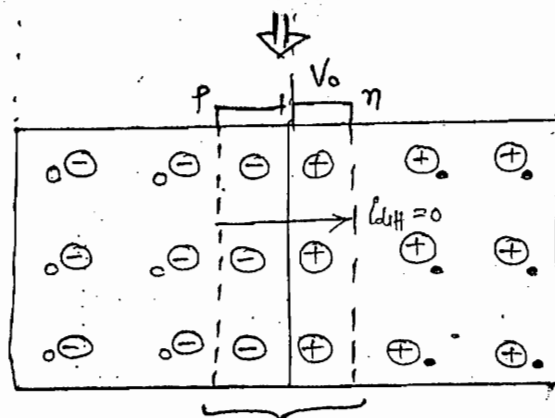
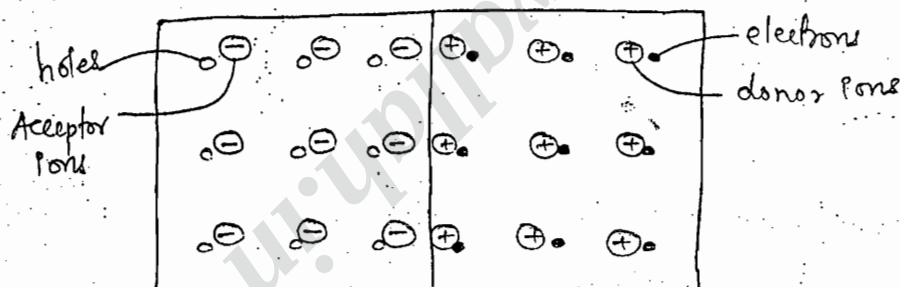
$$\begin{aligned}
 J_n(x) &= q D_n \frac{dn}{dx} \\
 &= q D_n \frac{n(0) - n_0}{0 - w} \\
 &= \frac{-q D_n (n(0) - n_0)}{w}
 \end{aligned}$$

$$\begin{aligned}
 &\xrightarrow{e^-} \\
 &\xleftarrow{I_e} \quad \left. \begin{array}{l} \\ \end{array} \right\} -ve
 \end{aligned}$$

Q. Explain about pn jxn theory in 3 conditions:

- (1) Open ckt pn jxn
- (2) Forward Biased
- (3) Reversed Biased.

(1) Open ckt pn jxn —



Depletion Region

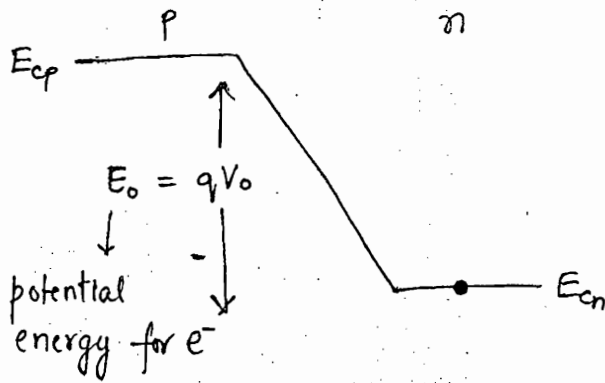
(or) Space charge Region (or) Transition region

V_0 (or) V_j (or) $V_s \rightarrow$ potential Barrier (or) jxn voltage (or) Contact potential (or) Cut in voltage

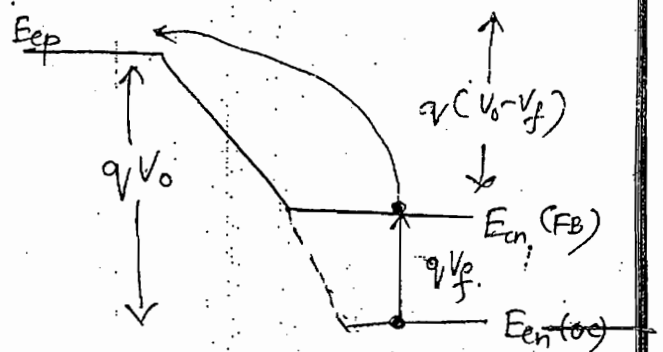
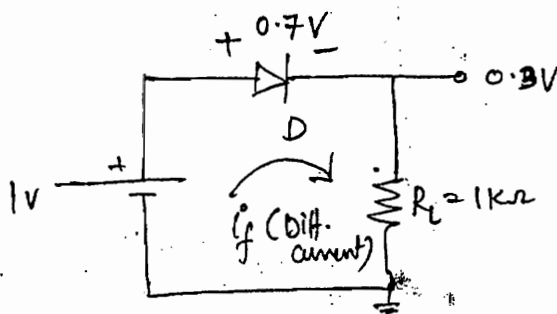
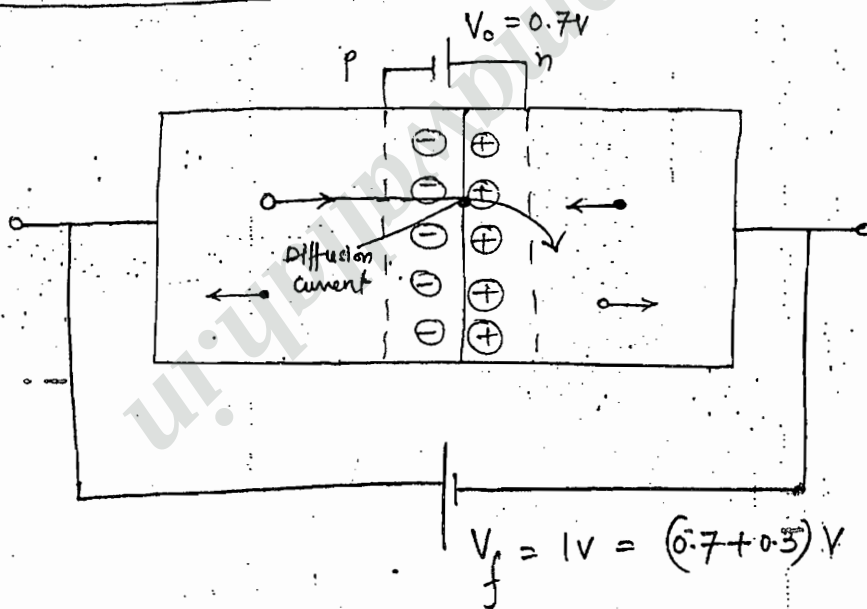
$$V_o = V_T \ln \left(\frac{N_A N_D}{n_i^2} \right)$$

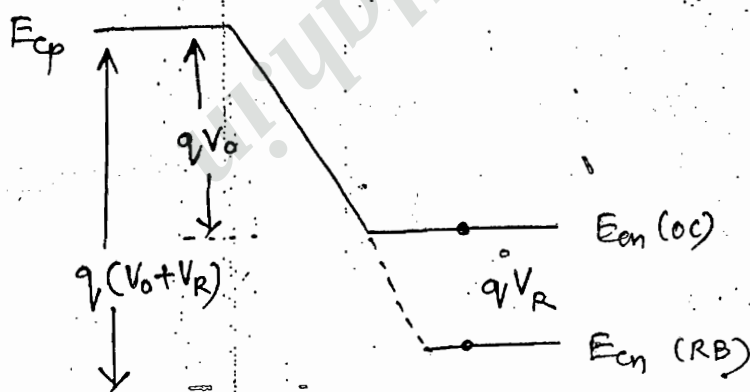
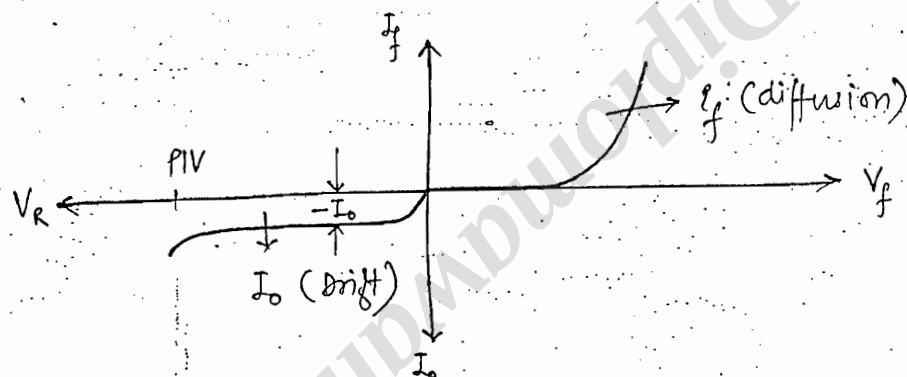
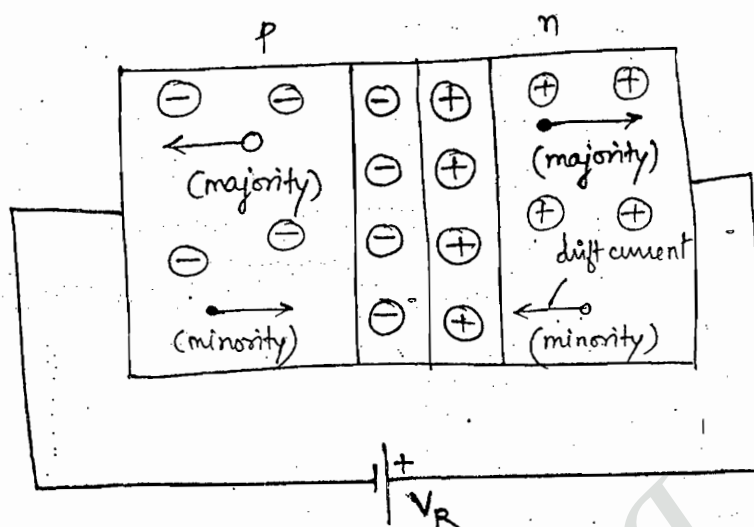
$$V_o \text{ (Si)} \rightarrow 0.5V \text{ to } 0.7V$$

$$V_o \text{ (Ge)} \rightarrow 0.1V \text{ to } 0.3V$$



(2) forward Bias:



(3). Reverse Bias —

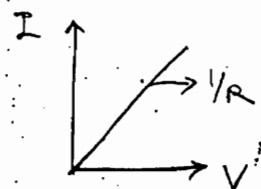
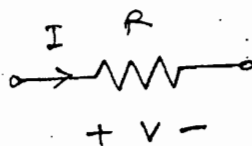
- * Due to appearance of immobile charge ions, resistance offered is more in R.B whereas in F.B resistance offered is less
- * Depletion layer in R.B has more width due to immobile charge ions ($\uparrow$).

Diode Applications:

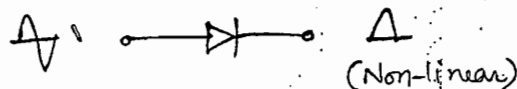
19

Linear Resistor

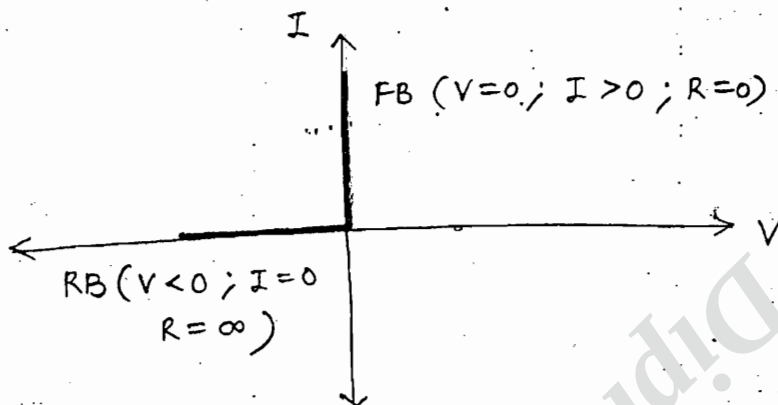
$$V = IR$$



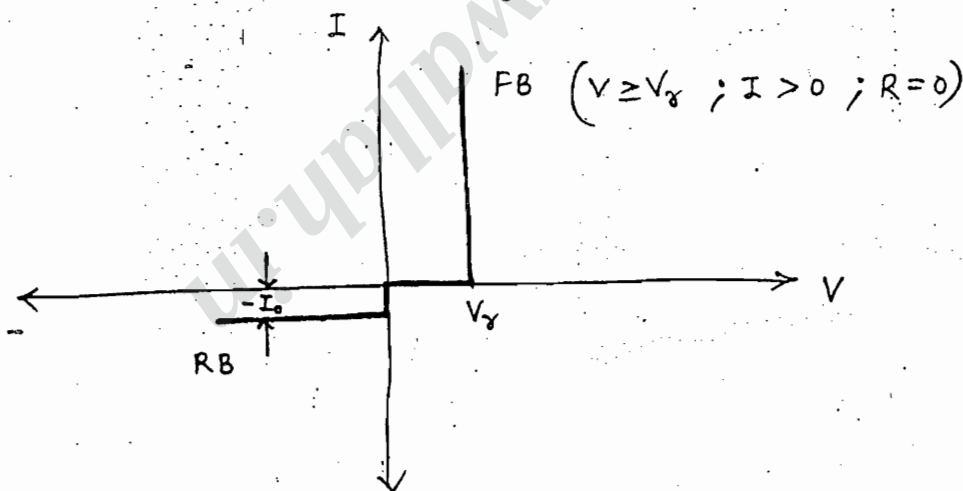
Ideal Diode:



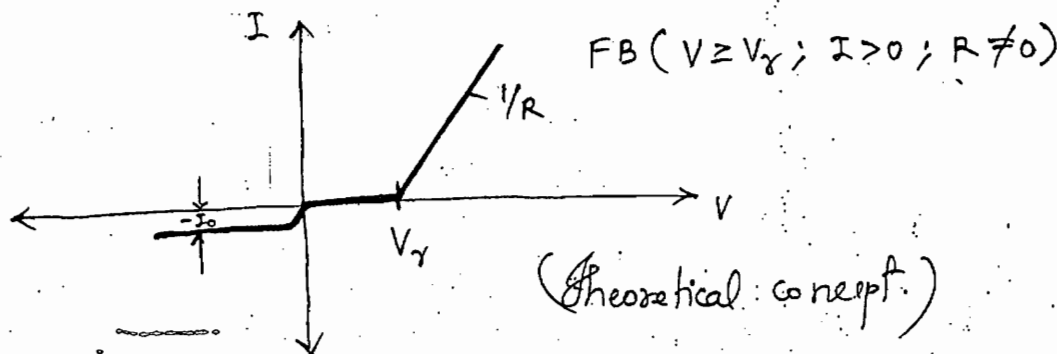
(Non-linear)



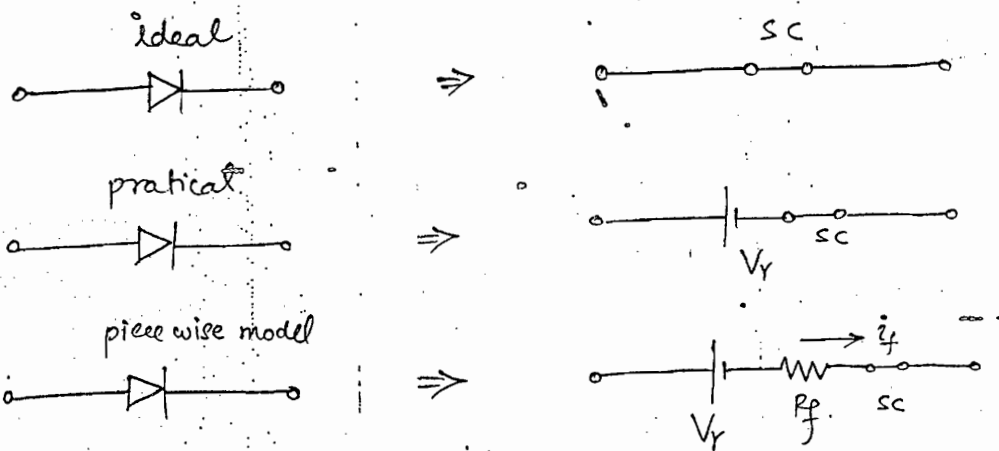
Practical diode:



Piece-wise linear model



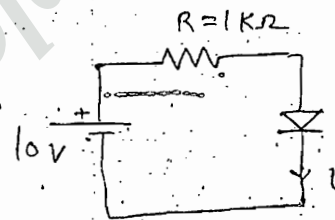
Equivalent models for a diode:



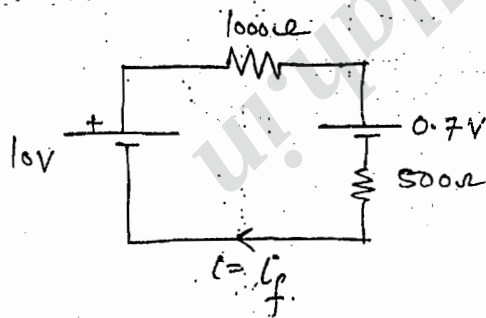
Problems:

Q1. V-I characteristics of pn diode is shown below.

$$i = \begin{cases} \frac{V-0.7}{500} & , V > 0.7 \\ 0 & , V < 0.7 \end{cases}$$

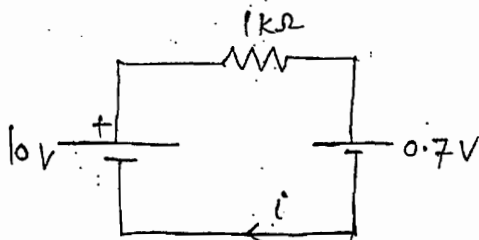


sol. Piece-wise linear model:



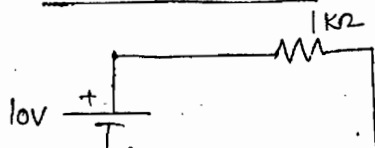
$$i = \frac{10-0.7}{1000+500} = 6.2 \text{ mA} \checkmark$$

Practical model:



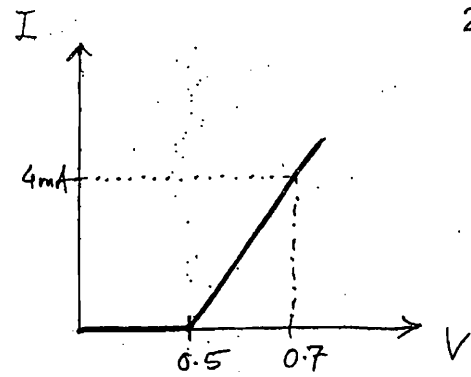
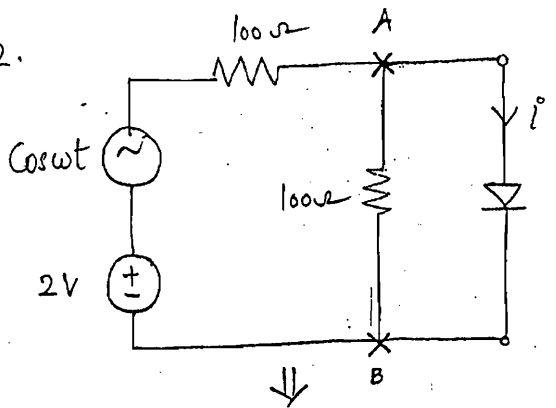
$$i = \frac{10-0.7}{1000} = 9.3 \text{ mA}$$

Ideal model:

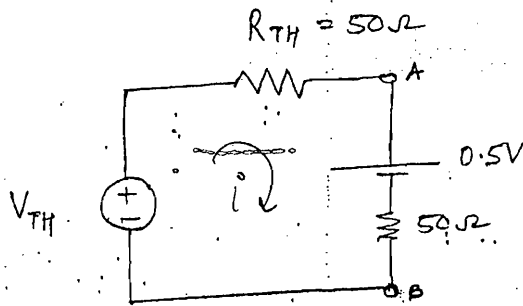


$$i = \frac{10}{1000} = 10 \text{ mA}$$

Q2.



Sol.



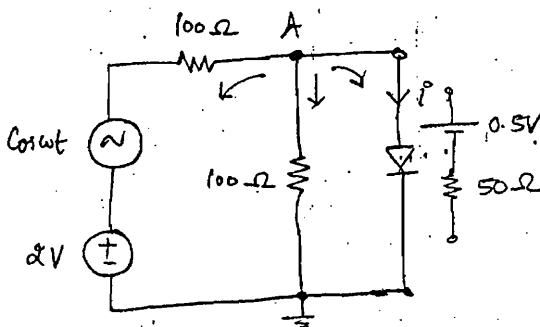
$$V_{TH} = \frac{\cos \omega t + 2}{2}$$

$$= 1 + \frac{\cos \omega t}{2}$$

$$i = \frac{\left(1 + \frac{\cos \omega t}{2}\right) - 0.5}{(50 + 50)} = \frac{0.5 + 0.5 \cos \omega t}{100}$$

$$i = \frac{0.5}{100} (1 + \cos \omega t) \text{ A}$$

$$\therefore i = 5(1 + \cos \omega t) \text{ mA}$$

Alter:

$$\frac{V_A - 2 - \cos \omega t}{100} + \frac{V_A}{100} + \frac{V_A - 0.5}{50} = 0$$

$$4V_A - 2 - \cos \omega t - 1 = 0$$

$$V_A = \frac{3 + \cos \omega t}{4}$$

$$i = \frac{V_A - 0.5}{50} = \frac{\frac{3 + \cos \omega t}{4} - 0.5}{50} = \frac{(1 + \cos \omega t)}{200} \text{ A}$$

$$= 5(1 + \cos \omega t) \text{ mA}$$

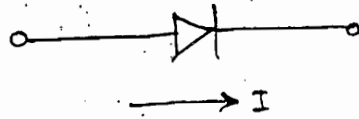
$$\therefore r_f = \frac{\Delta V}{\Delta i}$$

$$= \frac{0.7 - 0.5}{4 \text{ mA} - 0}$$

$$\therefore r_f = 50 \Omega \quad \left\{ \text{from graph} \right\}$$

$$\therefore V_f = 0.5 \text{ V}$$

Diode current Eqn:



$$I = I_0 (e^{\frac{V}{\eta V_T}} - 1)$$

where, $I \rightarrow$ total current through a diode

$\eta =$ Idealized factor

$I_0 \rightarrow$ Reverse saturation current (or) minority current

$\eta = 1$ (integrated theory) ✓
 $= 2$ (discrete theory)

$V \rightarrow$ applied voltage drop across a diode.

$V \rightarrow +ve$ (FB) $\rightarrow 0.7V$ etc

$V \rightarrow -ve$ (RB)

$V_T \rightarrow$ thermal voltage

$$V_T = \frac{T}{11600}$$

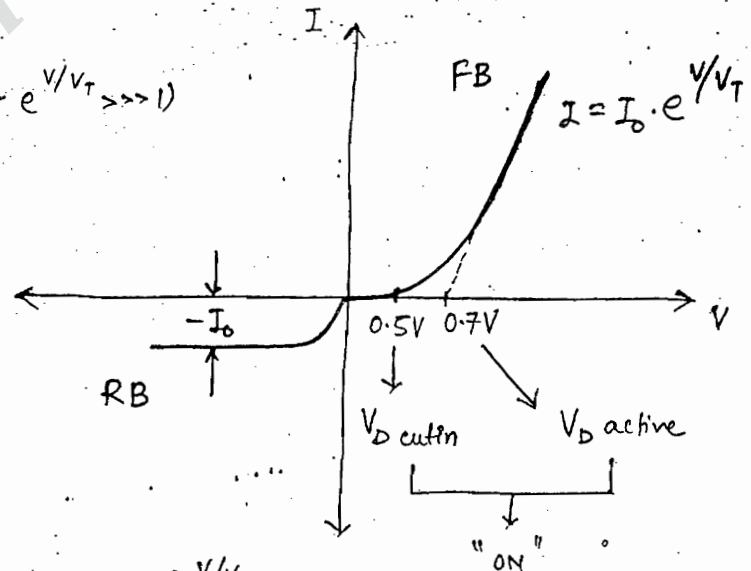
At room temperature, $V_T = 26mV$

Case (1): FB

$V = +ve$ (0.7V)

$$I = I_0 (e^{\frac{V}{V_T}} - 1) \quad (\because e^{\frac{V}{V_T}} \gg 1)$$

$$I = I_0 \cdot e^{\frac{V}{V_T}}$$



Case (2): RB

$V = -ve$

$$I = I_0 (e^{-\frac{V}{\eta V_T}} - 1)$$

($\because e^{-\frac{V}{V_T}} \ll 1$)

$$I = -I_0$$

Reverse saturation current.

Temperature dependence parameters in diode eqn:

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$$I = I_0 \left(e^{\frac{V}{V_T}} - 1 \right)$$

depends on T

depends on T ($V_T = \frac{T}{11600}$)

depends on T
(thermal agitation,
Breakage of covalent
Bonds)

I_0 vs temp:

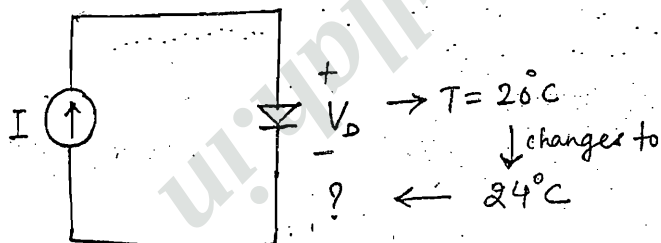
" I_0 increases by 7% per °C rise in temp."

(or)

" I_0 doubles for every 10°C rise in temp."

$$\therefore (1.07)^{10} = 1.967 \approx 2$$

V_D vs Temp:



$$\frac{dV_D}{dT} = -2.5 \text{ mV}/^\circ\text{C} \quad (\text{OR}) \quad -2 \text{ mV}/^\circ\text{C}$$

$$\frac{dV_D}{dT} = -2.5 \text{ mV}/^\circ\text{C}$$

$$(24 - 20) \times -2.5 \text{ mV} = dV_D$$

$$dV_D = -10 \text{ mV}$$

$$V_D' - V_D = -10 \text{ mV}$$

$$V_D' = (0.7 - 10 \times 10^{-3}) \text{ Volts} = 0.69 \text{ V}$$

$$= (700 - 10) \text{ mV} = 690 \text{ mV}$$

V_T Vs temp:

$$\uparrow V_T = \frac{I}{11600} \uparrow$$

Q6. $I_0 = 10 \text{ pA} \quad (20^\circ\text{C})$

$I_0 = ? \quad (40^\circ\text{C})$

Exact: $\frac{dI_0}{dT} = 0.07 \text{ pA}/^\circ\text{C}$

$$I_0' = I_0 \cdot 1.07^{(40-20)} = (1.07)^{20} I_0 = 3.89 I_0 = 38.9 \text{ pA}$$

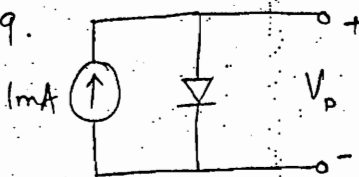
Approx:

$I_0 \rightarrow$ double for every 10°C rise in temp.

$I_0' \rightarrow 20 \text{ pA}$ at 30°C

$I_0' \rightarrow 40 \text{ pA}$ at 40°C

Q9.



$20^\circ\text{C}, V_D = 700 \text{ mV}$

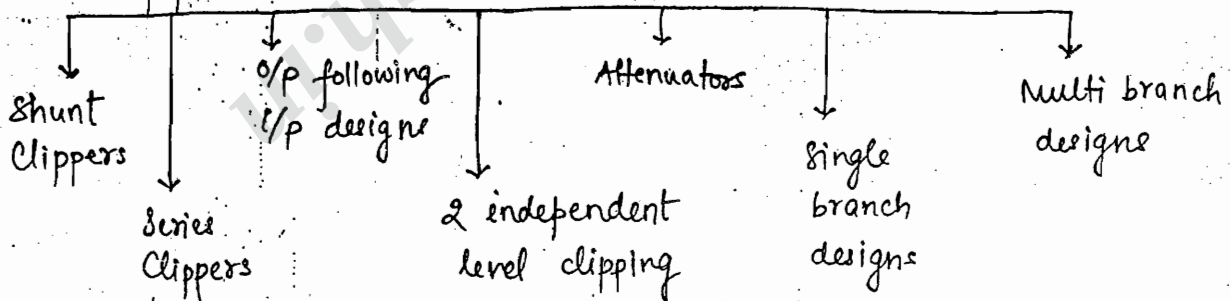
$40^\circ\text{C}, (?)$

$$\frac{dV_D}{dT} = -2.5 \text{ mV}/^\circ\text{C}$$

$$dV_D = -2.5 \times (20) = -50 \text{ mV}$$

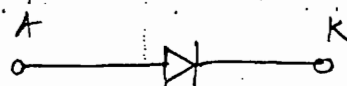
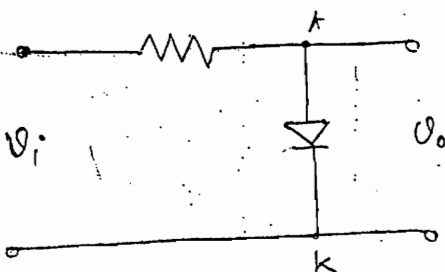
$$\therefore V_D' = 650 \text{ mV at } 40^\circ\text{C}$$

Clippers:



Shunt Clippers:

Model 1:



$$V_A > V_K \rightarrow \text{sc}$$

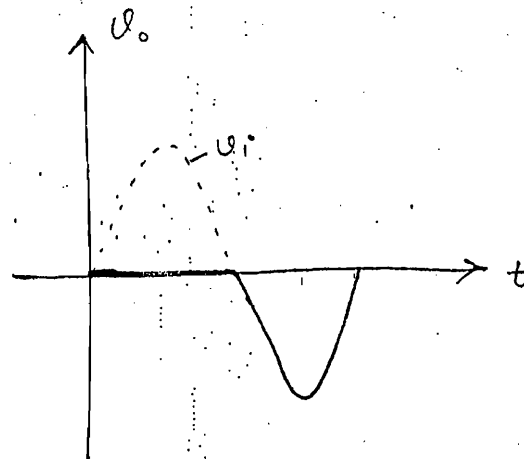
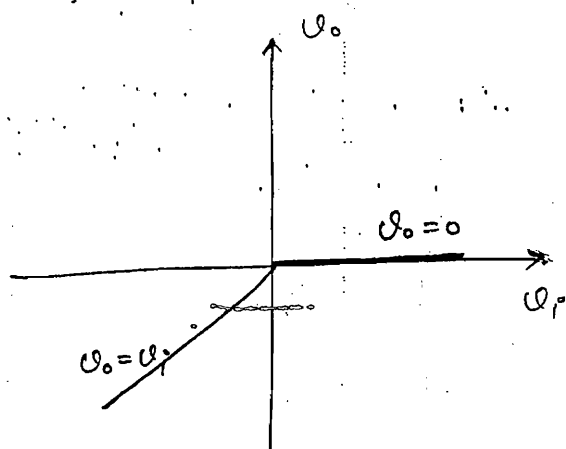
$$V_A < V_K \rightarrow \text{oc}$$

$$V_i < 0 \rightarrow D(\text{off}) \Rightarrow V_o = V_i$$

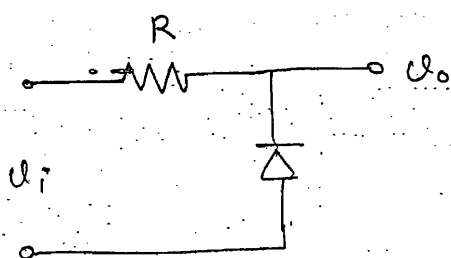
$$V_i > 0 \rightarrow D(\text{on}) \Rightarrow V_o = 0$$

Transfer characteristics :

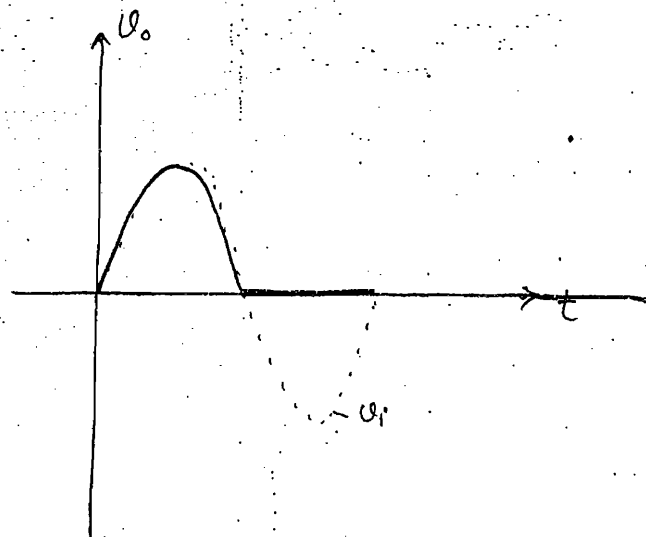
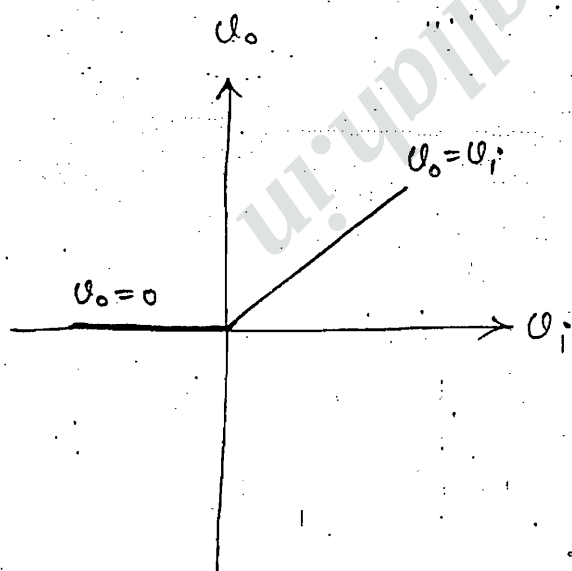
25



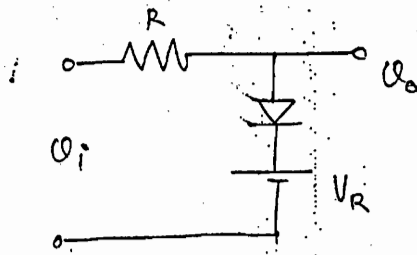
Model - 2 :



$V_i < 0 \quad D \text{ (on)} \rightarrow V_o = 0$
 $V_i > 0 \quad D \text{ (off)} \rightarrow V_o = V_i$

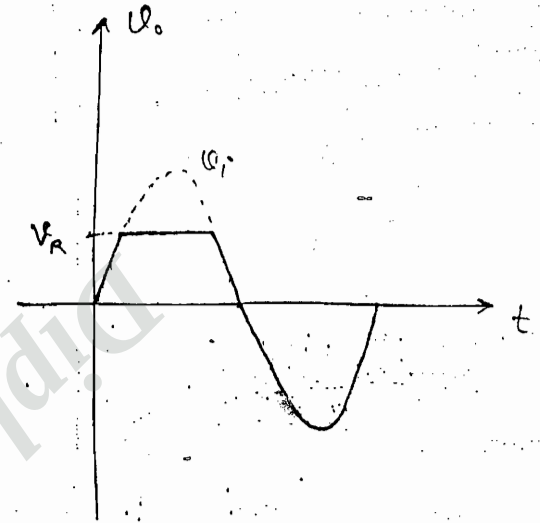
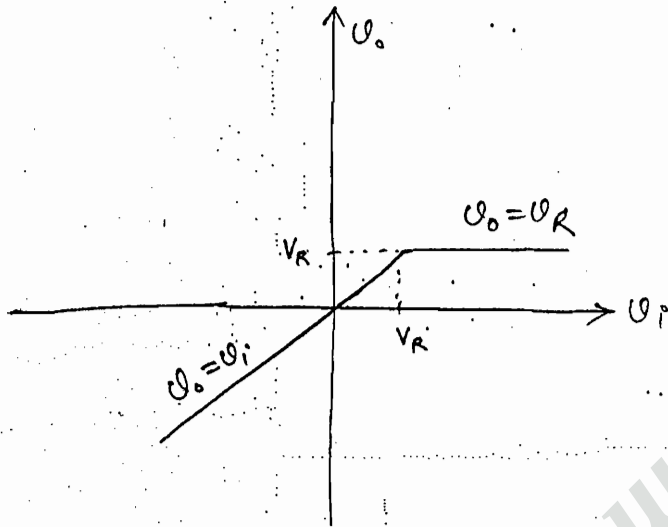


Model - 3 :

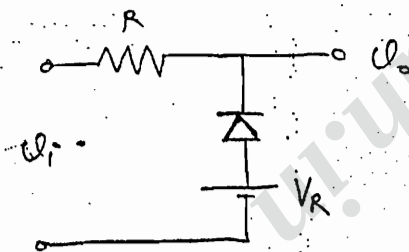


$$U_i < V_R \rightarrow D(\text{off}) \rightarrow U_o = U_i$$

$$U_i > V_R \rightarrow D(\text{on}) \rightarrow U_o = V_R$$

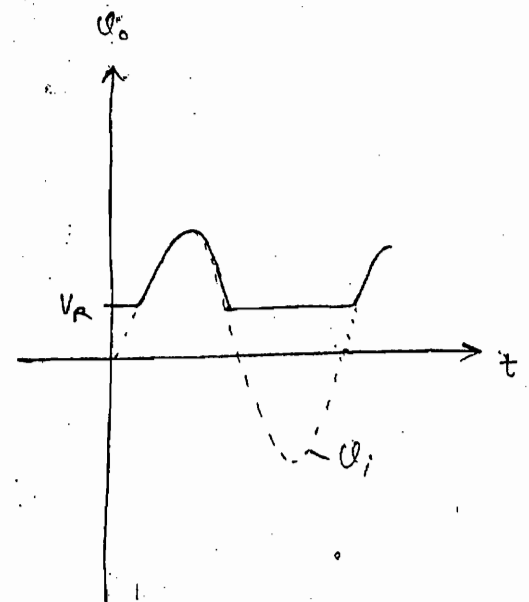
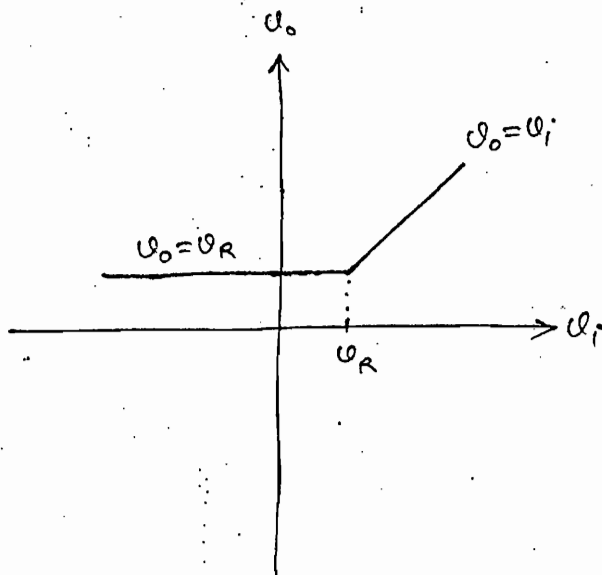


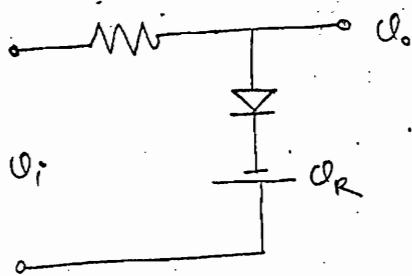
Model - 4 :



$$U_i < U_R \rightarrow D(\text{on}) \rightarrow U_o = U_R$$

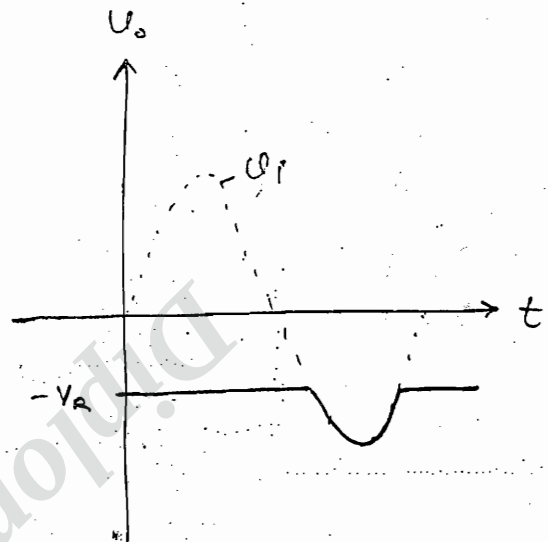
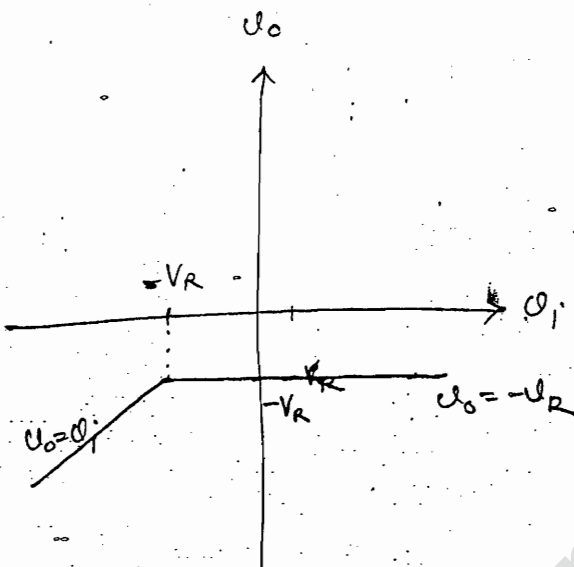
$$U_i > U_R \rightarrow D(\text{off}) \rightarrow U_o = U_i$$



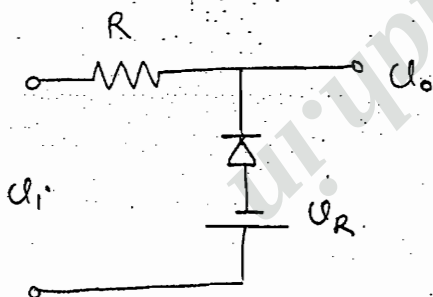


$$U_i < -U_R \rightarrow D(\text{off}) \rightarrow U_o = U_i$$

$$U_i > -U_R \rightarrow D(\text{on}) \rightarrow U_o = -U_R$$

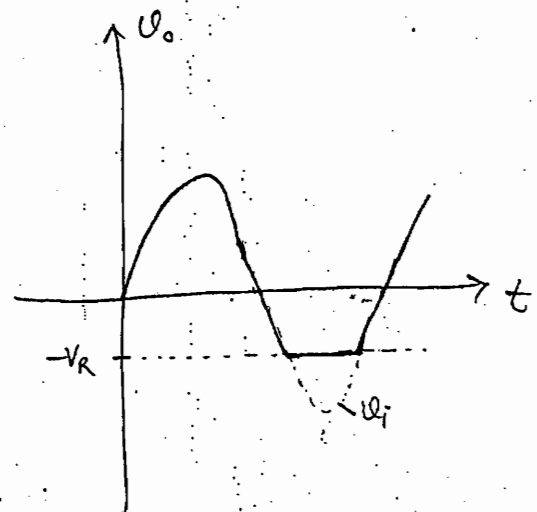
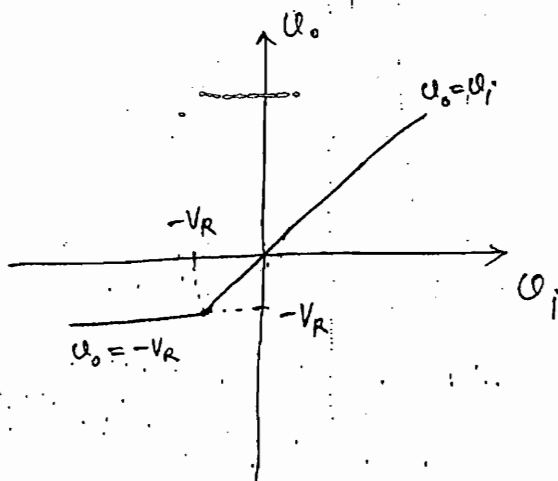


Model - 6 :



$$U_i < -U_R \rightarrow D(\text{on}) \rightarrow U_o = -U_R$$

$$U_i > -V_R \rightarrow D(\text{off}) \rightarrow U_o = U_i$$

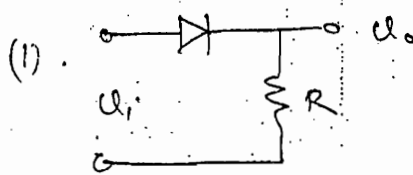


Conclusion:

- (1): When the diode is in downward disn, The s/g will be transmitted below the ref. voltage
- (2): When the diode is in upward disn, the s/g will be transmitted above the ref. voltage

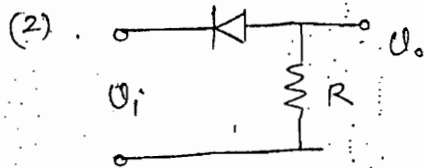
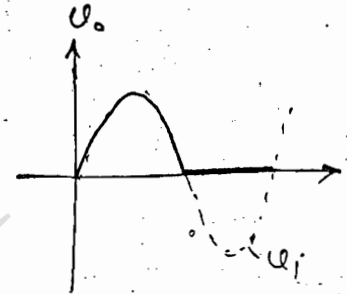
Series Clippers:

Models:



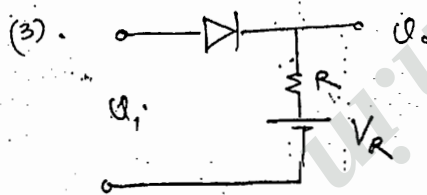
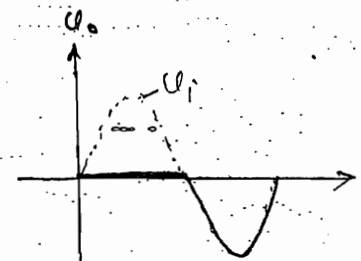
$$U_i < 0 \rightarrow D(\text{off}) \Rightarrow U_o = 0$$

$$U_i > 0 \rightarrow D(\text{on}) \Rightarrow U_o = U_i$$



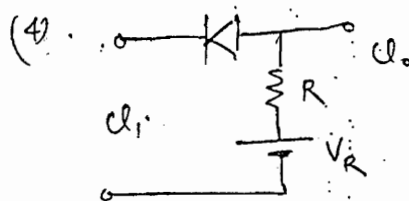
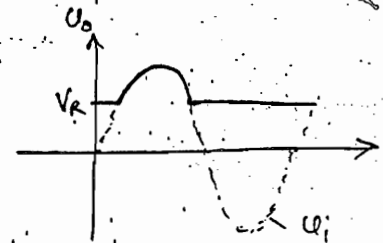
$$U_i < 0 \rightarrow D(\text{on}) \Rightarrow U_o = U_i$$

$$U_i > 0 \rightarrow D(\text{off}) \Rightarrow U_o = 0$$



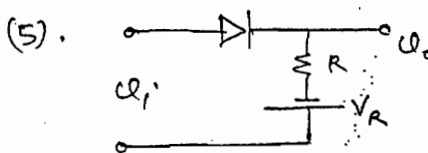
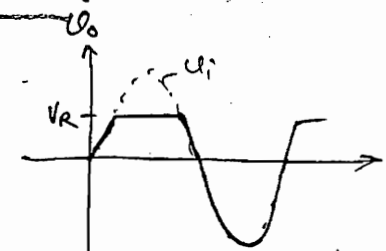
$$U_i < U_R \rightarrow D(\text{off}) \Rightarrow U_o = U_R$$

$$U_i > U_R \rightarrow D(\text{on}) \Rightarrow U_o = U_i$$



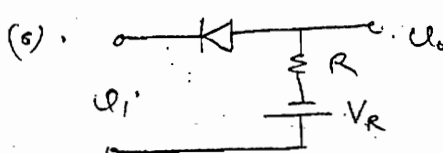
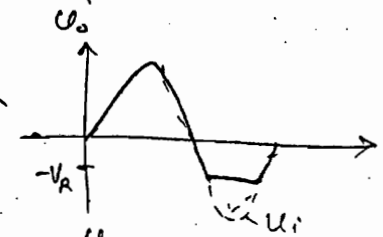
$$U_i < U_R \rightarrow D(\text{on}) \Rightarrow U_o = U_i$$

$$U_i > U_R \rightarrow D(\text{off}) \Rightarrow U_o = U_R$$



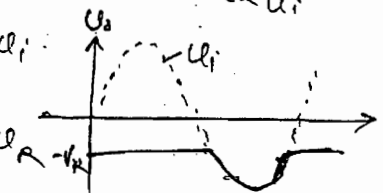
$$U_i < -U_R \rightarrow D(\text{off}) \Rightarrow U_o = -V_R$$

$$U_i > -U_R \rightarrow D(\text{on}) \Rightarrow U_o = U_i$$



$$U_i < -U_R \rightarrow D(\text{on}) \Rightarrow U_o = U_i$$

$$U_i > -U_R \rightarrow D(\text{off}) \Rightarrow U_o = -U_R - V_R$$

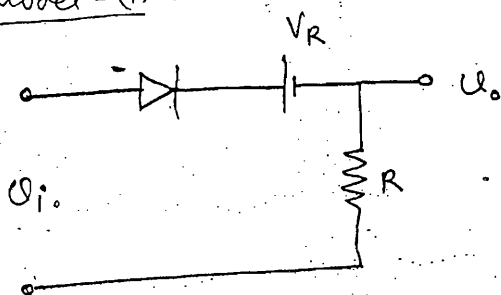


Conclusions:

- (1). When the diode is in forward bias to the i/p sig, the sig will be transmitted above the ref. voltage.
- (2). When the diode is in reverse bias to the i/p sig, the sig will be transmitted below the ref. voltage.

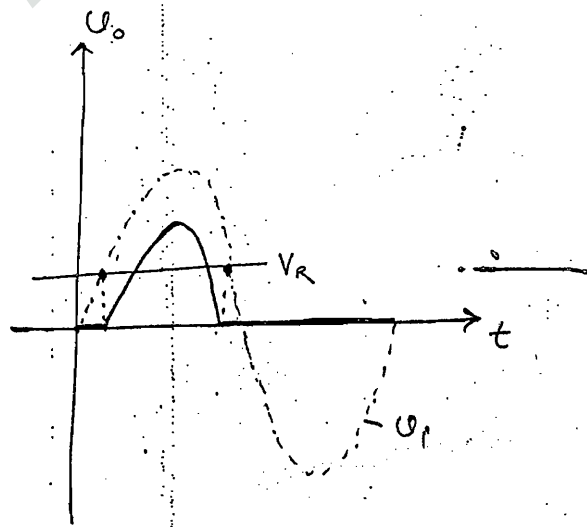
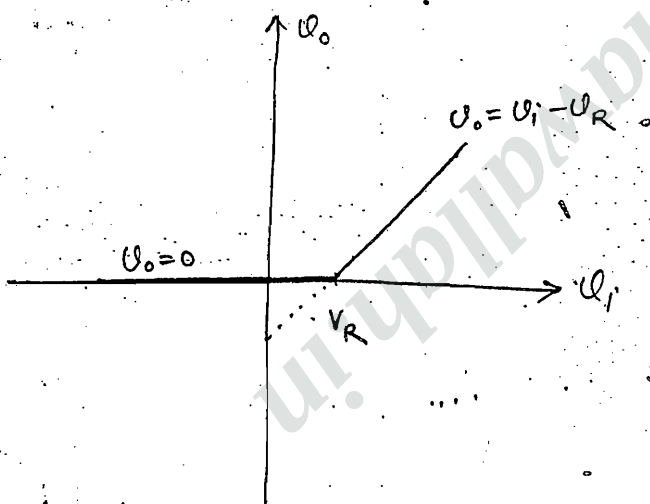
o/p following i/p designs :

Model - (1):

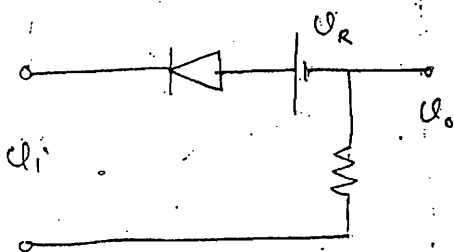


$$U_i < V_R \rightarrow D(\text{off}) \Rightarrow U_o = 0$$

$$U_i > V_R \rightarrow D(\text{on}) \Rightarrow U_o = U_i - V_R$$

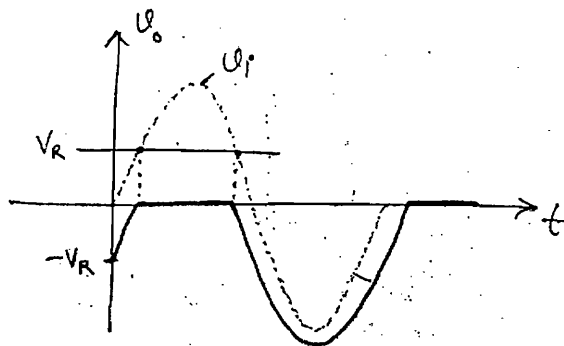
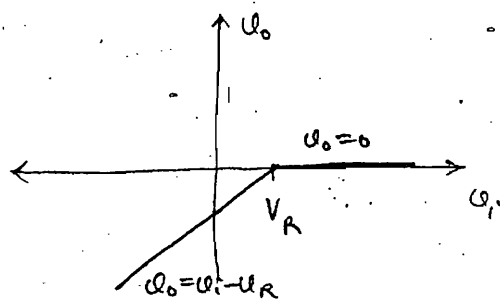


Model - (2):

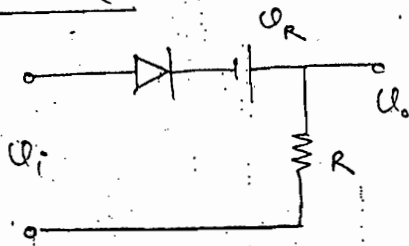


$$U_i < V_R \rightarrow D(\text{on}) \Rightarrow U_o = U_i - V_R$$

$$U_i > V_R \rightarrow D(\text{off}) \Rightarrow U_o = 0$$

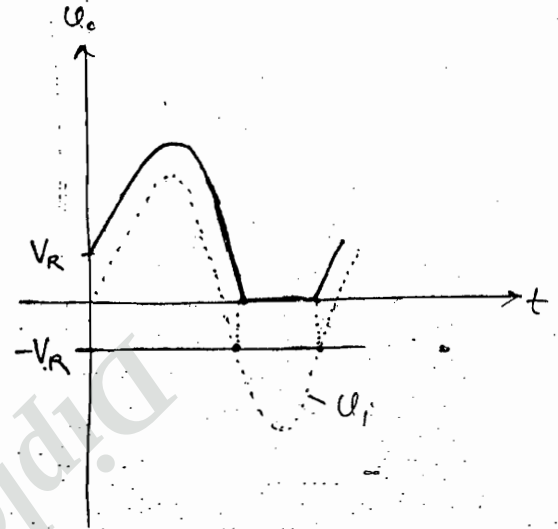
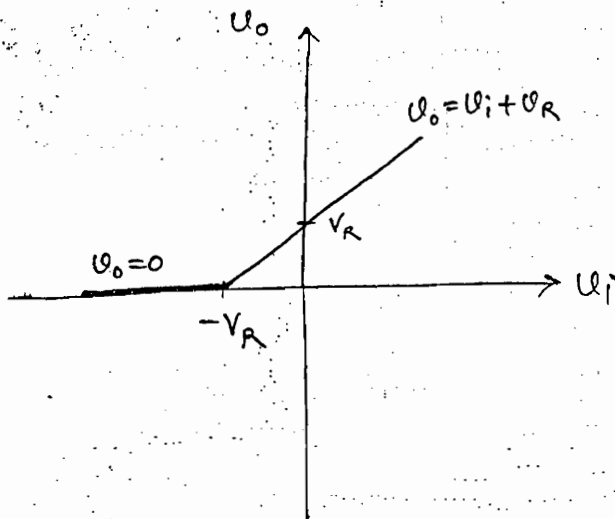


Model - (3):

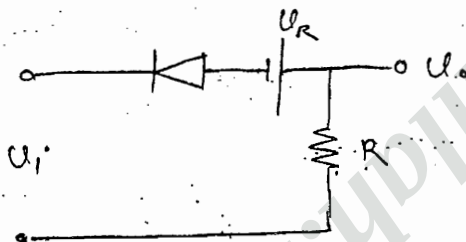


$$U_i < -U_R \rightarrow D(\text{off}) \Rightarrow U_o = 0$$

$$U_i > -U_R \rightarrow D(\text{on}) \Rightarrow U_o = U_i + U_R$$

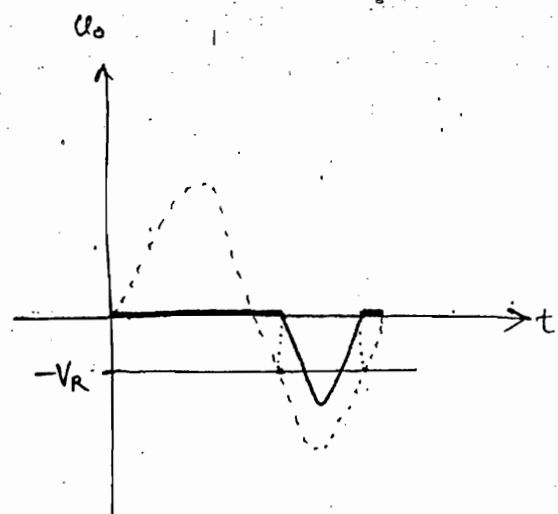
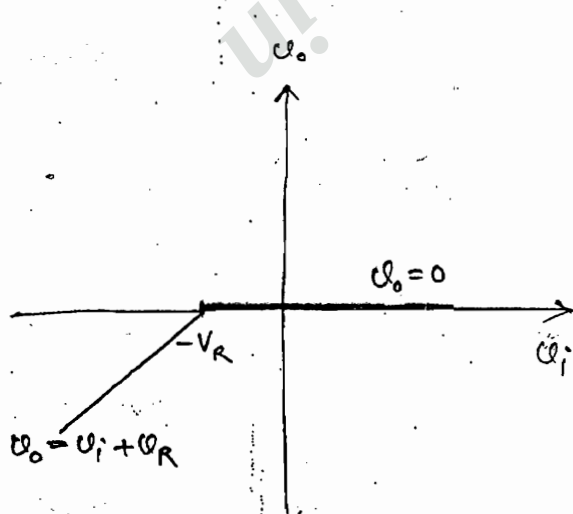


Model - (4):



$$U_i < -U_R \rightarrow D(\text{on}) \Rightarrow U_o = U_i + U_R$$

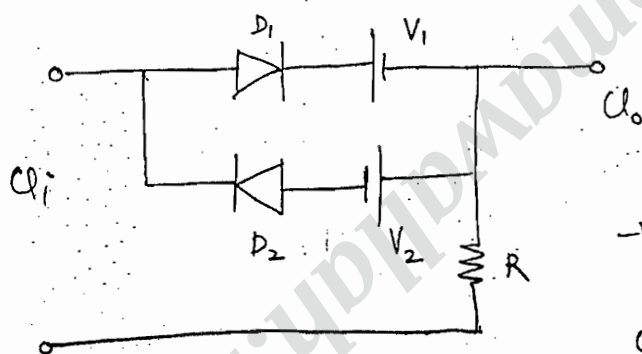
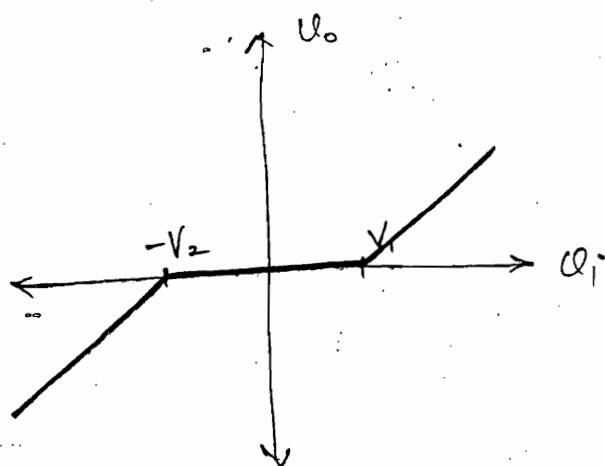
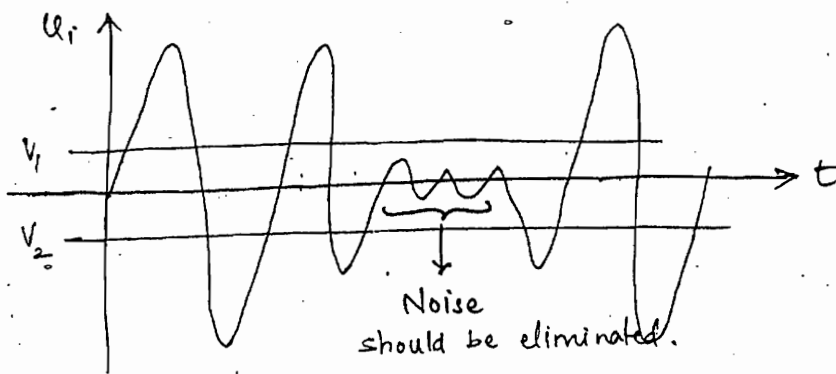
$$U_i > -U_R \rightarrow D(\text{off}) \Rightarrow U_o = 0$$



Applications: of %p following %p designs:

- Q. Design a noise clipper ckt. which has to eliminate the noise in b/w 2 threshold levels.

Sol.



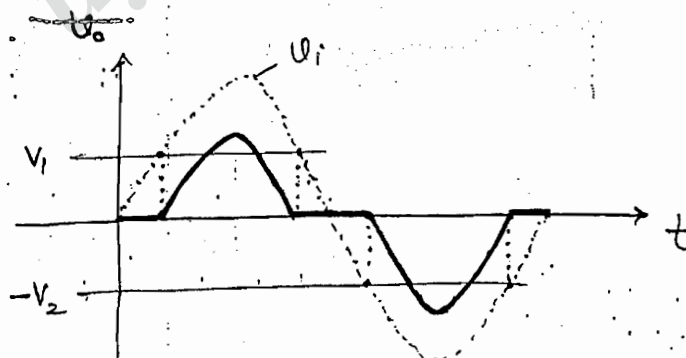
$$U_i < -V_2, D_1(\text{off}), D_2(\text{on})$$

$$U_o = U_i + V_2$$

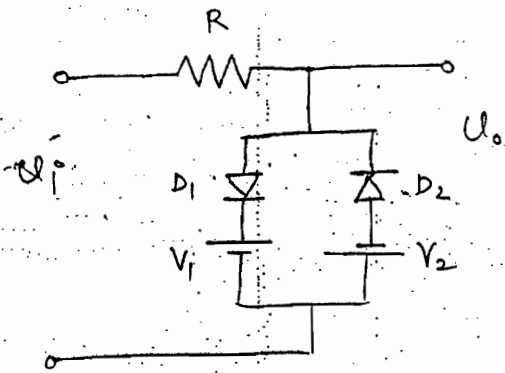
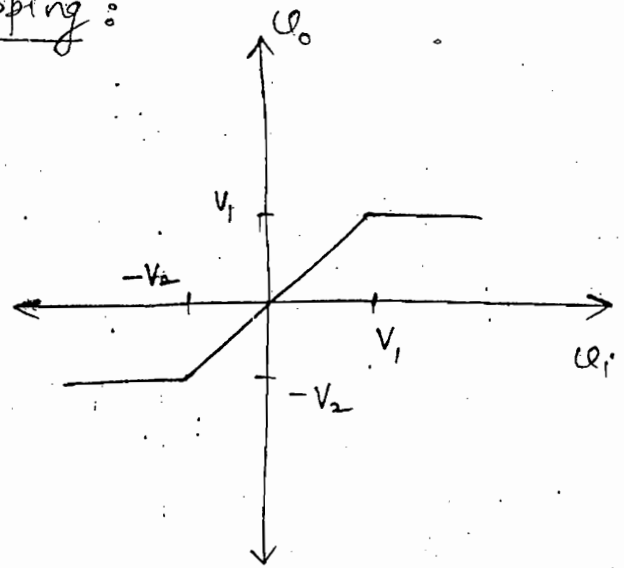
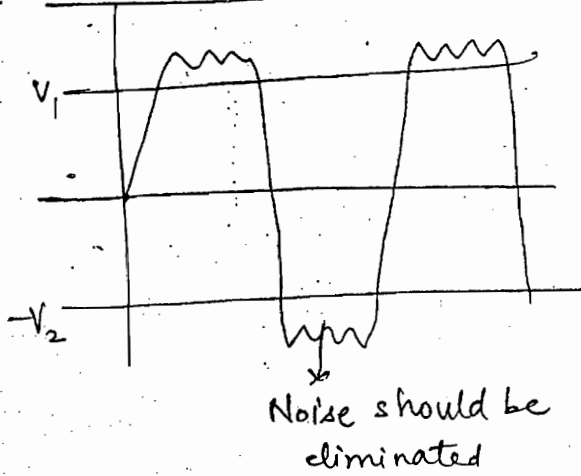
$$-V_2 < U_i < V_1, D_1(\text{off}), D_2(\text{off})$$

$$U_o = 0$$

$$U_i > V_1, D_1(\text{on}), D_2(\text{off}), U_o = U_i - V_1$$



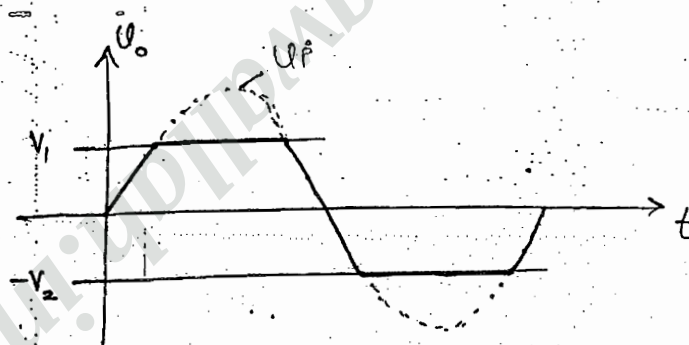
(4). Two Independent level Clipping:



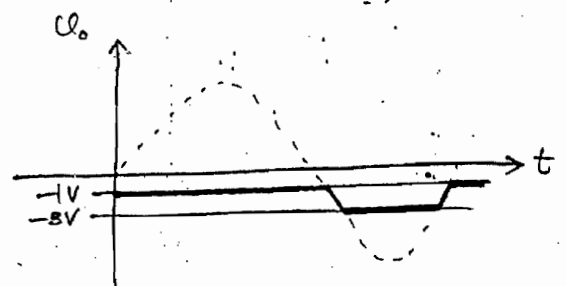
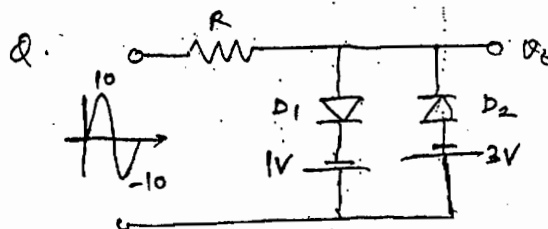
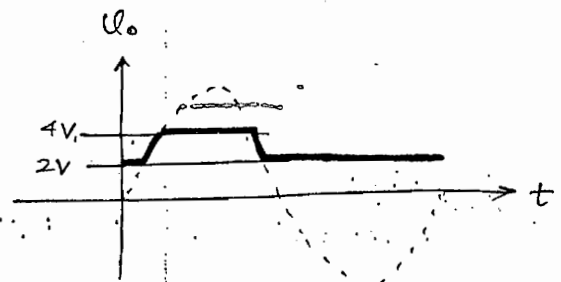
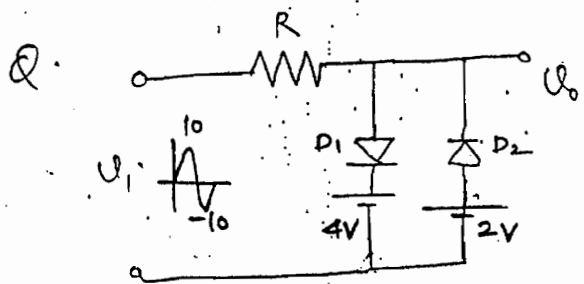
$$u_i < -V_2, D_1(\text{off}), D_2(\text{on}), u_o = -V_2$$

$$-V_2 < u_i < V_1, D_1(\text{off}), D_2(\text{off}), u_o = u_i$$

$$u_i > V_1, D_1(\text{on}), D_2(\text{off}), u_o = V_1$$

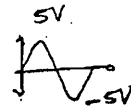
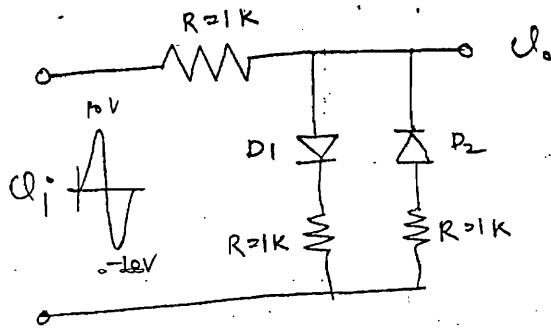


* $V_{D_1} > V_{D_2}$



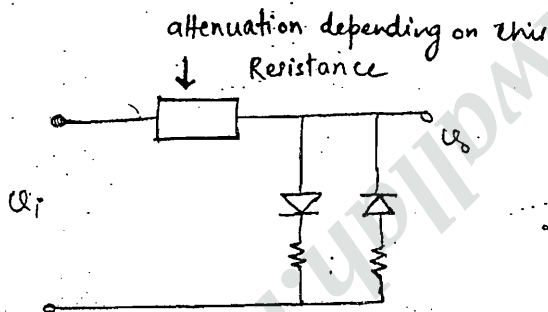
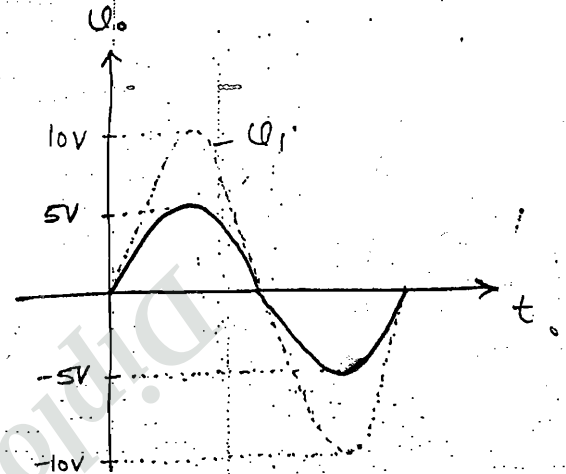
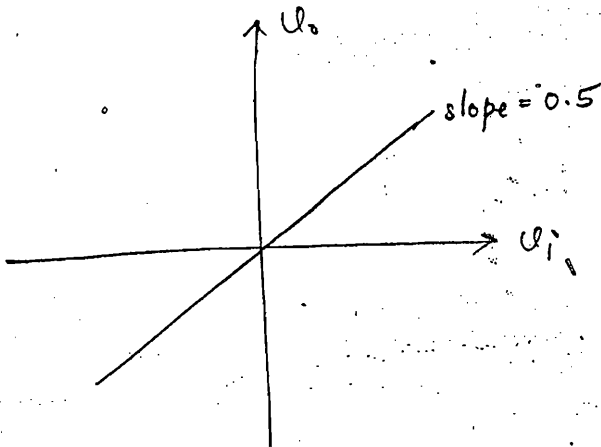
(5). Attenuator:

33



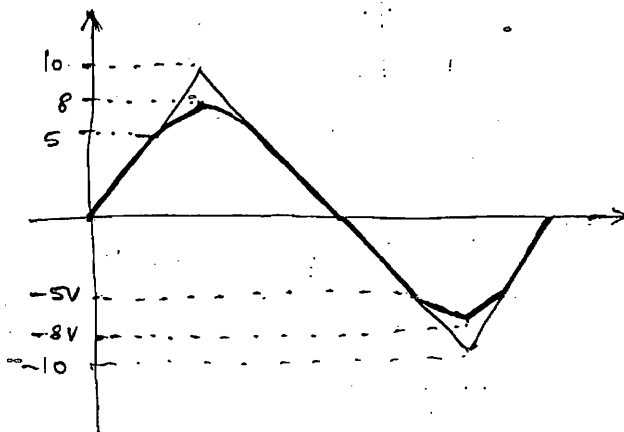
$$U_i < 0, D_1(\text{off}), D_2(\text{on}), U_o = \frac{U_i}{2}$$

$$U_i > 0, D_1(\text{on}), D_2(\text{off}), U_o = \frac{U_i}{2}$$

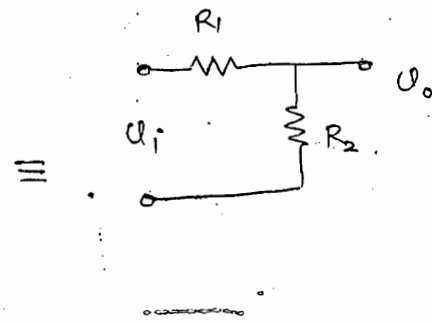
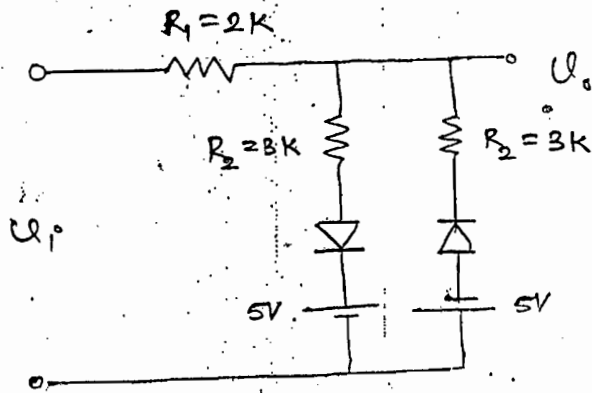


Q. Design an IC diode function generator to generate triangular wave into sine wave.

Sol.

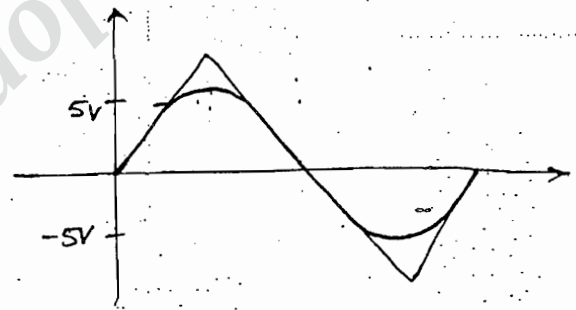
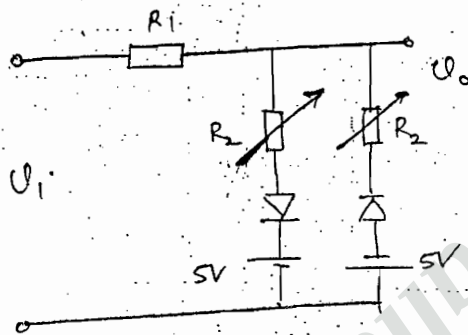


$$\begin{aligned} \frac{\Delta U_o}{\Delta U_i} &= \frac{8-5}{10-5} \\ &= \frac{3}{5} \\ &= \frac{3K}{2K+3K} \end{aligned}$$

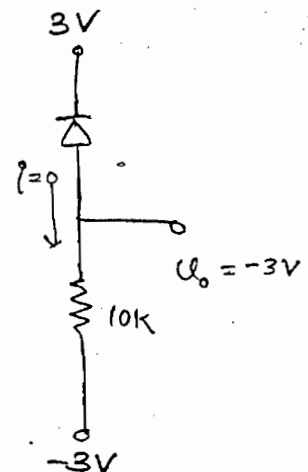
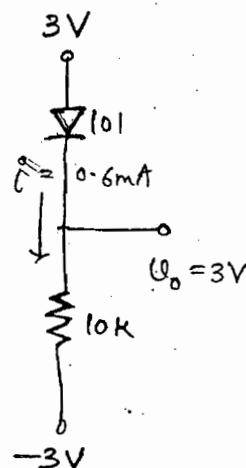
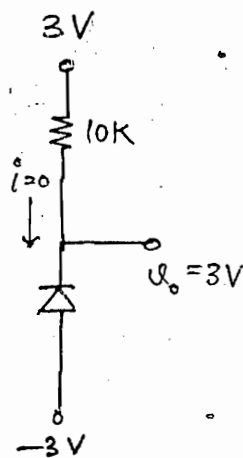
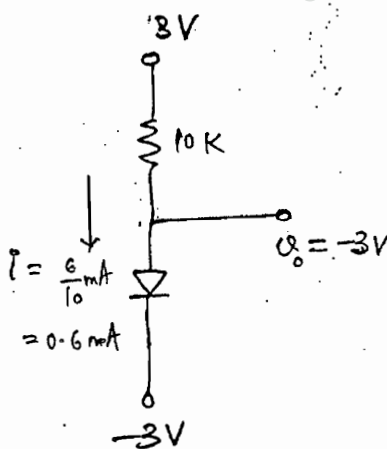


$$U_o = U_i \times \frac{R_2}{R_1 + R_2}$$

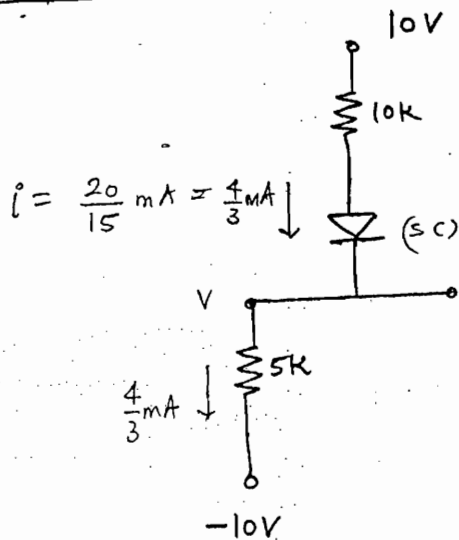
$$\frac{U_o}{U_i} = \frac{R_2}{R_1 + R_2} = \frac{\Delta U_o}{\Delta U_i} = \frac{3}{2+3} \Rightarrow R_1 = 2K, R_2 = 3K$$



Single Branch designs :

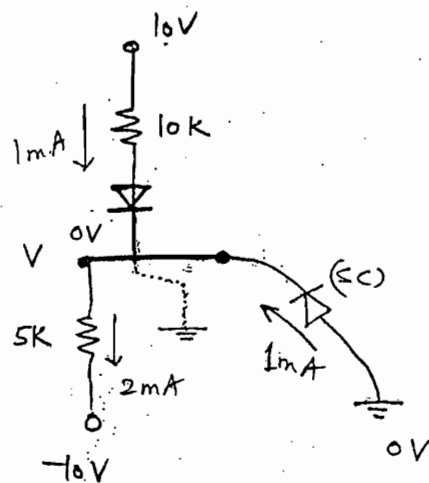


* In single branches, current cannot be changed, only voltage can be changed by different configurations.



$$i = \frac{20}{15} \text{ mA} = \frac{4}{3} \text{ mA}$$

$$V = 10 - \frac{10 \times 20}{15} = -3.3 \text{ V}$$

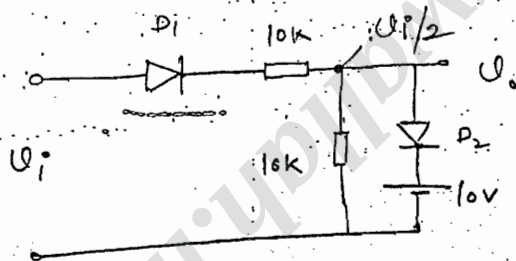


$$V = 0 \text{ V (due to ground)}$$

* In multiple branches, currents are different and can be changed by changing configurations.

CH-3

Q1.

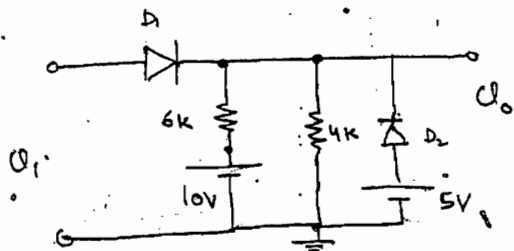


$$V_i < 0 \rightarrow D_1 (\text{off}), D_2 (\text{off}), V_o = 0 \checkmark$$

$$0 < V_i < 20 \rightarrow D_1 (\text{on}), D_2 (\text{off}), V_o = V_i/2$$

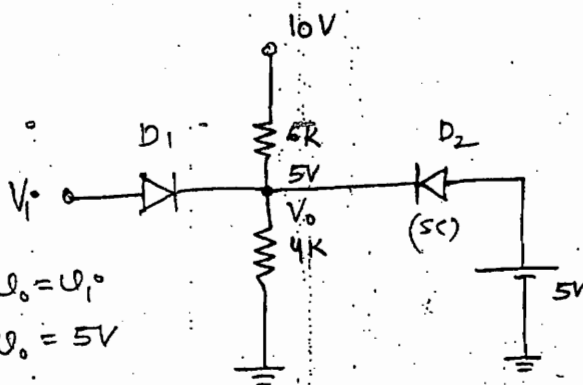
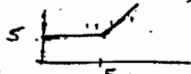
$$V_i > 20 \rightarrow D_1 (\text{off}), D_2 (\text{on}), V_o = 10 \text{ V}$$

Q7.

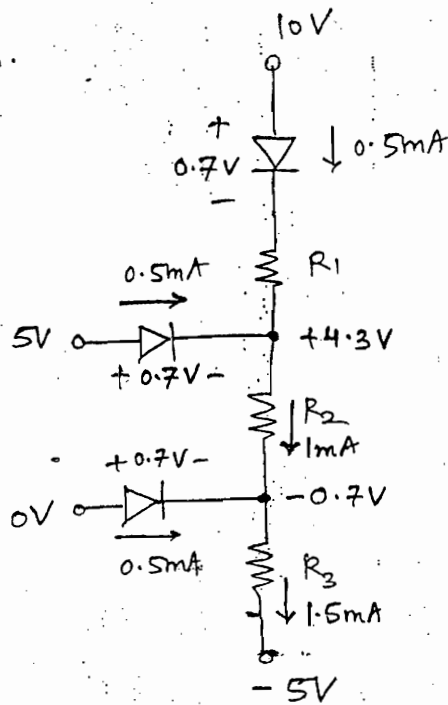


$$\text{If } V_i > 5 \Rightarrow D_1 (\text{on}), V_o = V_i$$

$$\text{If } V_i < 5 \Rightarrow D_1 (\text{off}), V_o = 5 \text{ V}$$



Q8.



$$\frac{10 - 0.7 - 4.3}{0.5 \text{ mA}} = R_1$$

$$\therefore R_1 = 10 \text{ K}\Omega$$

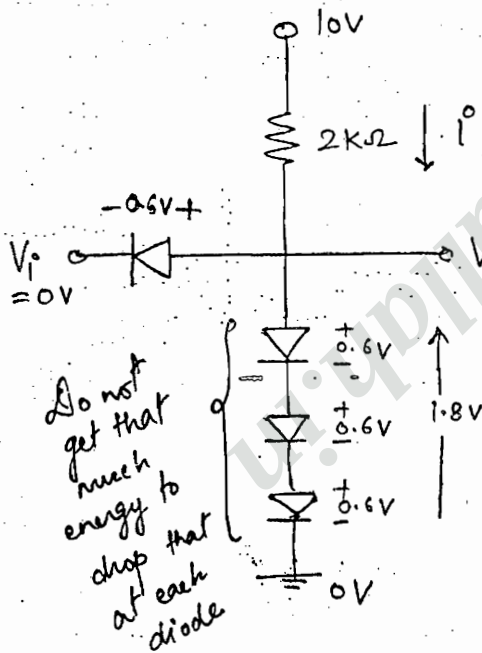
$$R_2 = \frac{4.3 - (-0.7)}{0.5 \text{ mA} + 0.5 \text{ mA}}$$

$$= \frac{5}{1 \text{ mA}}$$

$$= 5 \text{ K}\Omega$$

$$R_3 = \frac{-0.7 - (-5)}{1.5 \text{ mA}} = 2.86 \text{ K}\Omega$$

Q11.

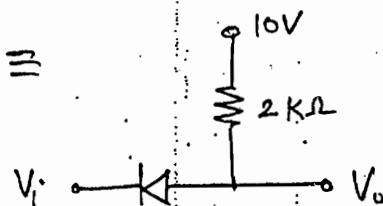


$$V_o = V_i$$

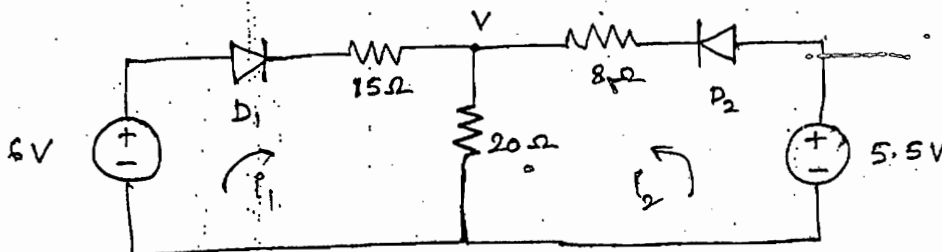
$$\text{i.e. } V_o = 0.6 \text{ V}$$

$$i = \frac{10 - 0.6}{2 \text{ K}}$$

$$= 4.7 \text{ mA}$$



Q12.



$$\frac{V - 6 + 0.6}{15} + \frac{V}{20} + \frac{V - 5.5 + 0.6}{8} = 0$$

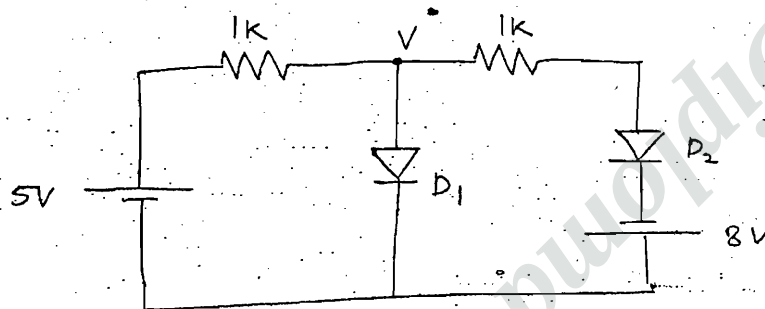
$$V \left(\frac{1}{15} + \frac{1}{20} + \frac{1}{8} \right) = \frac{6}{15} + \frac{5.5}{8} - \frac{0.6}{15} + \frac{0.6}{8}$$

$$V = \frac{6 - 0.6 - 5.5}{15} = 4.024 \text{ V}$$

$$p_1 = \frac{6 - 0.6 - 4.024}{15} = +ve \quad D_1 \text{ (ON)}$$

$$p_2 = \frac{5.5 - 0.6 - 4.024}{8} = +ve \quad D_2 \text{ (ON)}$$

Q13.



$$\frac{V - 5}{1} + \frac{V + 8}{1} = 0$$

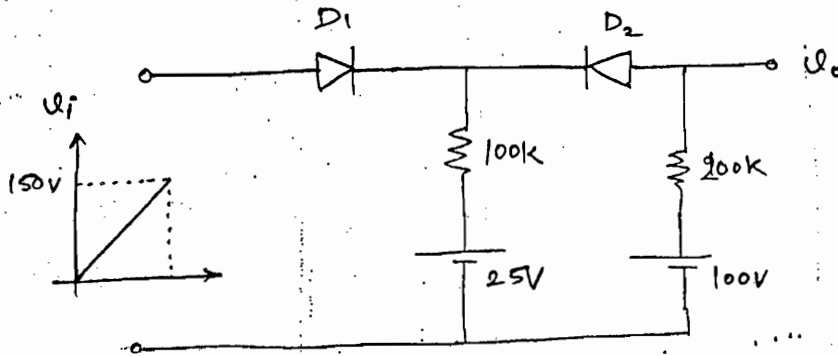
$$2V = -3 \Rightarrow V = -1.5 \text{ volts} \Rightarrow D_1 \text{ (OFF)}$$

Q14.

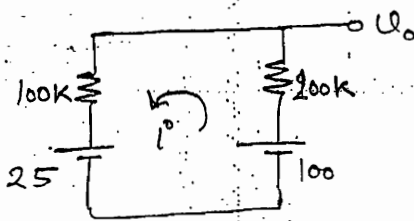
Special problems in clipper:

Ramp c/p problems:

Q.



$u_i = 0$: D_1 (off), D_2 (on)



$$I = \frac{100 - 25}{300} = \frac{75}{300} = 0.25 \text{ mA}$$

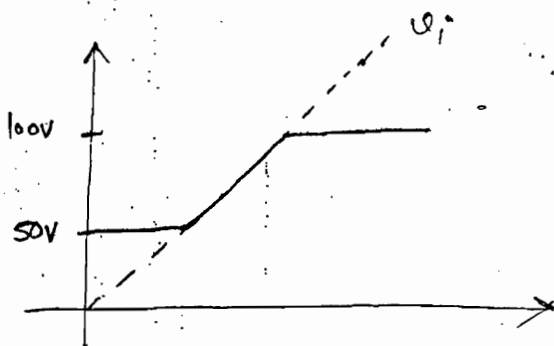
$$u_o = 100 - 200 \times 0.25 = 50 \text{ V}$$

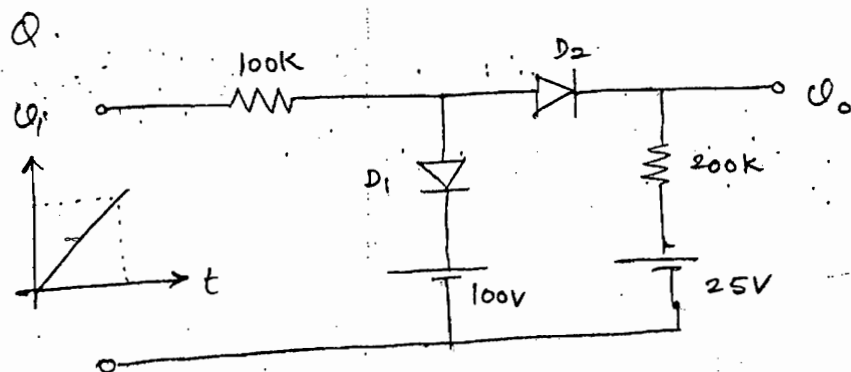
$\therefore 0 < u_i < 50 \text{ V} \Rightarrow D_1$ (off) $\Rightarrow D_2$ (on) $\Rightarrow u_o = 50 \text{ V}$

$u_i > 50 \text{ V}$: D_1 (on), D_2 (on) $\Rightarrow u_o = u_i$

$u_i < 100 \text{ V}$: D_1 (on), D_2 (on) $\Rightarrow u_o = u_i$

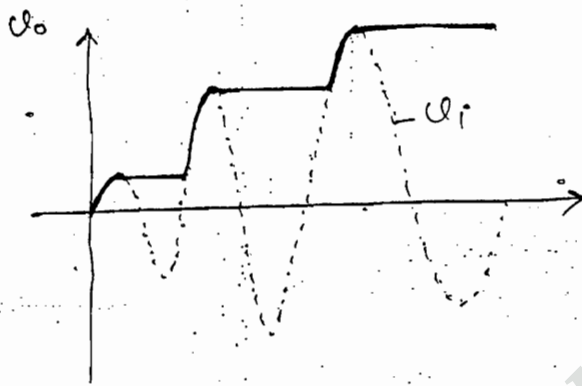
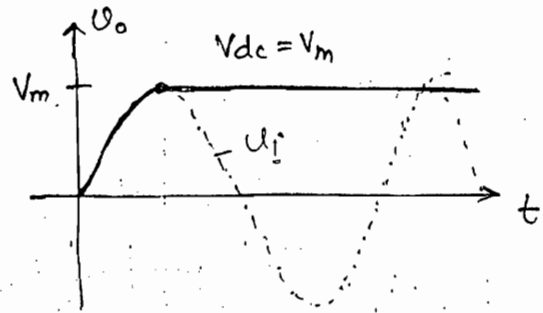
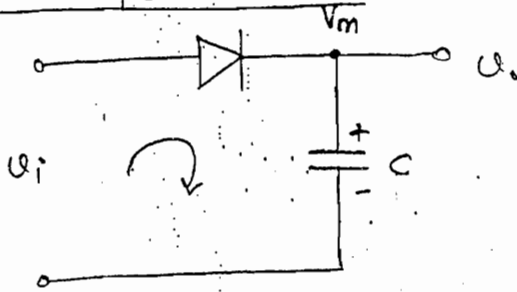
$u_i > 100 \text{ V}$: D_1 (on), D_2 (off) $\Rightarrow u_o = 100 \text{ V}$



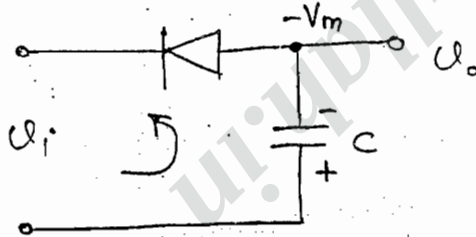


Peak detector, clamper, voltage Multiplier :

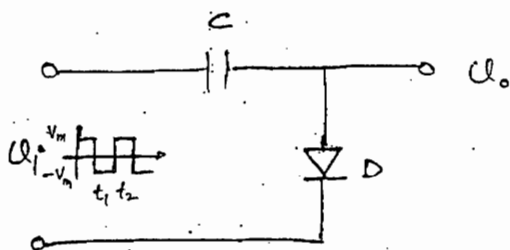
Positive peak detector :



Negative peak detector :



Negative Clamper :



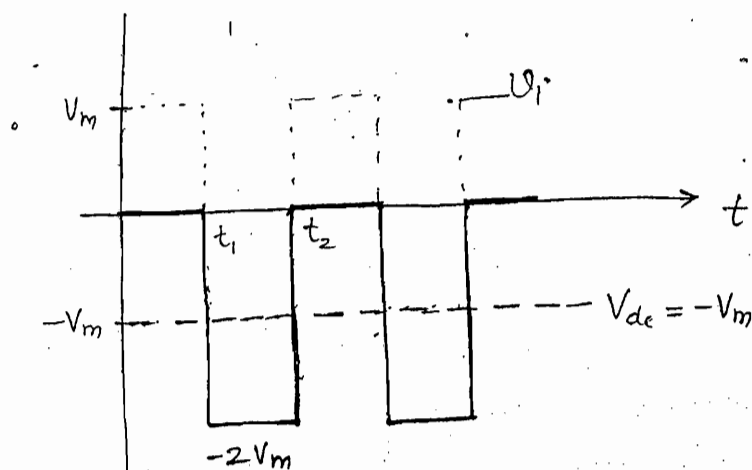
$0 \rightarrow t_1$:

$C \rightarrow$ get charged to V_m ; D (ON)

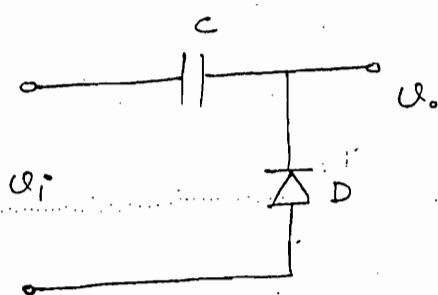
$u_o = 0$

$t_1 \rightarrow t_2$: D (OFF)

$u_o = -2V_m$



Positive Clamper:



$0 \rightarrow t_1$: D (off)

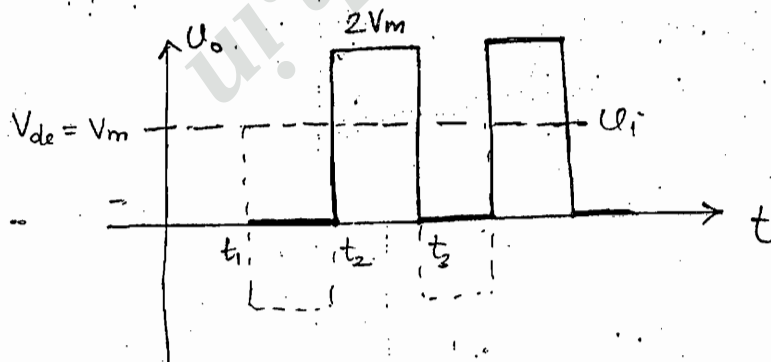
start with t_1

$C \rightarrow$ unchanged

$t_1 \rightarrow t_2$: D (on)

$C \rightarrow$ charges to V_m , $U_o = 0$

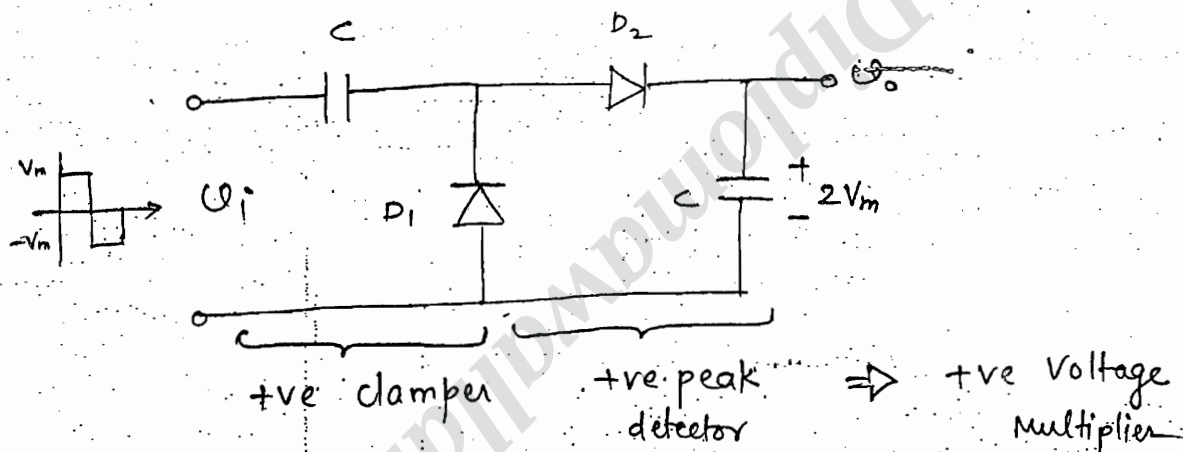
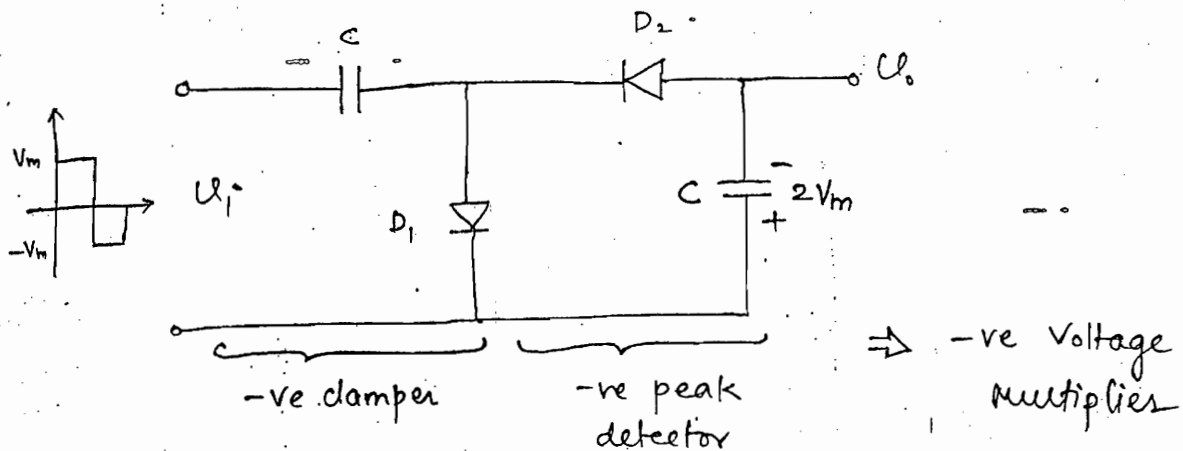
$t_2 \rightarrow t_3$: D (off), $U_o = 2V_m$



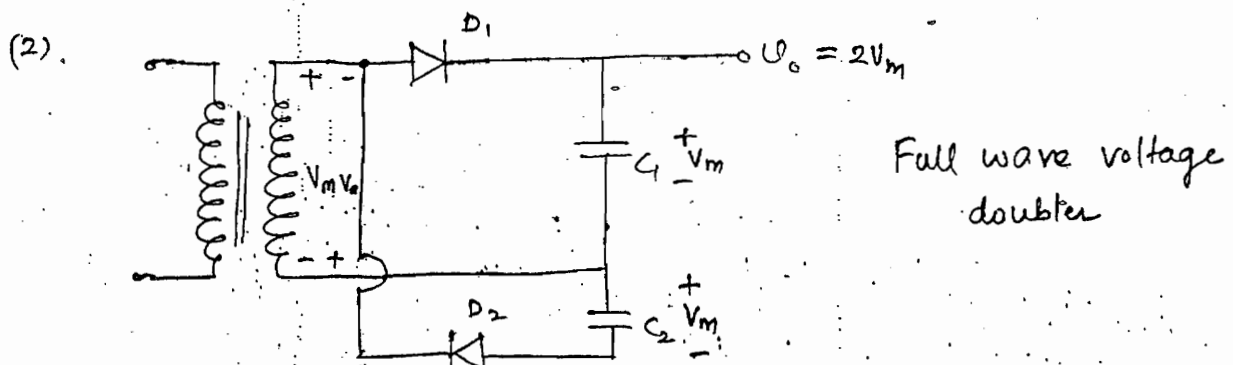
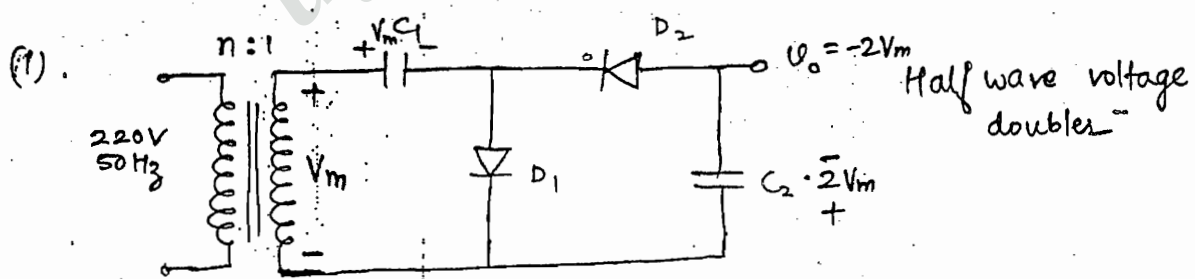
Techniques in clamping:

- (1). When the diode is in downward dirn, the total s/g will be clamped (pulling) below the ref. voltage.
- (2). When the diode is in $\uparrow$ dirn, the total s/g will be clamped above the ref. voltage.

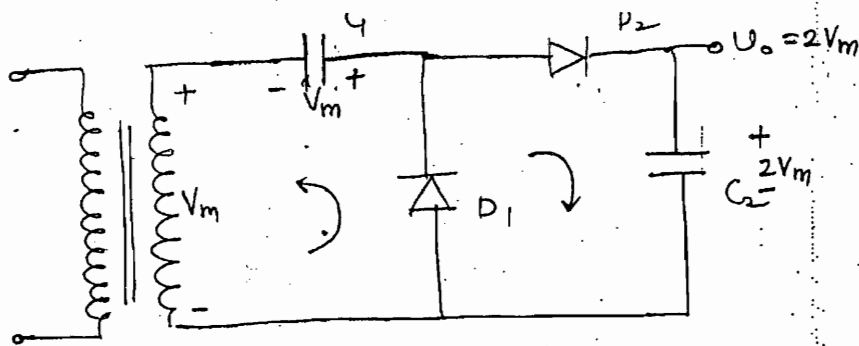
Voltage multiplier:



Voltage Multiplier problems:

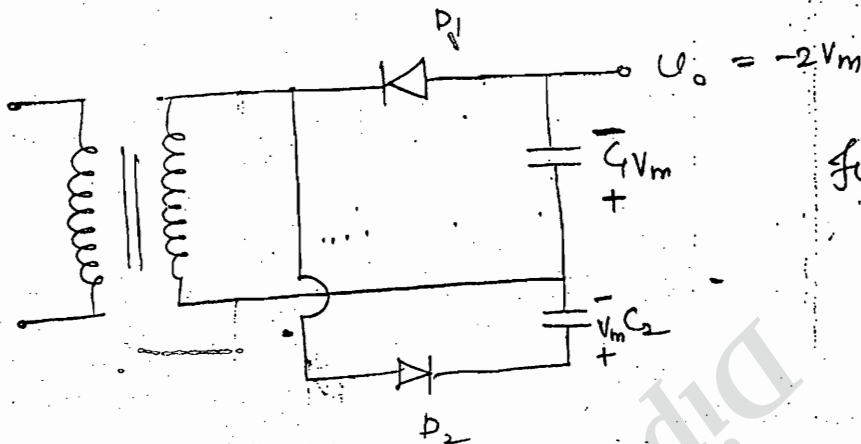


(3).



Half wave voltage doubler

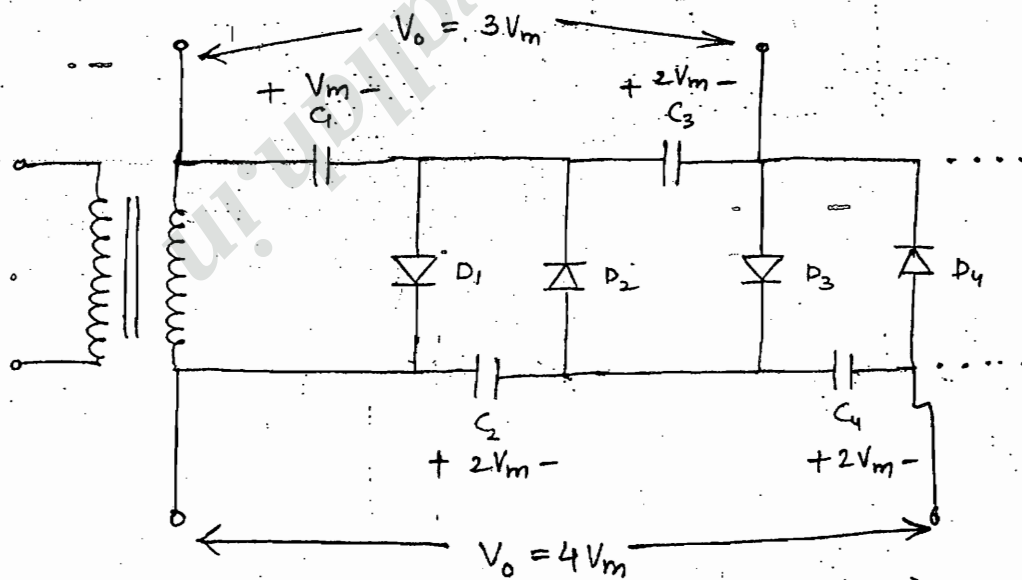
(4).



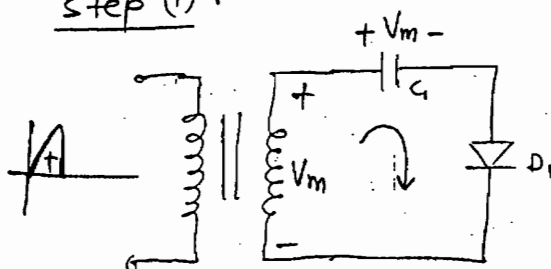
Full wave voltage doubler

Q. Design the voltage multiplier ckt which generates n multiples of DC.

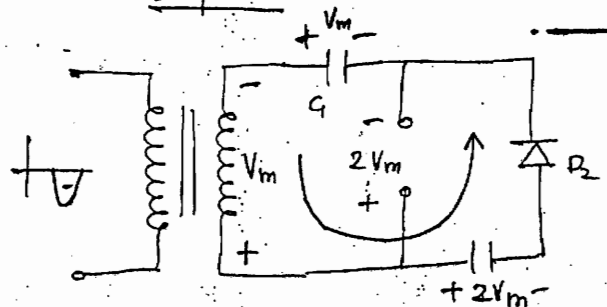
Sol.



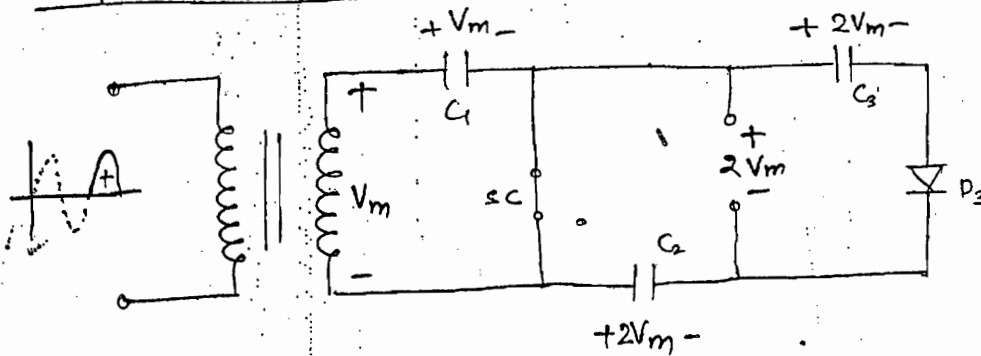
Step (1):



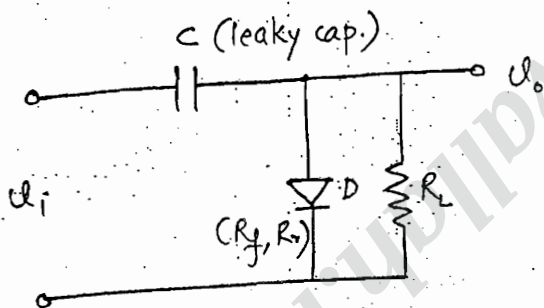
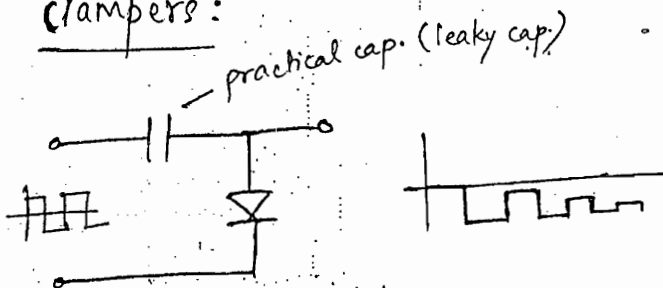
Step (2):



Step (3): 2nd cycle (+ve)

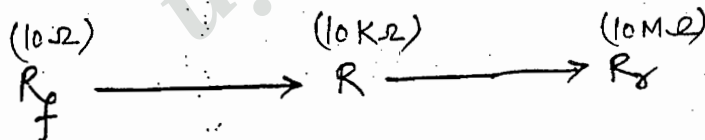


clampers:



R_f = forward resistance
= 10Ω

R_r = reverse resistance
= $10M\Omega$



Geometric mean gives the centre of (R_f, R_r) .

$$C = \sqrt{ab}$$

$$R = \sqrt{R_f \cdot R_r}$$

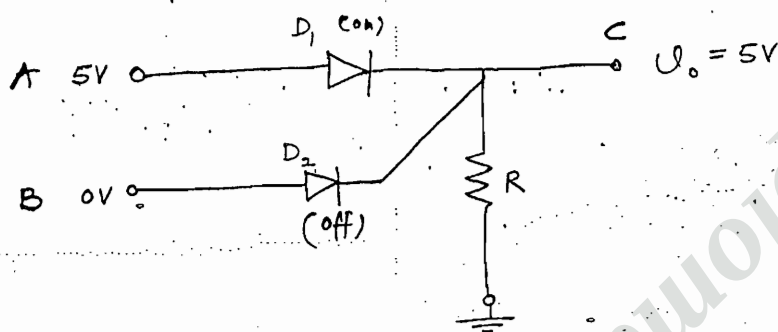
$$R = \sqrt{10 \times 10 \times 10^6} = 10^4 = 10K\Omega$$

Diode as a digital logic gate :

45

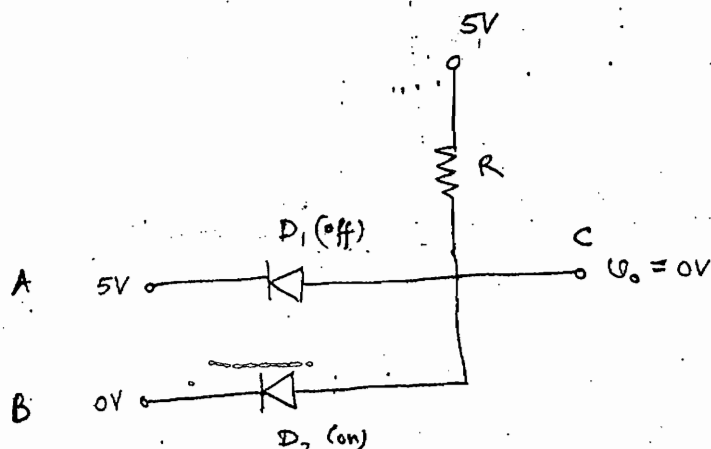
"OR" logic : $(A + B = C)$

A	B	C
0	0	0
0	1	1
1	0	1
1	1	1



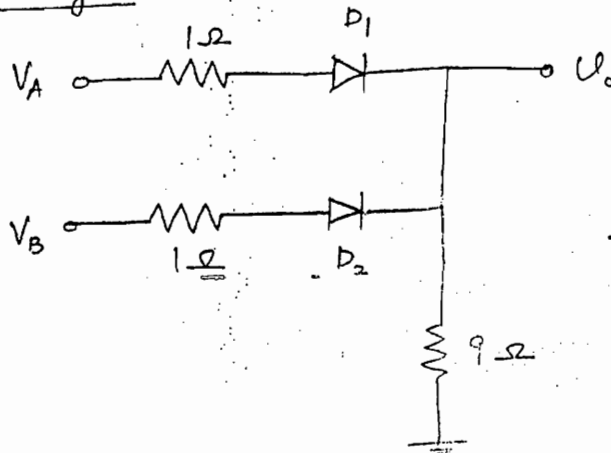
"AND" logic : $(A \cdot B = C)$

A	B	C
0	0	0
0	1	0
1	0	0
1	1	1

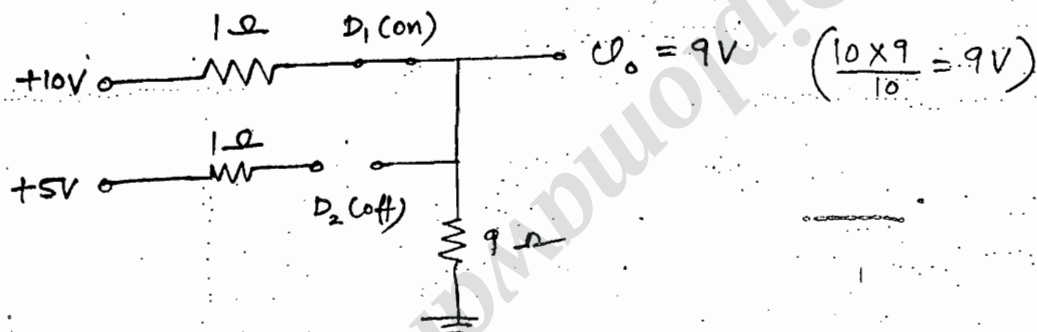


Problems:

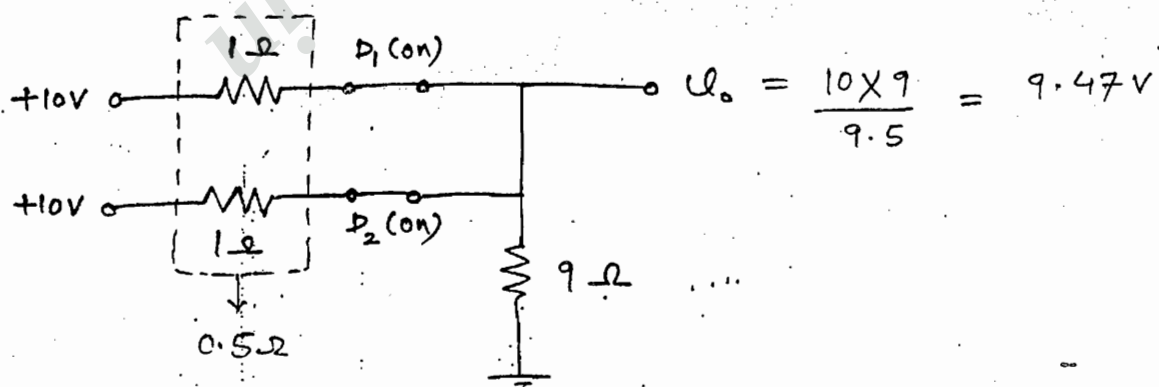
"OR" logic:



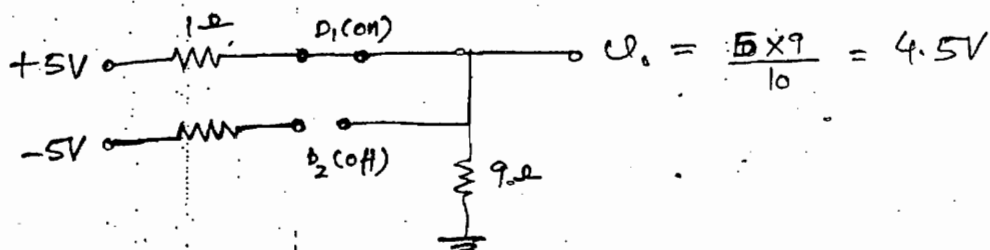
Case (1): $V_A = 10V$, $V_B = 5V$



Case (2): $V_A = 10V$, $V_B = 10V$

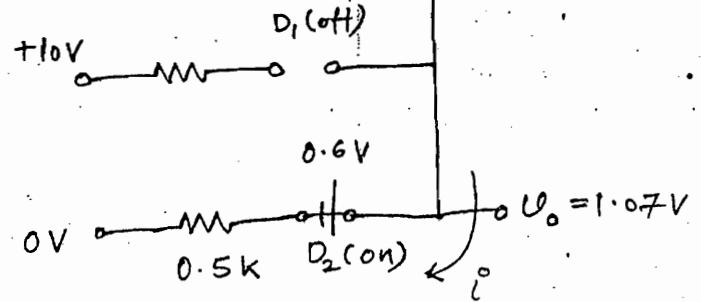
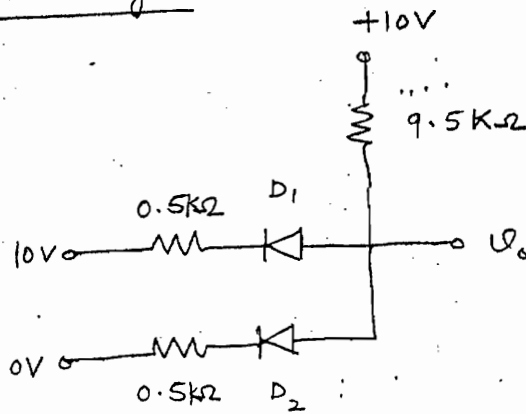


Case (3): $V_A = 5V$, $V_B = -5V$



"AND" logic - Case (1): $V_A = 10V$, $V_B = 0V$

47

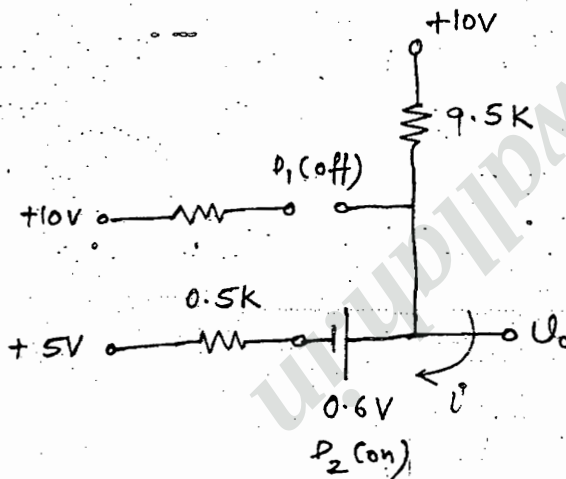


Assume $V_Y = 0.6V$.

$$i = \frac{10 - 0.6}{10K} = 0.94mA$$

$$V_O = 0.6 + 0.94mA \times 0.5K = 1.07V$$

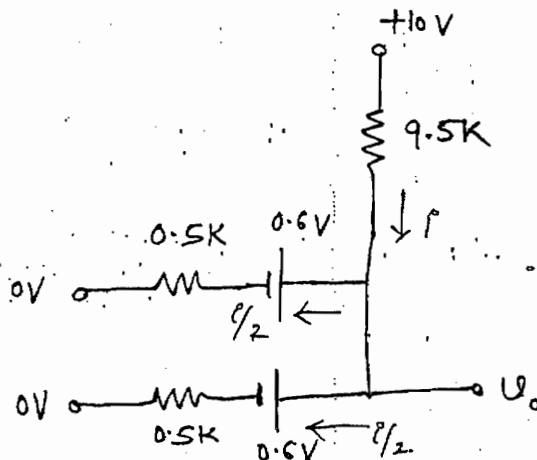
Case (2): $V_A = 10V$ and $V_B = 5V$



$$i = \frac{10 - 0.6 - 5}{10K} = 0.44mA$$

$$V_O = 0.6 + 0.44mA \times 0.5K + 5 = 5.82V$$

Case (3): $V_A = 0V$ and $V_B = 0V$

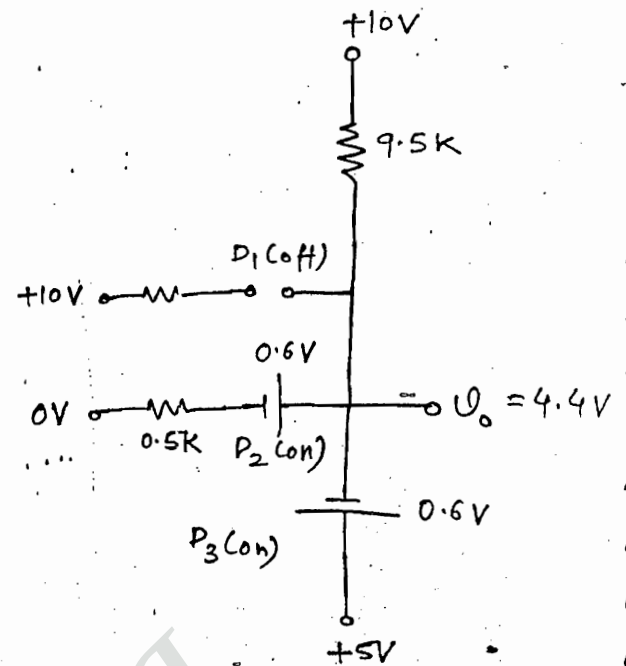
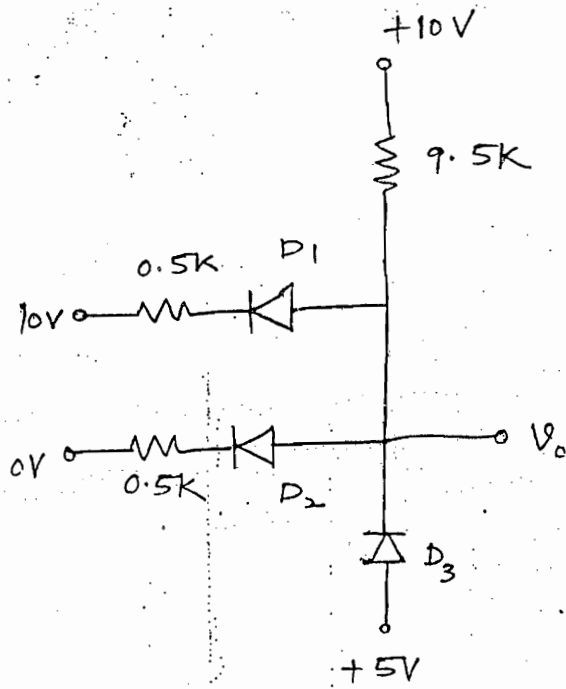


$$10 - 9.5i - 0.6 - 0.5i/2 = 0$$

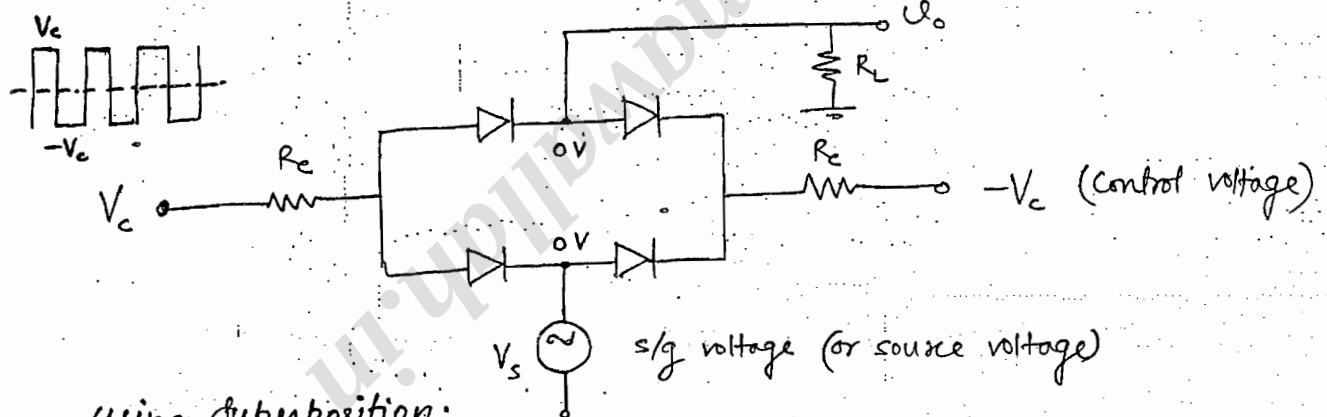
$$i = 0.96mA$$

$$V_O = 0.6 + 0.5i/2 = 0.842V$$

Q.

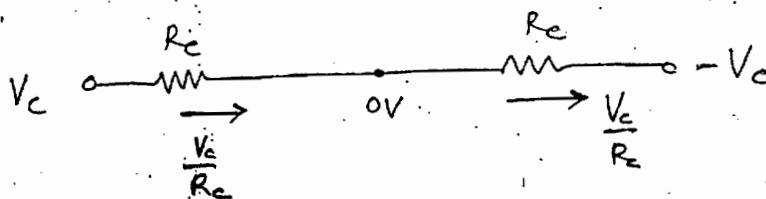
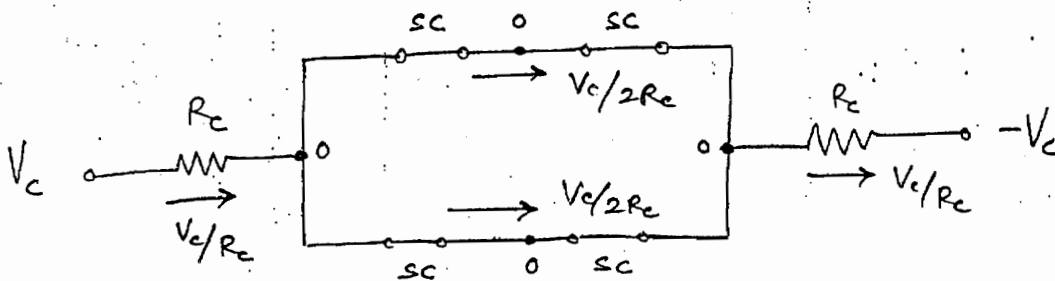


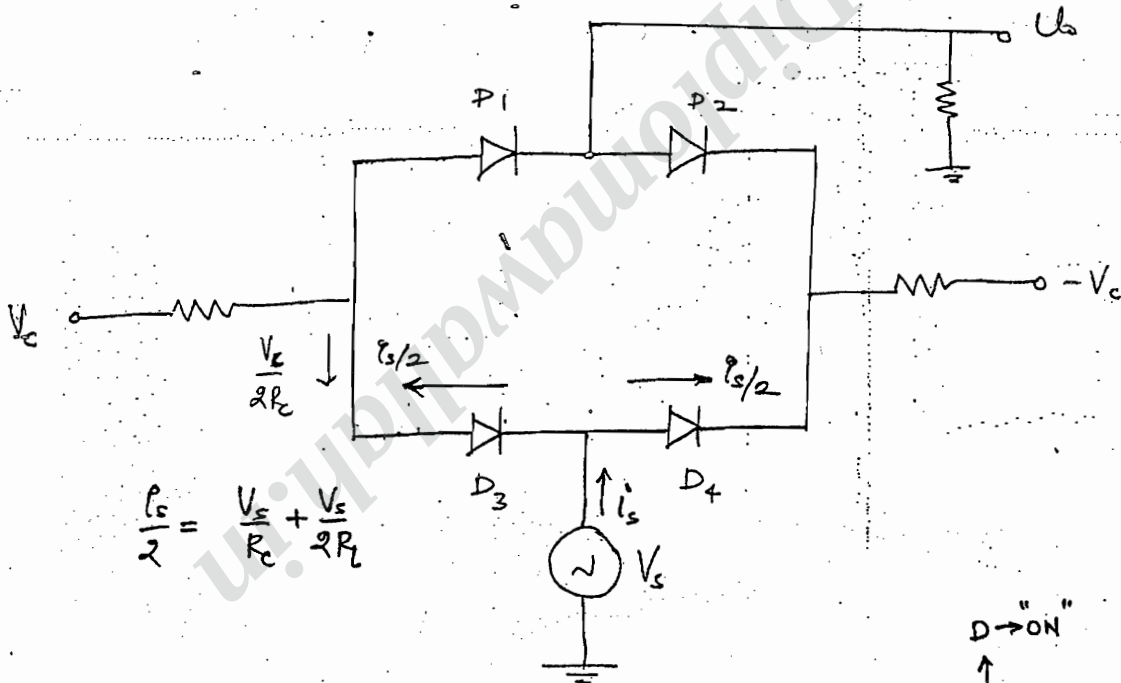
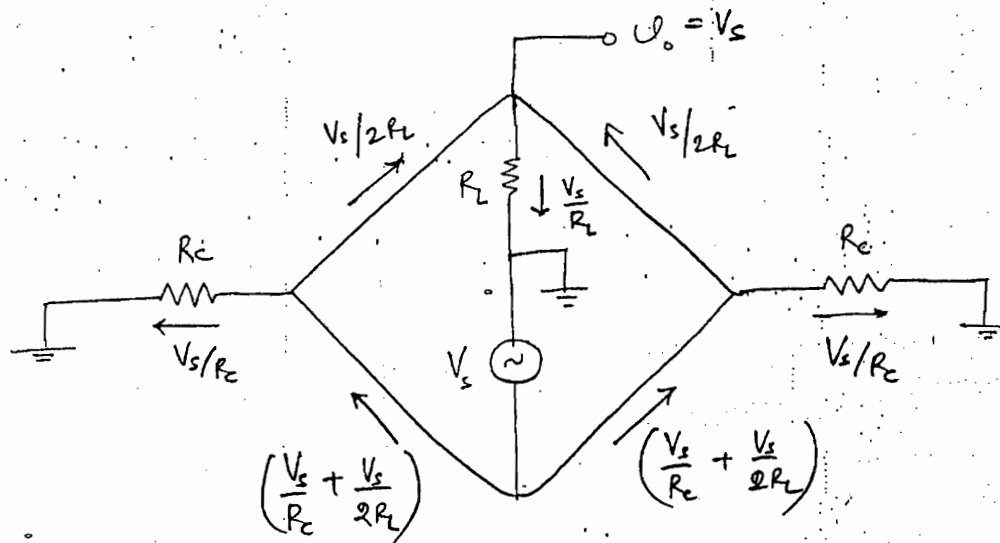
Diode as an analog gate:



using superposition:

DC Analysis:



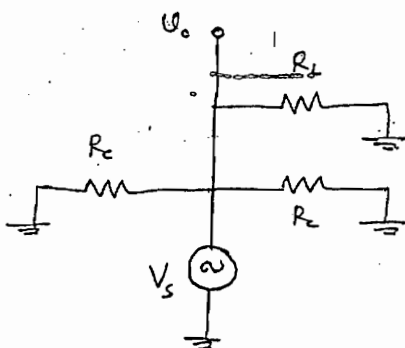


$$\frac{V_c}{2R_L} > \frac{I_s}{2}$$

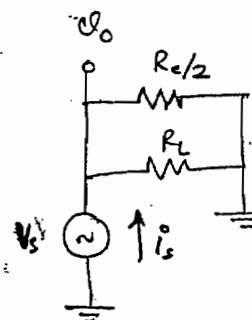
(Diode will be ON)

$D \rightarrow \text{"ON"}$

$$\therefore \frac{V_c}{2R_L} > \frac{V_s}{R_c} + \frac{V_s}{2R_L}$$



$\Rightarrow$

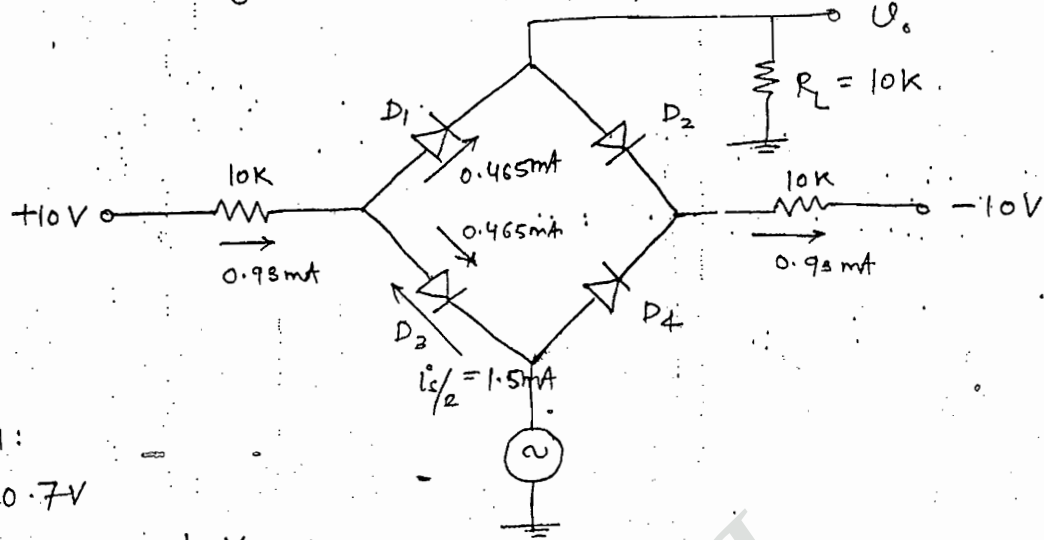


$$I_s = \frac{V_s}{\left(\frac{R_c/2 \cdot R_L}{R_c/2 + R_L} \right)}$$

$$V_o = V_s$$

Problem:

Draw the Transfer characteristic for the ckt given below:

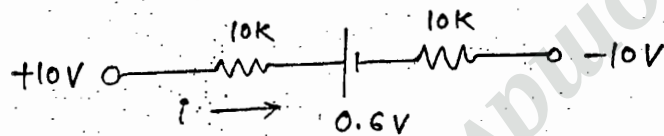


Given:

$$V_f = 0.7V$$

$$-10V \leq V_s \leq 10V$$

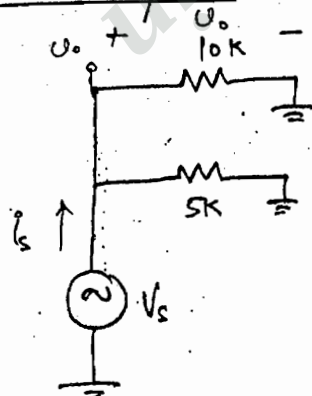
sol. D.C Analysis:



$$I = \frac{10 - 1.4 - (-10)}{20K} = 0.93 \text{ mA}$$

$$I/2 = 0.465 \text{ mA}$$

Ac Analysis:



$$I_s = \frac{V_s}{\left(\frac{10 \times 5}{15}\right)} = 0.3 V_s \text{ mA}$$

Assume : peak level that is. $V_s = 10V$

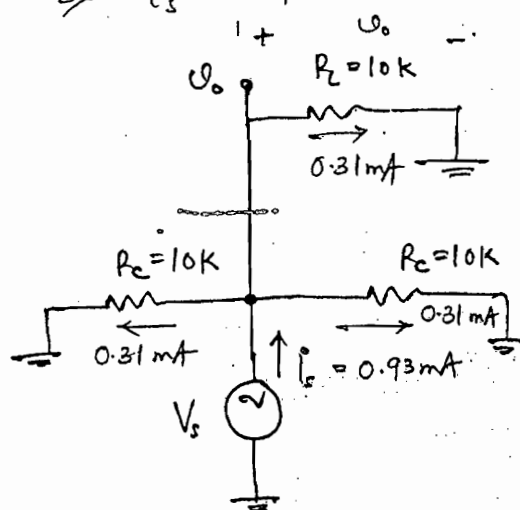
$$\therefore I_s = 0.3 (10) = 3 \text{ mA}$$

$$I_s/2 = 1.5 \text{ mA}$$

At this source current, D_3 (off). s/g can be passed only if $I_s/2$ is less than I_f i.e. 0.465 mA .

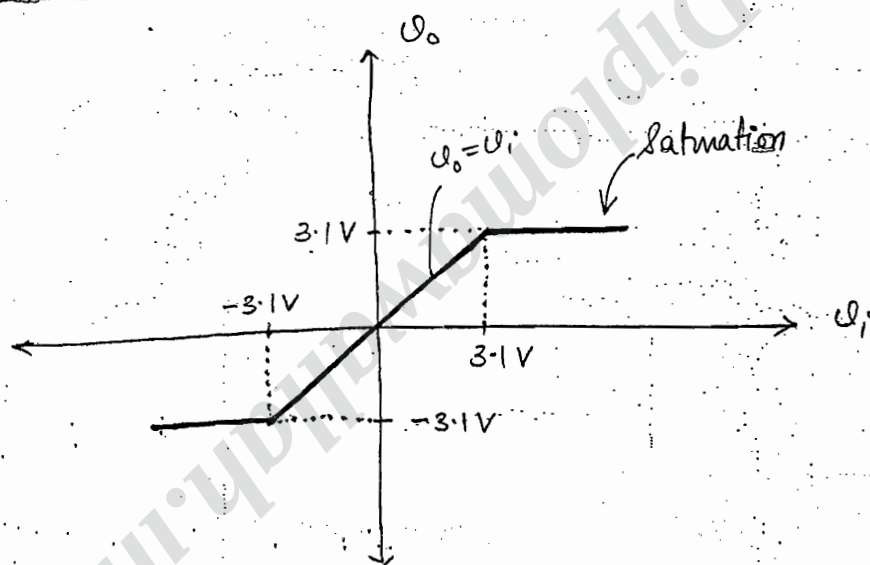
$$i_s/2 = 0.465 \text{ mA}$$

$$\Rightarrow i_s = 0.93 \text{ mA}$$



$$V_o = 0.31 \text{ mA} \times 10 \text{ K} \\ = 3.1 \text{ Volts.}$$

Transfer characteristics:

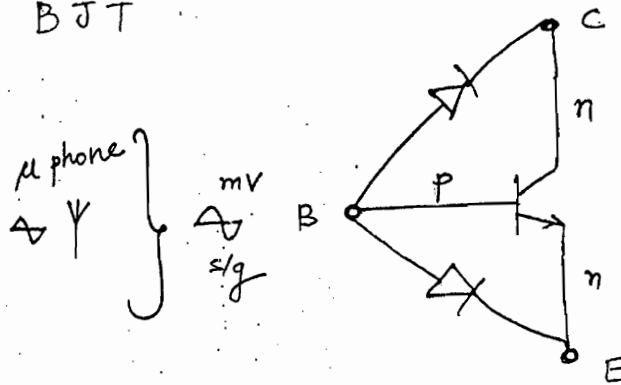


Conclusion:

In Analog gate design, The control voltage should be always greater than 3 times of v_{γ} voltage to make all the diodes ON.

Small s/g Analysis of a Diode:

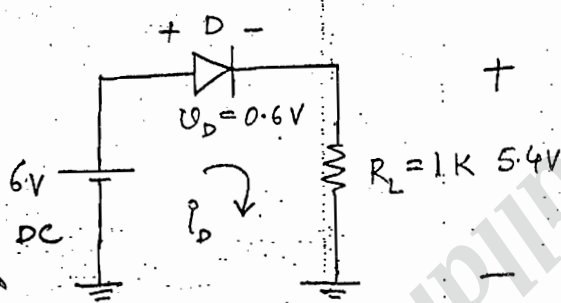
BJT



Diode Resistance

Static Resistance

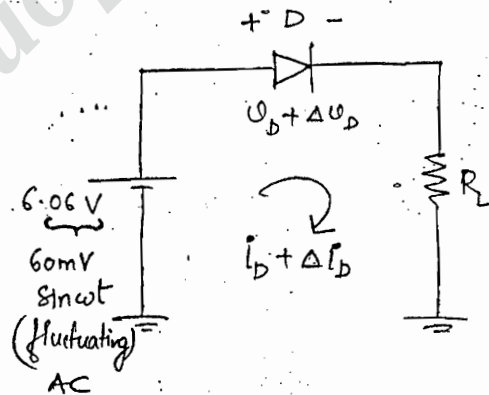
Dynamic Resistance



$$I_D = \frac{6 - 0.6}{1K} = 5.4 \text{ mA}$$

$$R_D = \frac{V_D}{I_D} = \frac{0.6 \text{ V}}{5.4 \text{ mA}}$$

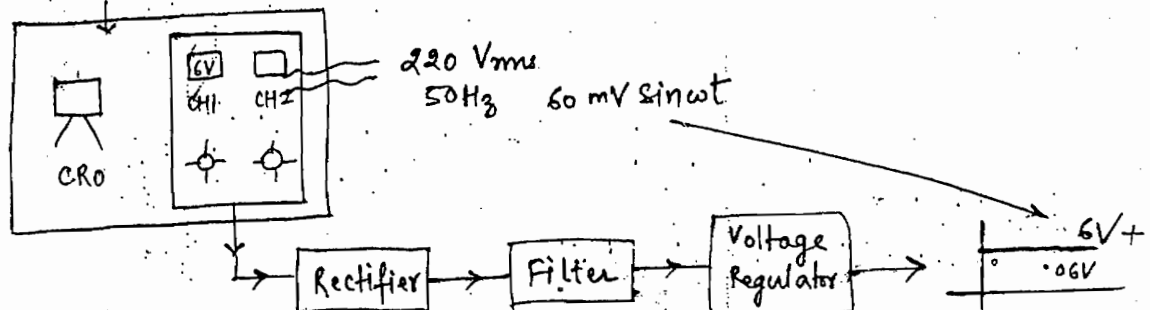
static/DC
Resistance = 111 Ω



$$r_D = \frac{\Delta V_D}{\Delta I_D}$$

dynamic/AC Resistance

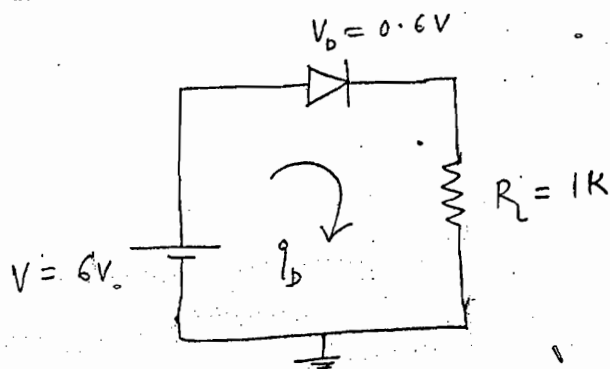
Regulated Power supply (DC):



Analysis:

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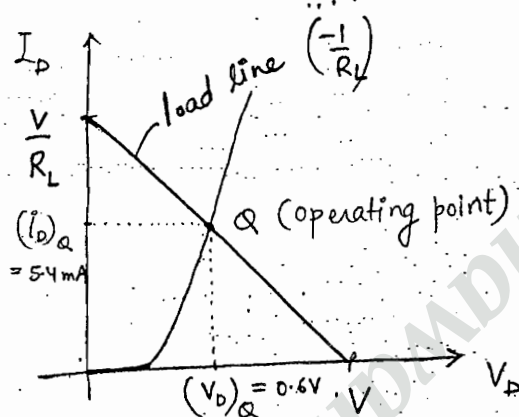
Static Resistance



Mathematical Analysis:

$$\begin{aligned} I_D &= \frac{V - V_D}{R_L} \\ &= \frac{6 - 0.6}{1K} \\ &= 5.4 \text{ mA} \end{aligned}$$

Graphical Analysis:



$$\begin{aligned} I_D &= \frac{V - V_D}{R_L} \\ I_D &= -\frac{V_D}{R_L} + \frac{V}{R_L} \end{aligned}$$

$$y = mx + c$$

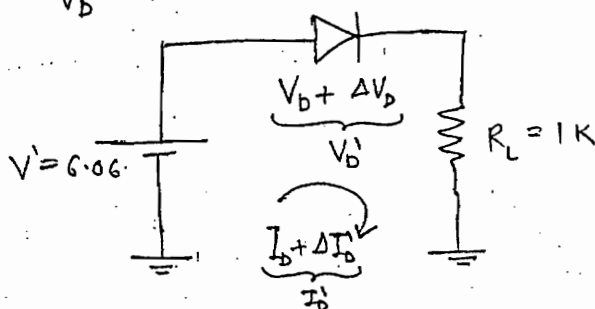
$$m = \text{slope} = -1/R_L$$

$$R_D = \frac{V_D}{I_D} = \frac{0.6}{5.4 \text{ mA}} = 111 \Omega$$

* As I_D increases, R_D decreases.

Design:

Suppose due to a small s/g variation ($V_i = 60 \text{ mV sin } \omega t$) the DC voltage $V = 6 \text{ volts}$ is changed to $V' = 6.06 \text{ volts}$. Then find the new diode current I_D' & new diode voltage V_D' .



Sol. $V' = V + \Delta V$

$$6.06V = 6V + 0.06V$$

$$I_D' = I_D + \Delta I_D$$

$$? \quad \downarrow \quad ?$$

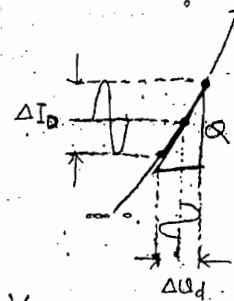
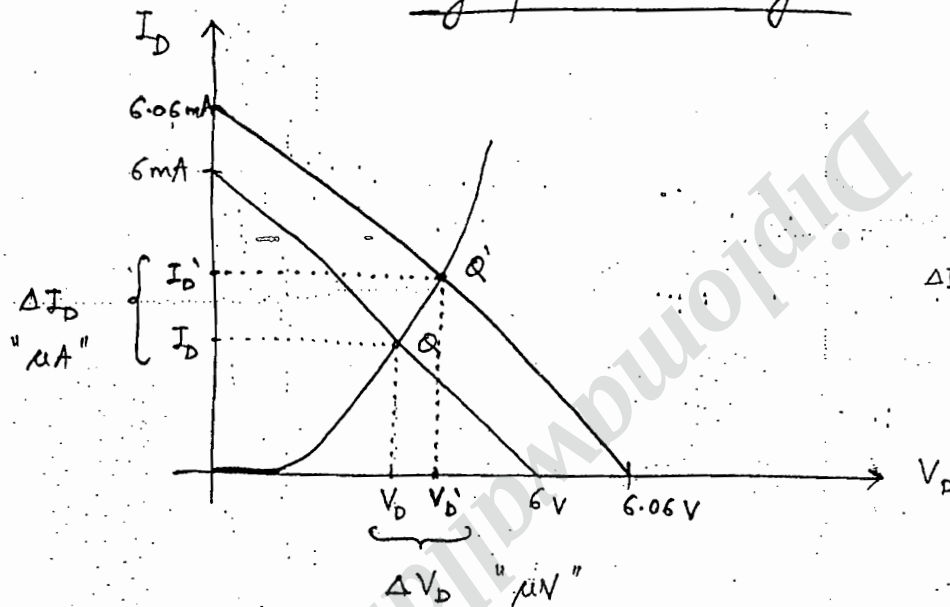
5.4 mA

$$V_D' = V_D + \Delta V_D$$

$$? \quad \downarrow \quad ?$$

0.6 V

graphical analysis:

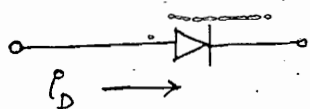


As the change in V_D and I_D are at μ level i.e. very small. That's why we do not go for graphical methods.

Note:

For small s/g analysis of a device (diode, BJT, FET, MOSFET) graphical method is not suitable because the variations are in the order of " μV " and " μA ". Mathematical analysis is best suitable for small s/g study.

- * Lower the Q-point; (smaller current or lower voltage), the higher is the ac resistance.
- * As with the dc and ac res. levels, the lower the level of currents, the higher is the resistance level.



$$I_D = I_0 e^{V_D/V_T}$$

$$\begin{aligned} I_D + \Delta I_D &= I_0 e^{(V_D + \Delta V_D)/V_T} \\ &= I_0 \cdot e^{V_D/V_T} \cdot e^{\Delta V_D/V_T} \\ &= I_D \cdot e^{\Delta V_D/V_T} \end{aligned}$$

$$\Delta V_D \ll V_T \quad (\text{small s/g analysis})$$

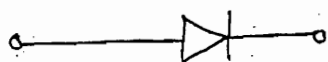
$$I_D + \Delta I_D = I_D \left(1 + \frac{\Delta V_D}{V_T}\right)$$

$$I_D + \Delta I_D = I_D + I_D \cdot \frac{\Delta V_D}{V_T}$$

$$\Delta I_D = I_D \cdot \frac{\Delta V_D}{V_T}$$

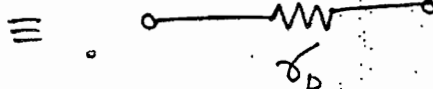
$$\boxed{\gamma_D = \frac{\Delta V_D}{\Delta I_D} = \frac{V_T}{I_D(\text{DC})}}$$

Small s/g equivalent model:

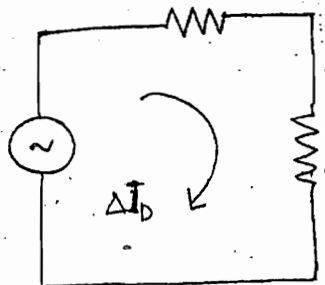


+ 0.3mV -

γ_d



60mV
sin wt



+ 59.7mV

$R_L = 1K$

$$\gamma_D = \frac{V_T}{I_D}$$

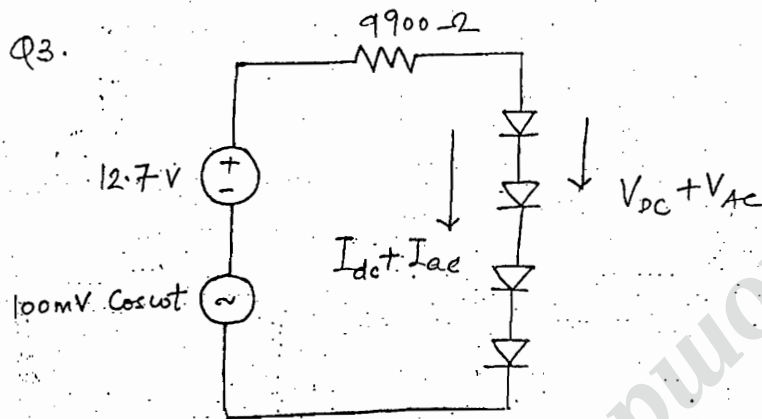
$$= \frac{26mV}{5.4mA}$$

$$= 5\Omega$$

$$\Delta I_D = \frac{\Delta V}{\gamma_d + R_L} = \frac{60mV \sin wt}{1K\Omega} = 60\mu A \sin wt$$

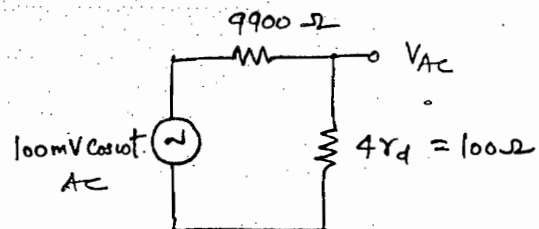
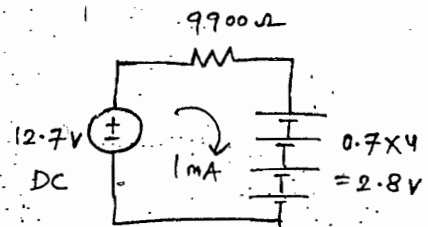
$$\begin{aligned}
 \Delta V_D &= \Delta I_D \cdot r_D \\
 &= 60 \mu A \sin \omega t \cdot (5 \Omega) \\
 &= 300 \mu V \cdot \sin \omega t \\
 &= 0.3 \text{ mV} \sin \omega t
 \end{aligned}$$

Hence, diode is a battery DC wise whereas AC wise it is a resistor.



Sol. $I_{dc} = \frac{12.7 - 4 \times 0.7}{9900} = 1 \text{ mA}$

$$\begin{aligned}
 r_D &= \frac{V_T}{I_{dc}} \\
 &= \frac{25 \text{ mV}}{1 \text{ mA}} \\
 &= 25 \Omega
 \end{aligned}$$



$$\begin{aligned}
 V_{ac} &= 100 \text{ mV} \cos \omega t \cdot \frac{100 \Omega}{(9900 + 100) \Omega} \\
 &= 100 \text{ mV} \cos \omega t \cdot \frac{100}{10,000} \\
 &= 1 \text{ mV} \cos \omega t
 \end{aligned}$$

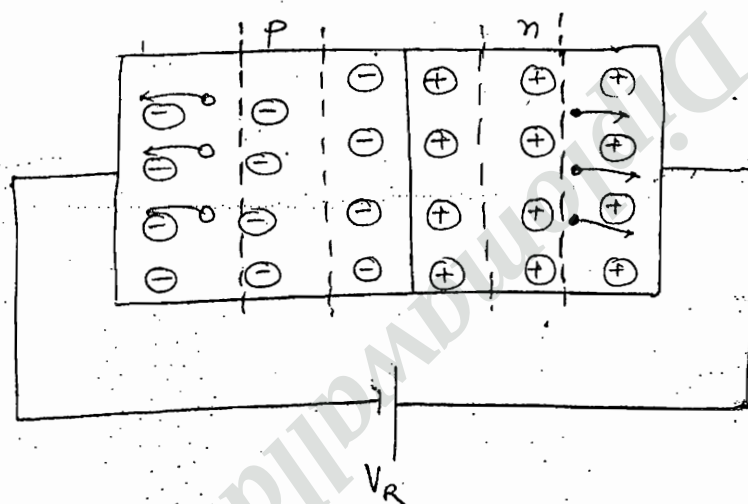
Transition Capacitance (C_T)
(RB)

(Transition means depletion which always occur in RB)

Diffusion Capacitance (C_D)
(FB)

(Diffusion occurs in FB because there is no depletion region)

Transition capacitance (C_T): (or) Depletion Region capacitance



MY BOOK HOUSE
PHOTO STATE SHOP
AVAILABLE ALSO NOTES, EC, ME, CS
ALL ERGONOMICAL, PSU
FORMS ALSO AVAILABLE
PH: 9312610333

"The rate of change of charge w.r. to applied voltage" — Capacitance

"The rate of change of immobile charge in the depletion region w.r. to reverse bias voltage" — Transition capacitance

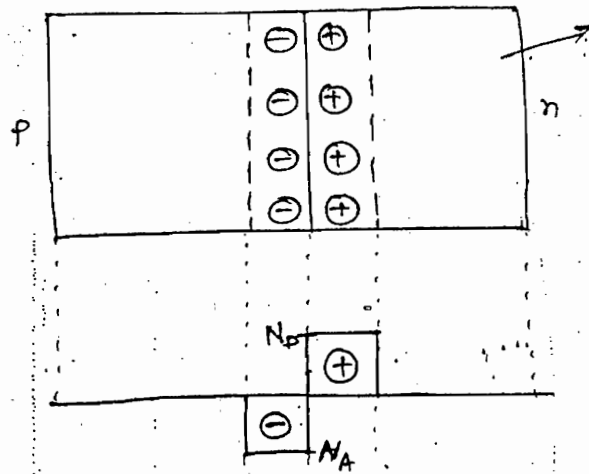
$$C_{||} = \frac{A\epsilon}{d}$$

$$C_T = \frac{A\epsilon}{\omega} \propto \frac{1}{\omega}$$

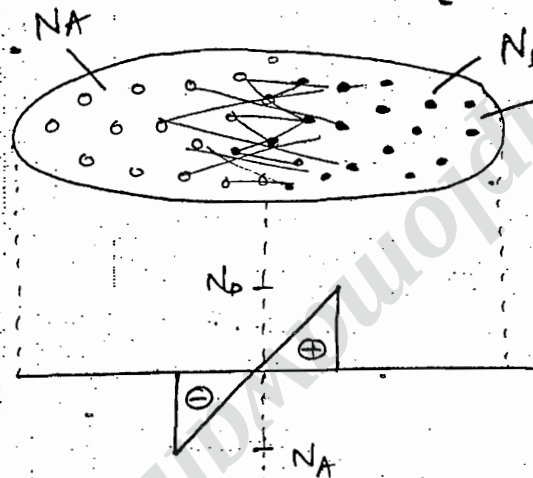
$$\omega \propto \sqrt{V_j} \rightarrow \text{Alloy type}$$

$$\omega \propto \sqrt[3]{V_j} \rightarrow \text{Grown jxn type}$$

} OC-jxn



Alloy type (or)
Step graded jxn (or)
Sudden change (or)
Abrupt change



Grown jxn (or)
Linearly graded (or)
Diffused type

From OC to RB -

$$\omega \propto \sqrt{V_j + V_R} \quad \text{— alloy type}$$

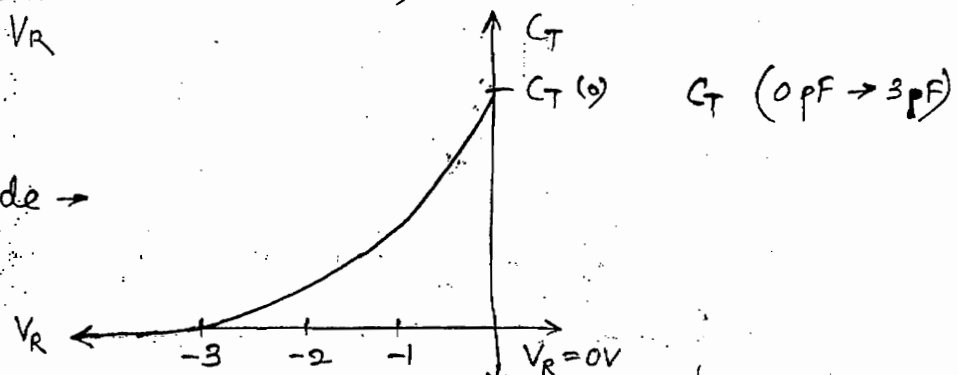
$$\omega \propto \sqrt[3]{V_j + V_R} \quad \text{— grown jxn type}$$

$$C_T \propto \frac{1}{\sqrt{V_R}}$$

$$C_T \propto \frac{1}{\sqrt[3]{V_R}}$$

(if V_j is neglected, generally V_j is very small)

ordinary diode →



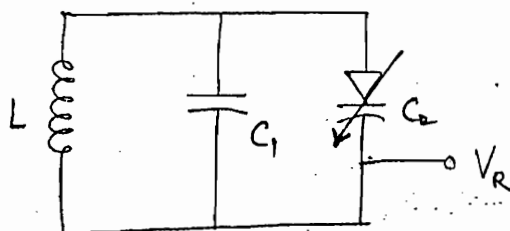
Varactor diode
(or)
Varicap



Power diode: $C_T: (5 \text{ pF} \rightarrow 200 \text{ pF})$

Tuning ckt:

VCO $\rightarrow$ Voltage Controlled Oscillator

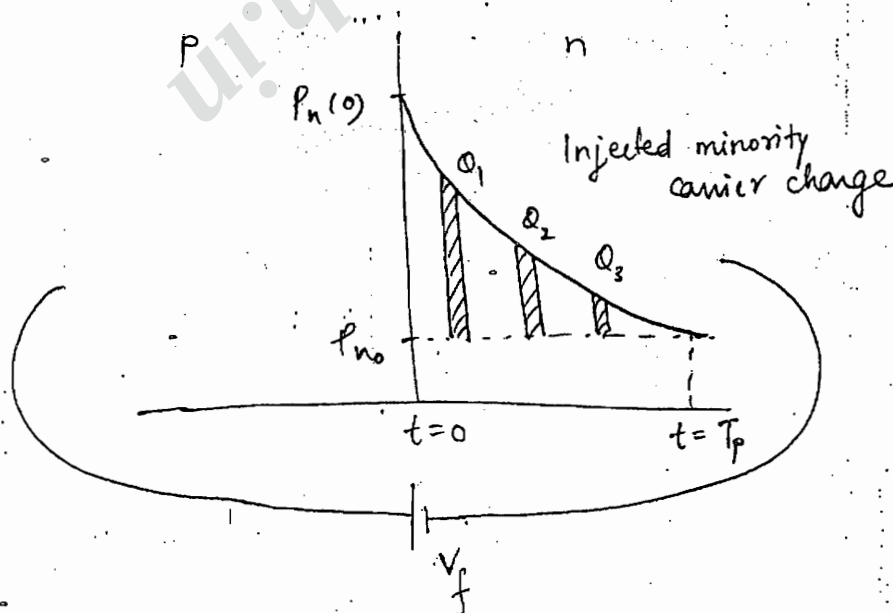


$$f = \frac{1}{2\pi \sqrt{LC_T}}$$

$$\& \quad C_T = C_1 + C_2$$

$\downarrow$
depends on V_R

Diffusion capacitance (C_D): (or) storage capacitance



"The rate of change of injected minority carrier charge w.r. to forward Bias voltage". $- C_D$

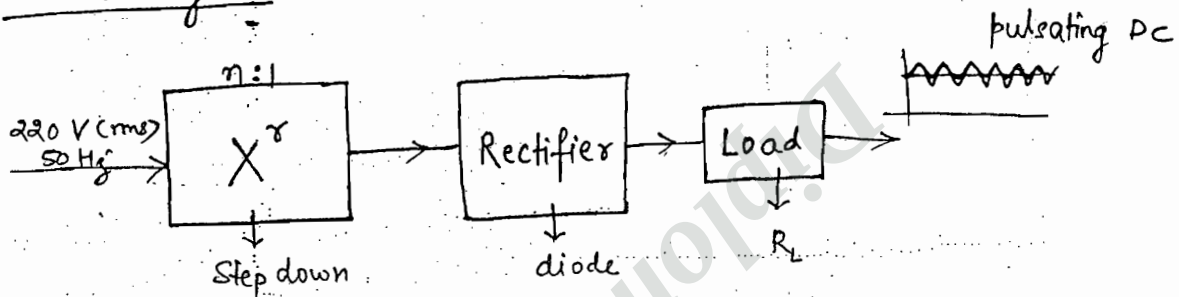
$$Q = I_p \times T_p$$

$$C_D = \frac{dQ}{dV} = T_p \frac{dI_p}{dV} = \frac{I_p I_p}{V_T} = \frac{T_p I_{de}}{V_T}$$

$$C_D \cong 100 \text{ pF}$$

Rectifiers:

Block Diagram:



It is a ckt which converts ac to pulsating dc.

Practical Rectifier designs are:

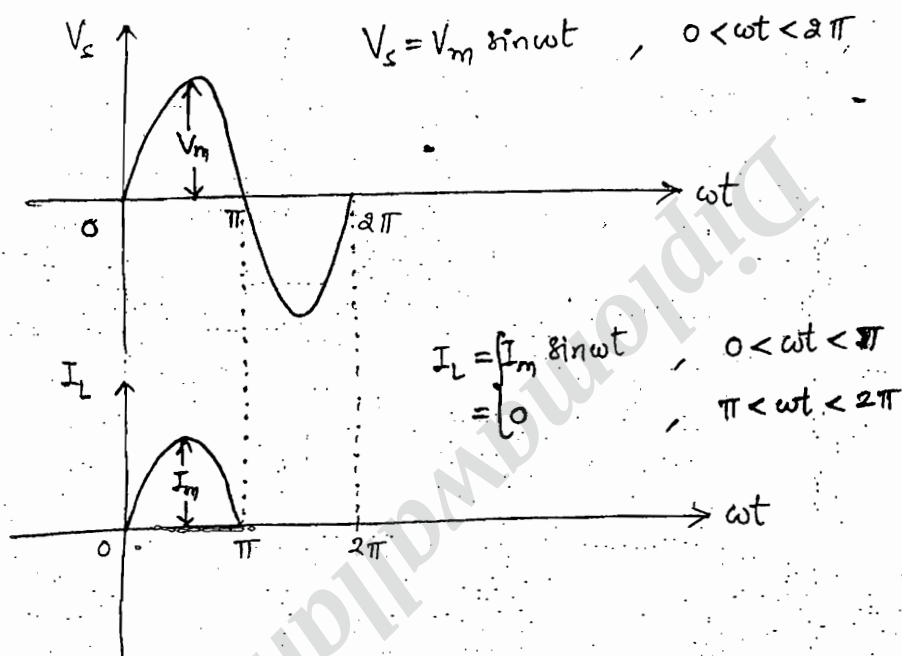
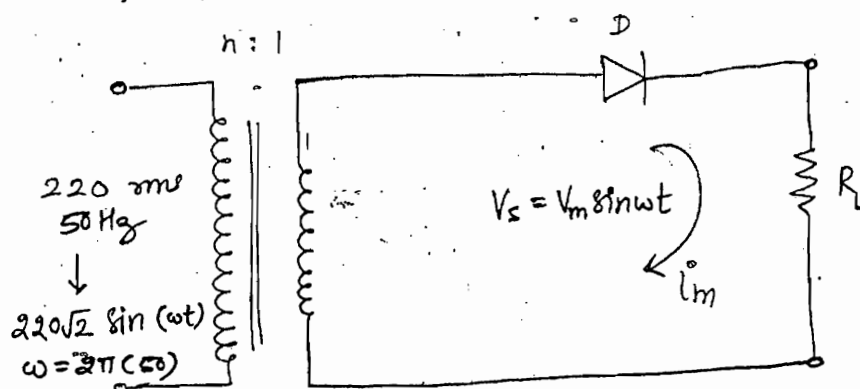
- (1). Half Wave Rectifier
- (2). Full Wave Rectifier with centre tap
- (3). Bridge Rectifier

Rectifier parameters:

- (1). Average current (I_{de})
- (2). DC o/p voltage (V_{de})
- (3). RMS load current (I_{rms})
- (4). Ripple factor
- (5). Voltage Regulation
- (6). Rectification Efficiency (η)
- (7). Transformer utilization factor (TOF)
- (8). Peak Inverse Voltage (PIV)
- (9). Form factor

(1). Half Wave Rectifier : (HWR)

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(1). I_{dc} :

$$I_{dc} = \frac{\text{area under the curve}}{2\pi}$$

$$I_{dc} = \frac{1}{2\pi} \int_0^{2\pi} I_L \cdot d\alpha \quad (\alpha = \omega t)$$

$$= \frac{1}{2\pi} \int_0^{\pi} I_L \cdot d\alpha + 0$$

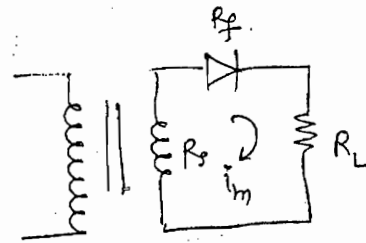
$$I_{dc} = \frac{I_m}{\pi}$$

(2). V_{dc} :

$$V_{dc} = I_{dc} \cdot R_L$$

$$= \frac{I_m}{\pi} \cdot R_L$$

$$V_{dc} = \frac{V_m \cdot R_L}{\pi (R_f + R_s + R_L)}$$



$$V_{dc} = \frac{V_m}{\pi \left(1 + \frac{R_s + R_f}{R_L}\right)}$$

$$V_{dc} \Big|_{R_L \rightarrow \infty} = \frac{V_m}{\pi} \quad (\text{no load condition})$$

(3). I_{rms} :

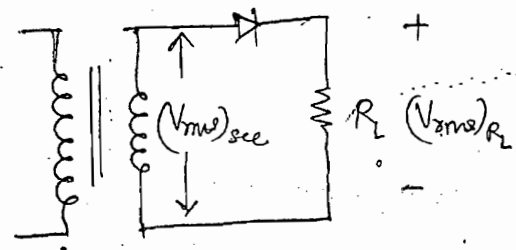
$$I_{rms} = \sqrt{\frac{1}{2\pi} \int_0^{2\pi} I^2 d\alpha}$$

$$= \sqrt{\frac{I_m^2}{2\pi} \int_0^{2\pi} \sin^2 \alpha d\alpha}$$

$$= \frac{I_m}{2}$$

$$(V_{rms})_{R_L} = V_m/2$$

$$(V_{rms})_{sec} = V_m/\sqrt{2}$$



(4). Ripple factor :

$$r_f = \frac{\text{rms value of alternating component}}{\text{average value}}$$

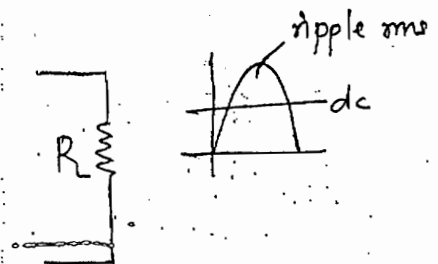
$I' \rightarrow$ ac current

$I_{dc} \rightarrow$ dc current

$I \rightarrow$ total current

$I'_{rms} \rightarrow$ rms value of ac current

$$r_f = \frac{I'_{rms}}{I_{dc}}$$



$$I_{rms} = \sqrt{\frac{1}{2\pi} \int_0^{2\pi} I'^2 d\alpha}$$

$$I = I_{dc} + I'$$

$$I' = I - I_{dc}$$

$$I_{rms}^2 = \frac{1}{2\pi} \int_0^{2\pi} (I - I_{dc})^2 d\alpha$$

$$= \underbrace{\frac{1}{2\pi} \int_0^{2\pi} I^2 d\alpha}_{I_{rms}^2} = \underbrace{\frac{2I_{dc}}{2\pi} \int_0^{2\pi} I d\alpha}_{I_{dc}} + \underbrace{\frac{I_{dc}^2}{2\pi} \int_0^{2\pi} d\alpha}_1$$

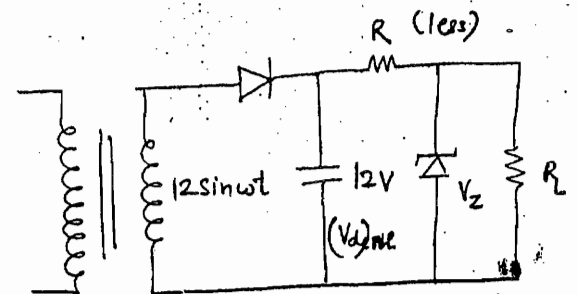
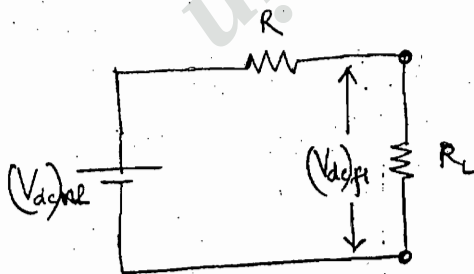
$$\gamma_f = \frac{I_{rms}}{I_{dc}} = \sqrt{\frac{I_{rms}^2 - I_{dc}^2}{I_{dc}^2}} = \sqrt{\left(\frac{I_{rms}}{I_{dc}}\right)^2 - 1}$$

$$\gamma_f = \sqrt{\left(\frac{I_m/2}{I_m/\pi}\right)^2 - 1}$$

$$= \sqrt{\left(\frac{\pi}{2}\right)^2 - 1}$$

$$= 1.21 \quad (\text{or}) \quad 121\%$$

(5) Voltage Regulation:



$$\% VR^n = \frac{(V_{dc})_{ne} - (V_{dc})_{fe}}{(V_{dc})_{fe}}$$

* Ideally $\% VR^n$ should be zero.

$$I_{dc} = \frac{I_m}{\pi}$$

$$I_{dc} = \frac{V_m}{\pi (R_f + R_s + R_L)}$$

$$I_{dc} (R_f + R_s) + I_{dc} \cdot R_L = \frac{V_m}{\pi}$$

$\downarrow$ $\downarrow$
 $(V_{dc})_f$ $(V_{dc})_{RL}$

$$\% VR^n = \frac{I_{dc} (R_f + R_s)}{\frac{V_m}{\pi} - I_{dc} (R_f + R_s)}$$

(6). Rectification Efficiency (η) :

$$\eta = \frac{\text{DC power delivered to load}}{\text{Ac i/p power}}$$

$$= \frac{P_{dc}}{P_{ac}} = \frac{I_{dc}^2 \cdot R_L}{I_{m}^2 \cdot (R_f + R_s + R_L)}$$

$$= \frac{I_{dc}^2}{I_m^2} \cdot \frac{1}{\left(1 + \frac{R_f + R_s}{R_L}\right)}$$

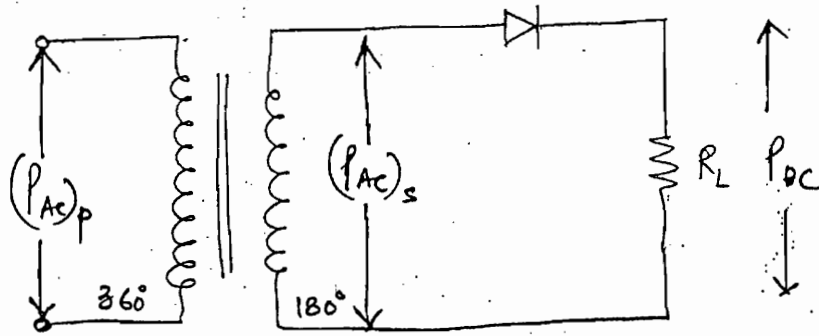
If $R_L \gg (R_f + R_s)$

$$\eta \approx \frac{I_{dc}^2}{I_m^2}$$

$$= \frac{(I_m / \pi)^2}{(I_m / 2)^2}$$

$$= \frac{4}{\pi^2}$$

$$= 40.6\% \quad (\text{max}^m)$$

(7). TUF:

$$TUF = \frac{(TUF)_p + (TUF)_s}{2} \quad (X)$$

$$(TUF)_p = \frac{P_{dc}}{(P_{AC})_p} = \eta = 40.6\%$$

$$(TUF)_s = \frac{P_{dc}}{(P_{AC})_s} = \frac{I_{dc}^2 R_L}{(V_{rms} \cdot I_{rms})_{sec}}$$

$$= \frac{I_{dc}^2 R_L}{\frac{V_m}{\sqrt{2}} \cdot \frac{I_m}{2}}$$

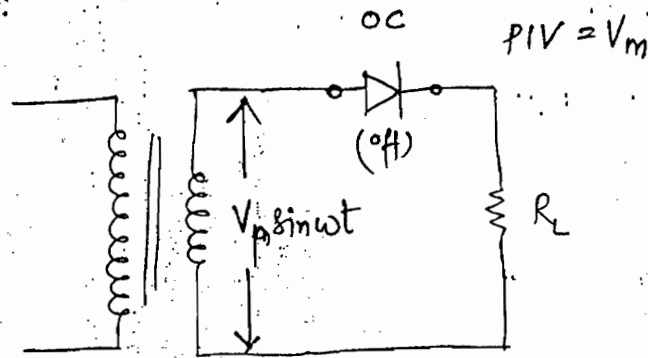
$$= \frac{I_{dc}^2 R_L}{\frac{I_m^2}{2\sqrt{2}} (R_f + R_s + R_L)}$$

$$= \frac{I_{dc}^2}{\frac{I_m^2}{2\sqrt{2}} \left(1 + \frac{R_f + R_s}{R_L}\right)}$$

$$\text{if } R_L \gg (R_f + R_s),$$

$$\therefore (TUF)_s = \frac{(I_m/\pi)^2}{I_m^2/2\sqrt{2}} = \frac{2\sqrt{2}}{\pi^2} = 28\%$$

$$\therefore TUF = (TUF)_s = 28\%$$

(8). PIV:(9). Form factor (FF):

$$FF = \frac{\text{rms value}}{\text{avg. value}} = \frac{I_{\text{rms}}}{I_{\text{dc}}}$$

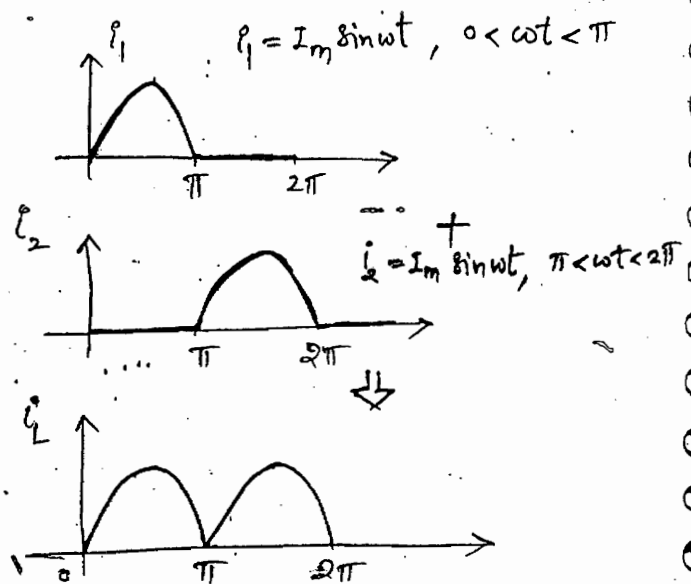
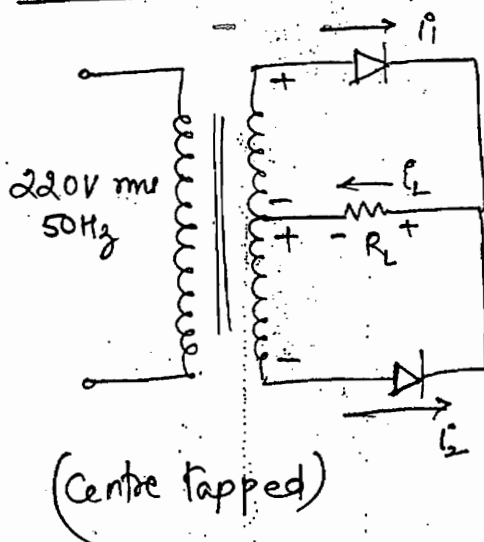
$$= \frac{I_m / 2}{I_m / \pi}$$

$$= \pi / 2 = 1.57$$

(10). Peak factor (PF):

$$\text{Peak factor} = \frac{\text{peak value}}{\text{rms value}}$$

$$= \frac{I_m}{I_m / 2} = 2$$

(2). Full Wave Rectifier:

$$(1) \quad I_{dc} = \frac{2 I_m}{\pi}$$

$$(2) \quad (V_{dc})_{re} = \frac{2 V_m}{\pi}$$

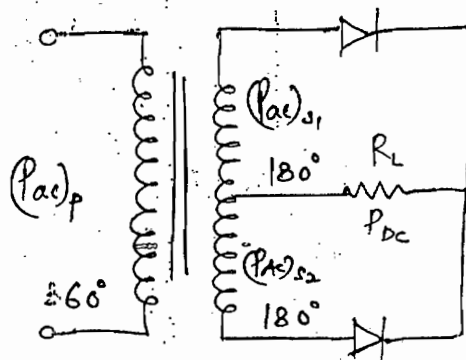
$$(3) \quad I_{rms} = \frac{I_m}{\sqrt{2}}$$

$$\begin{aligned} (4) \quad \gamma_f &= \sqrt{\left(\frac{I_{rms}}{I_{dc}}\right)^2 - 1} \\ &= \sqrt{\frac{(I_m/\sqrt{2})^2}{(2 I_m/\pi)^2} - 1} \\ &= \sqrt{\frac{\pi^2}{8} - 1} = 0.48 \\ &= 48\% \end{aligned}$$

$$(5) \quad \begin{array}{ccccccc} I_{dc} (R_f + R_s) + I_{dc} R_L = V_m / \pi \\ \downarrow \quad \quad \downarrow \quad \quad \downarrow \quad \quad \downarrow \\ 2 I_m / \pi \quad R_s / 2 \quad 2 V_m / \pi \quad 2 V_m / \pi \end{array}$$

$$V_R^n =$$

$$\begin{aligned} (6) \quad \eta &= \frac{P_{dc}}{P_{ac}} \\ &= \frac{I_{dc}^2}{I_{rms}^2} \\ &= \frac{(2 I_m / \pi)^2}{(I_m / \sqrt{2})^2} = \frac{8}{\pi^2} \\ &= 81.2\% \end{aligned}$$

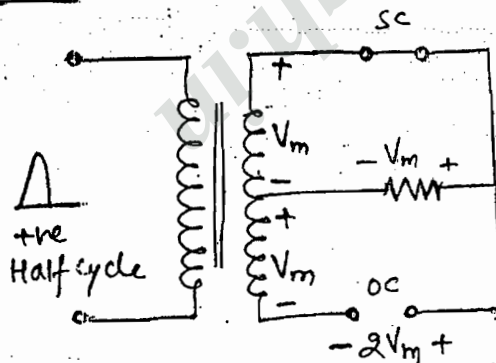
(7). TUF:

$$(TUF)_p = \frac{P_{DC}}{(P_{Ac})_p} = \eta = 81.2\%$$

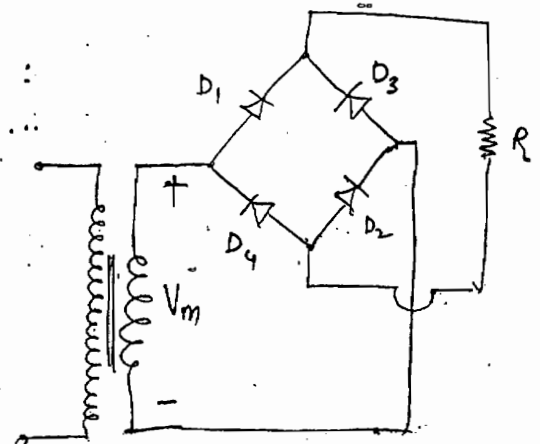
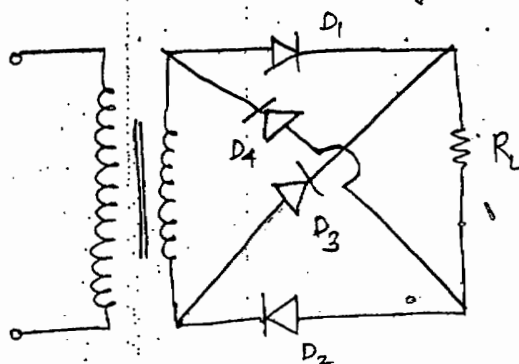
$$(TUF)_s = \frac{P_{DC}}{(P_{Ac})_{s1} + (P_{Ac})_{s2}} = 57.3\%$$

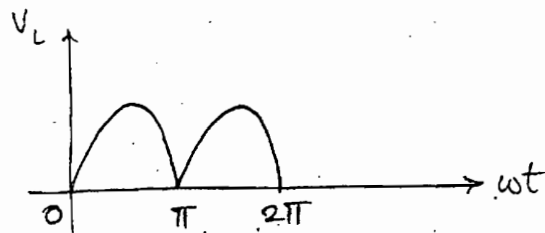
$$TUF = \frac{81.2 + 57.3}{2} = 69.3\% \text{ (disadvantage)}$$

* Avg can be taken here becoz 180° each get added and mean val be taken.

(8) PIV:

$$PIV = 2V_m \text{ (disadvantage)}$$

(3). Full Wave Rectifier (Bridge Type):



Common parameters of full wave Rectifier (centre tapped)

$$(1) I_{dc} = \frac{2I_m}{\pi}$$

$$(2) (V_{dc})_{re} = \frac{2V_m}{\pi}$$

$$(3) I_{rms} = \frac{I_m}{\sqrt{2}} \quad \& \quad V_{rms} = \frac{V_m}{\sqrt{2}}$$

$$(4) \gamma_f = 48\%$$

$$(5) \eta = 81.2\%$$

Other parameters :

$$(6) I_{dc} \left(\underbrace{R_f}_{2R_f} + \underbrace{R_s}_R \right) + I_{dc} R_L = \frac{2V_m}{\pi}$$

$$I_{dc} (2R_f + R_s) + I_{dc} R_L = \frac{2V_m}{\pi}$$

$$V_R^n =$$

(7) TUF :

$$(TUF)_p = \frac{P_{dc}}{(P_{ac})_p} = \eta = 81.2\%$$

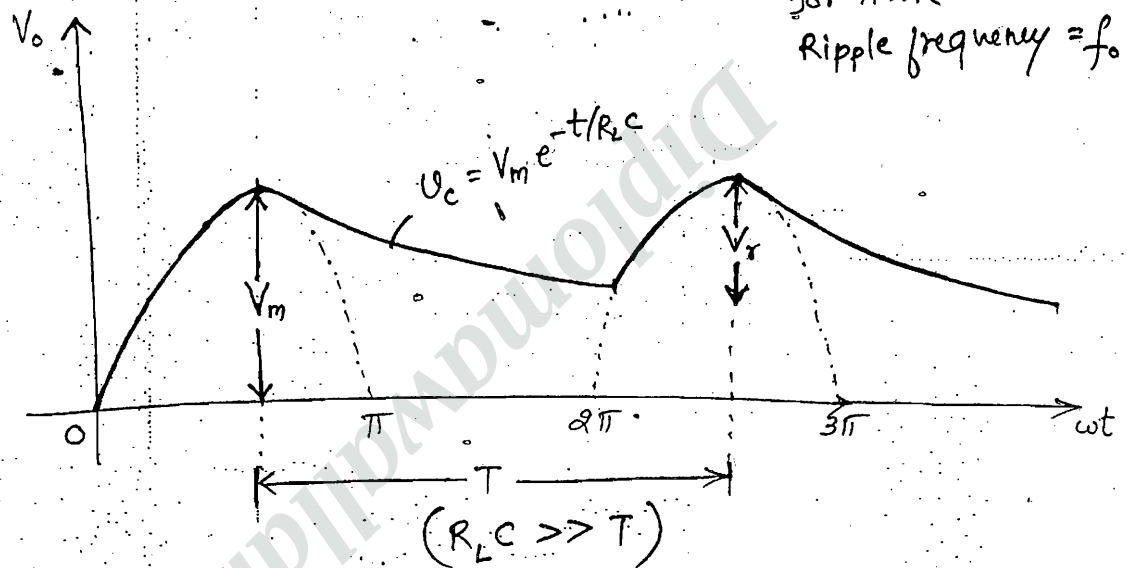
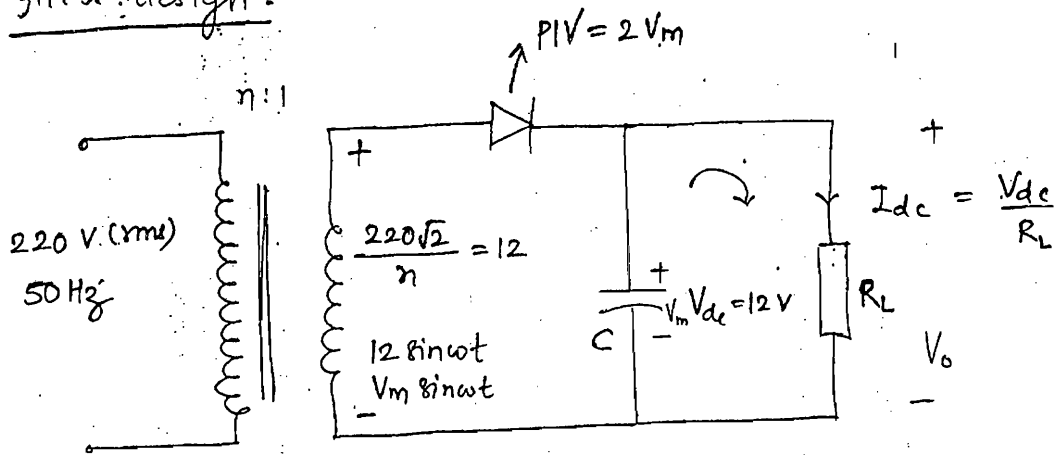
(360° - 360°)

$$(TUF)_s = \frac{P_{dc}}{(P_{ac})_s} = \frac{P_{dc}}{(P_{ac})_p} = \eta = 81.2\%$$

$$\therefore TUF = 81.2\%$$

$$(8) PIV = V_m \quad (\text{advantage})$$

filter design:



$$f = \frac{1}{T} = 50$$

$$T = \frac{1}{f} = \frac{1}{50} = 20 \text{ ms}$$

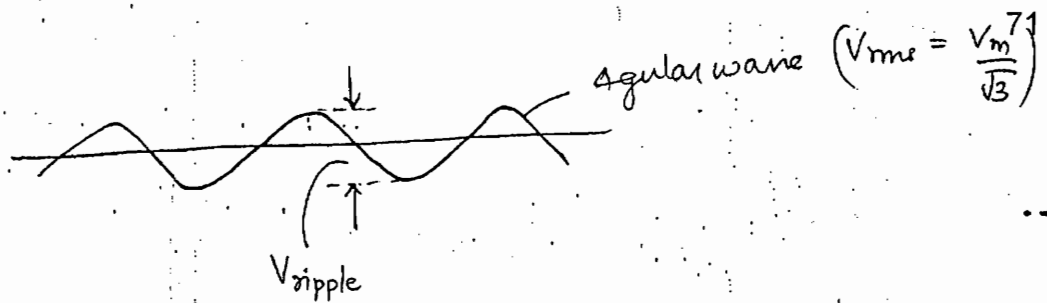
$$V_c = V_m e^{-t/RLC}$$

$$\text{if } R_L C \gg T$$

$$V_c = V_m \left(1 - \frac{T}{R_L C}\right)$$

$$V_c = \underbrace{V_m}_{V_{dc}} - \underbrace{\frac{V_m T}{R_L C}}_{V_{\text{ripple}}}$$

$$\downarrow V_{\text{ripple}} = \frac{V_m T}{(R_L C)} \uparrow$$



$$r_f = \frac{V_{\text{ripple (rms)}}}{V_{dc}}$$

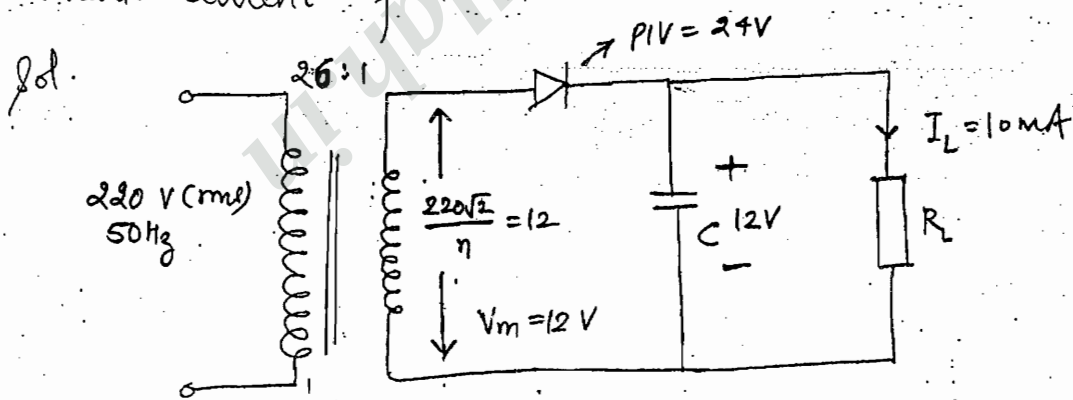
$$= \left(\frac{V_m T}{R_L C} \right) \cdot \frac{1}{\sqrt{3}} \cdot \frac{1}{V_{dc}}$$

$$r_f = \frac{T}{\sqrt{3} R_L C} = \frac{1}{\sqrt{3} f R_L C}$$

If $R_L = \infty$, $r_f = 0$ & $V_c = V_m$

Problem:

Design a DC power supply of 12V where the r_f should not exceed 5% for a 220V (rms), 50Hz supply if the load current of 10mA.



(1) Turns ratio:

$$\frac{220\sqrt{2}}{\eta} = 12 \Rightarrow \eta = 26$$

(2) R_L :

$$R_L = \frac{V_{dc}}{I_L} = \frac{12V}{10mA} = 1.2K\Omega$$

(3) δf :

$$\delta f = \frac{V_{\text{ripple}} (m\%) }{V_{dc}}$$

$$\frac{5}{100} = \frac{V_{\text{ripple}}}{12\sqrt{3}}$$

$$V_{\text{ripple}} = \frac{60\sqrt{3}}{100} \approx 1 \text{ Volt}$$

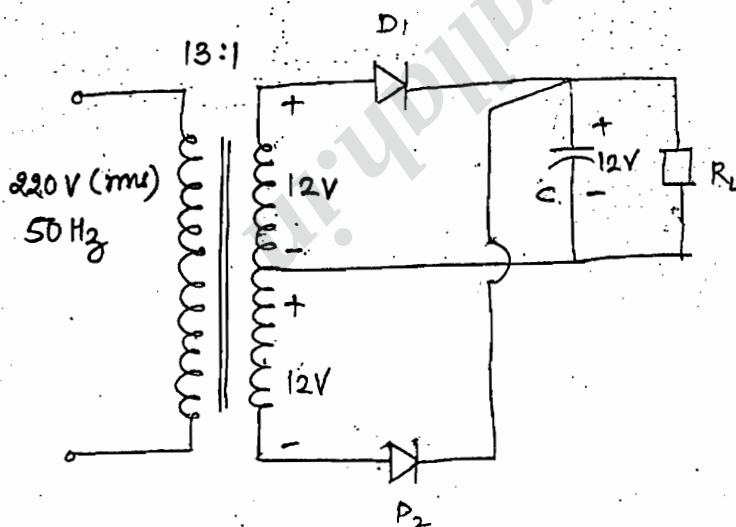
(4) V_{ripple} :

$$V_{\text{ripple}} = \frac{V_m T}{R_L C}$$

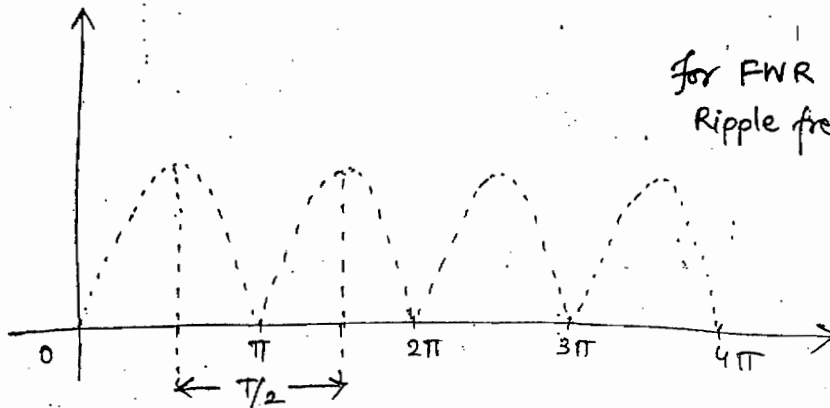
$$I = \frac{I_L T}{C}$$

$$C = 10 \text{ mA} \times 20 \text{ ms}$$

$$\therefore C = 200 \mu\text{F}$$

FWR with filter (capacitor):

for FWR
Ripple frequency = $2f_0$



$$(1) \frac{220\sqrt{2}}{\eta} = 24 \Rightarrow \eta = 13$$

$$(2) R_L = \frac{V_{dc}}{I_L} = \frac{12V}{10mA} = 1.2 K\Omega$$

$$(3) r_f = \frac{V_{ripple (rms)}}{V_{dc}}$$

$$\frac{5}{100} = \frac{V_{ripple}}{12\sqrt{3}} \Rightarrow V_{ripple} \cong 1 \text{ volt}$$

$$(4) V_{ripple} = \frac{V_m}{R_L C} \cdot \left(\frac{I}{2}\right)$$

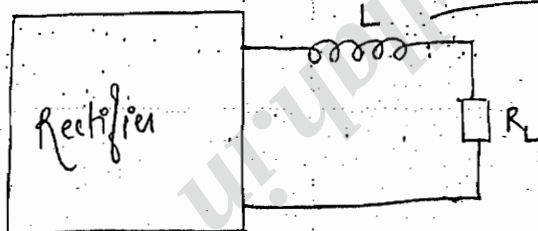
$$I = \frac{I_L}{C} \cdot \left(\frac{I}{2}\right)$$

$$C = 10mA \times 10ms$$

$$\therefore C = 100 \mu F$$

Conclusion:

(Inductor filter):

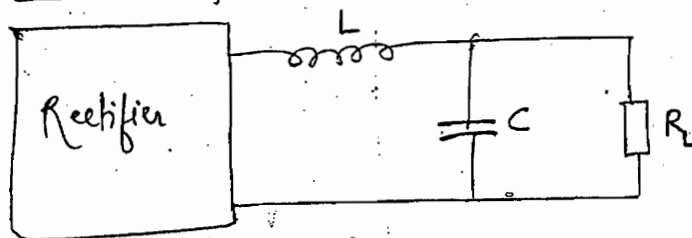


size $\uparrow$, $r_f \propto R_L$

* Heavy load techniques are not possible with inductor.

* Good for No load.

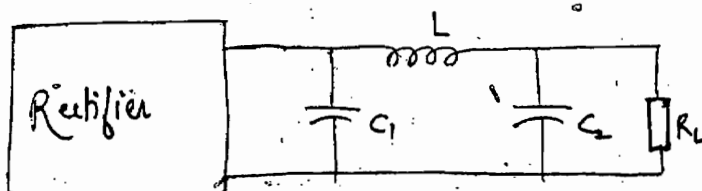
L section filter:



$$r_f = \frac{\sqrt{2}}{3} \cdot \frac{X_C}{X_L}$$

* for variable load.

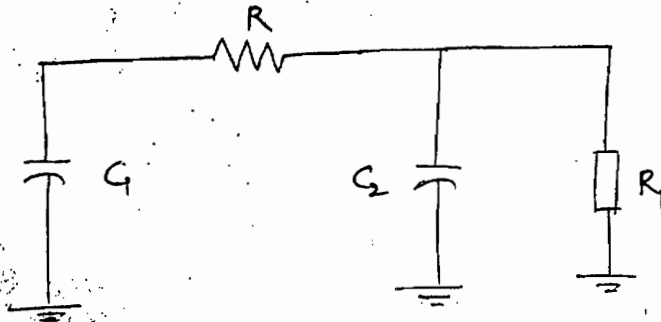
Π Section filter:



$$r_f = \frac{\sqrt{2} \cdot X_{C1} \cdot X_{C2}}{R_L \cdot X_L}$$

* for low loads.

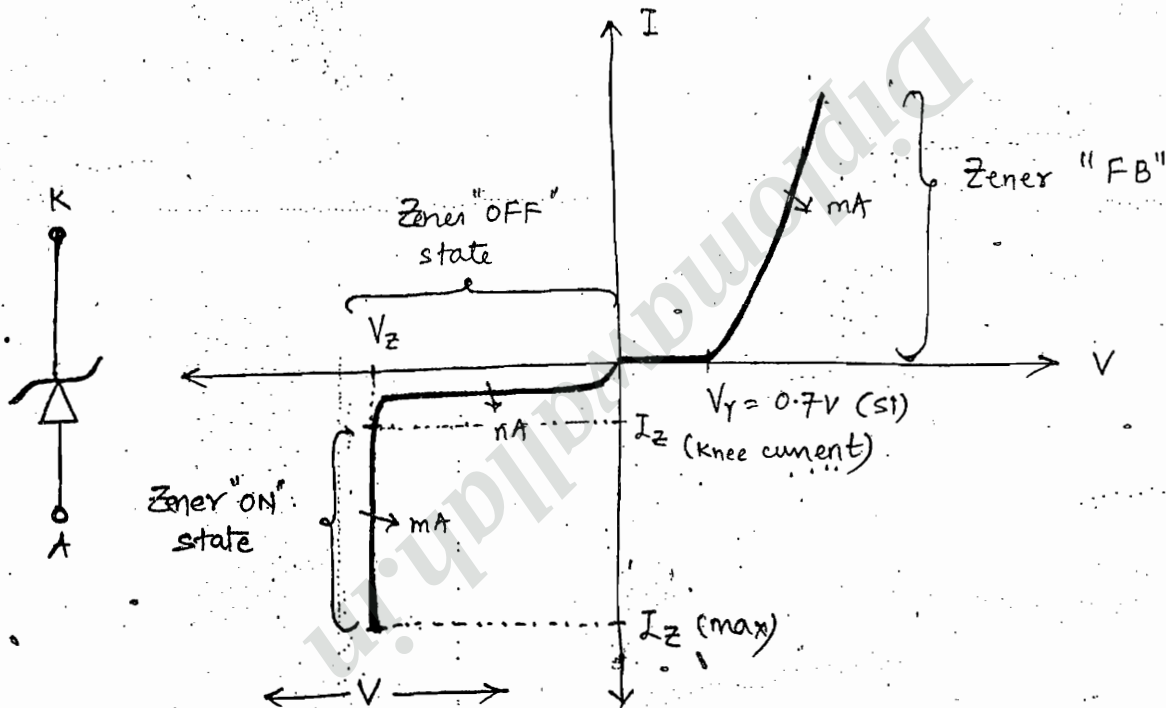
RC filter:



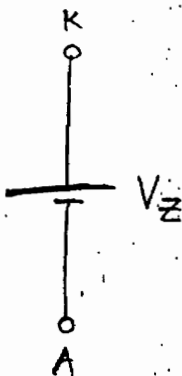
$$r_f = \frac{\sqrt{2} X_{C1} \cdot X_{C2}}{R_L \cdot R}$$

* for low loads

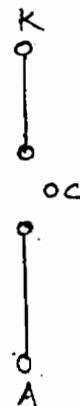
Zener Voltage Regulator -



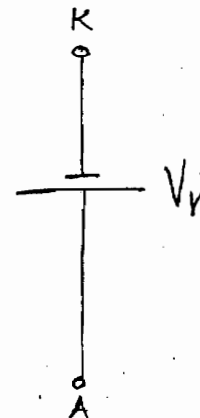
$V > V_Z$
"ON" state



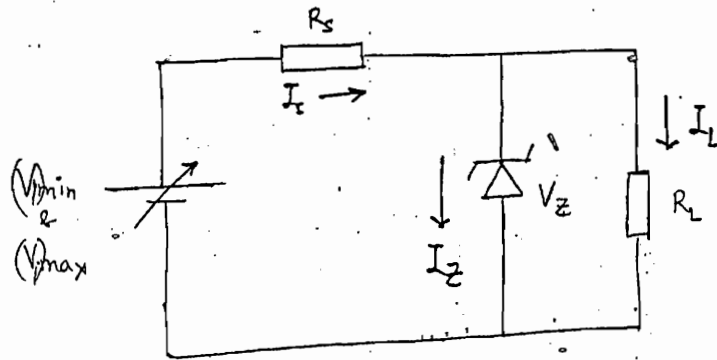
$0 < V < V_Z$
"OFF" state



$V \rightarrow FB$



(1). Zener diodes will be manufactured by Si material whereas tunnel diode will be manufactured by Ge or GaAs.



$$(I_s)_{\min} = \frac{(V_i)_{\min} - V_z}{R_s}$$

$$(I_s)_{\max} = \frac{(V_i)_{\max} - V_z}{R_s}$$

$$\left. \begin{aligned} (I_z)_{\min} &= (I_s)_{\min} - I_L \\ (I_z)_{\max} &= (I_s)_{\max} - I_L \end{aligned} \right\} \begin{array}{l} V_i \rightarrow \text{variable} \\ \& \\ R_L \rightarrow \text{fixed} \end{array}$$

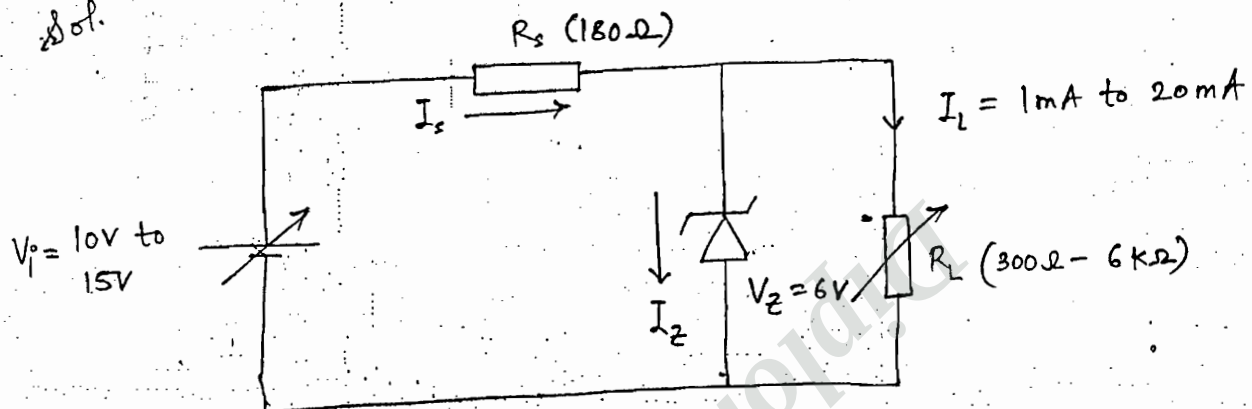
$$\left. \begin{aligned} (I_z)_{\min} &= (I_s)_{\min} - (I_L)_{\max} \\ (I_z)_{\max} &= (I_s)_{\max} - (I_L)_{\min} \end{aligned} \right\} \begin{array}{l} V_i \rightarrow \text{variable} \\ \& \\ R_L \rightarrow \text{variable} \end{array}$$

$$\left. \begin{aligned} (I_z)_{\min} &= I_s - (I_L)_{\max} \\ (I_z)_{\max} &= I_s - (I_L)_{\min} \end{aligned} \right\} \begin{array}{l} V_i \rightarrow \text{fixed} \\ \& \\ R_L \rightarrow \text{variable} \end{array}$$

Problem:

Design a DC regulated power supply of 6V across the load with variable load current of 1mA to 20mA. Zener diode specifications are $(I_Z)_{\min} = 0.1 \text{ mA}$ & $(I_Z)_{\max} = 50 \text{ mA}$ with o/p unregulated DC voltage of 10V to 15V.

sol.



$$I_s = \frac{V_i - V_Z}{R_s}$$

$$(I_s)_{\min} = \frac{(V_i)_{\min} - V_Z}{(R_s)_{\max}}$$

$$(I_s)_{\min} - (I_L)_{\max} > (I_Z)_{\min}$$

$$\frac{(V_i)_{\min} - V_Z}{(R_s)_{\max}} - (I_L)_{\max} > (I_Z)_{\min}$$

$$\frac{10 - 6}{(R_s)_{\max}} - 20 \text{ mA} > 0.1 \text{ mA}$$

$$\frac{4}{(R_s)_{\max}} > 20.1 \text{ mA}$$

$$\therefore (R_s)_{\max} < 199 \Omega$$

Now,

$$\text{similarly, } (I_s)_{\max} - (I_L)_{\min} < (I_Z)_{\max}$$

$$\frac{(V_i)_{\max} - V_Z}{(R_S)_{\min}} - (I_L)_{\min} < (I_Z)_{\max}$$

$$\frac{15 - 6}{(R_S)_{\min}} - 1 \text{ mA} < 50 \text{ mA}$$

$$(R_S)_{\min} > \frac{9}{51 \text{ mA}}$$

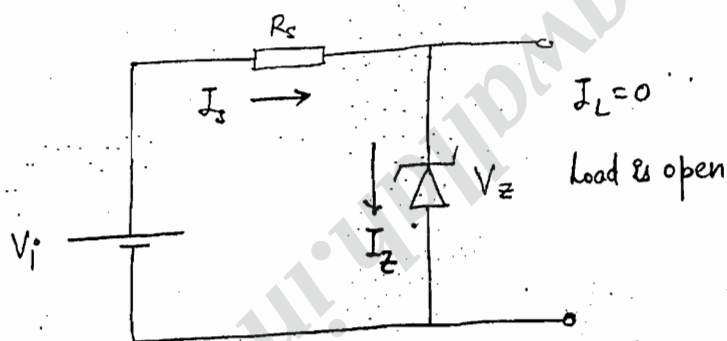
$$\therefore (R_S)_{\min} > 176 \Omega$$

$$\therefore R_S \in (176, 199) \Omega$$

$$176 < R_S < 199$$

Hence, we choose $R_S = 180 \Omega$ (Better)

Small s/g Analysis of Zener diode :



$$V_i = I_S R_S + V_Z$$

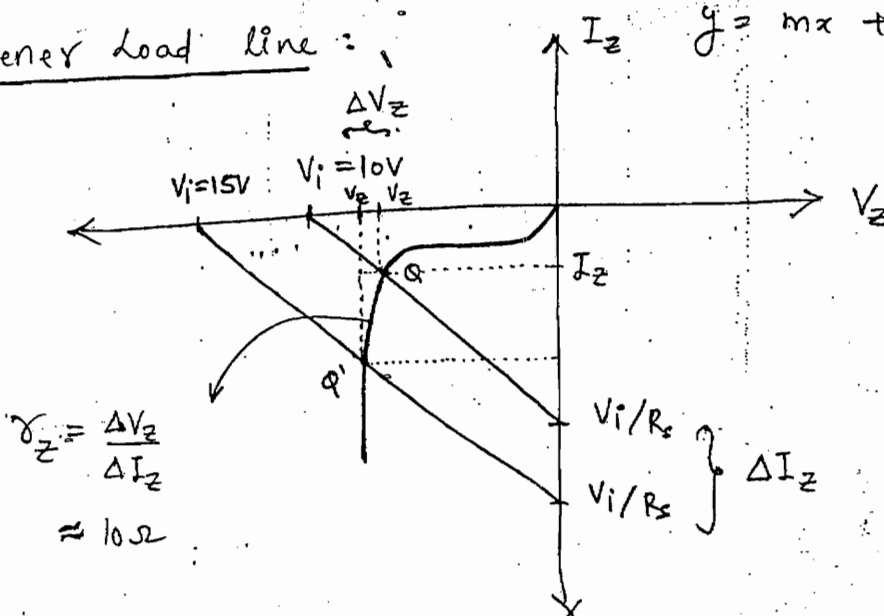
$$I_S = I_Z$$

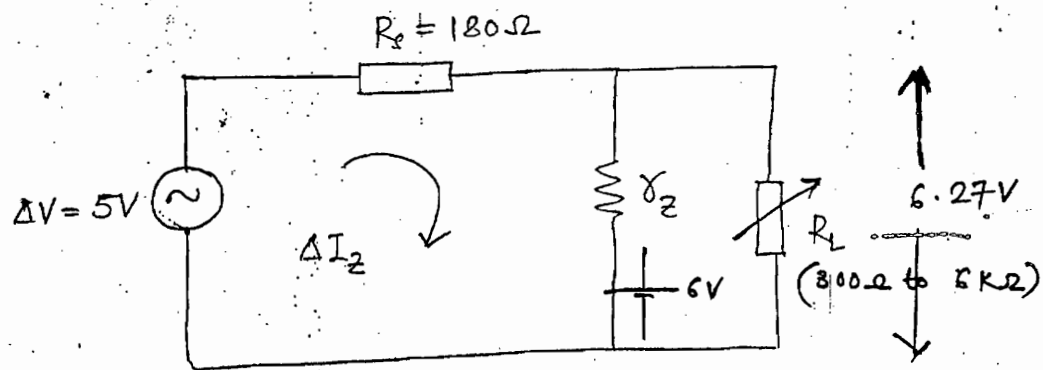
$$\therefore V_i = I_Z R_S + V_Z$$

$$I_Z = \left(-\frac{1}{R_S}\right) V_Z + \frac{V_i}{R_S}$$

$$y = mx + c$$

Zener Load line :





$$\Delta I_z \approx \frac{\Delta V}{R_s + r_z} \quad (r_z \ll R_L)$$

$$\Delta I_z \approx \frac{\Delta V}{R_s}$$

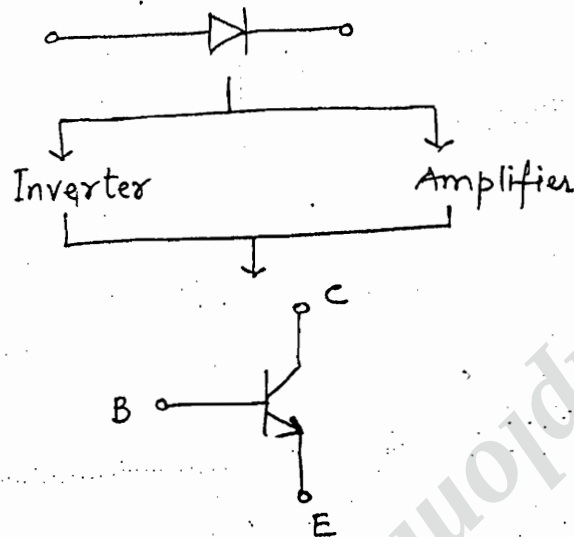
And,

$$\begin{aligned} \Delta V_z &= \Delta I_z \times r_z \\ &= \frac{\Delta V}{R_s} \cdot r_z \\ &= \frac{5}{180} \times 10 \\ &= \frac{5}{18} = 0.27V \end{aligned}$$

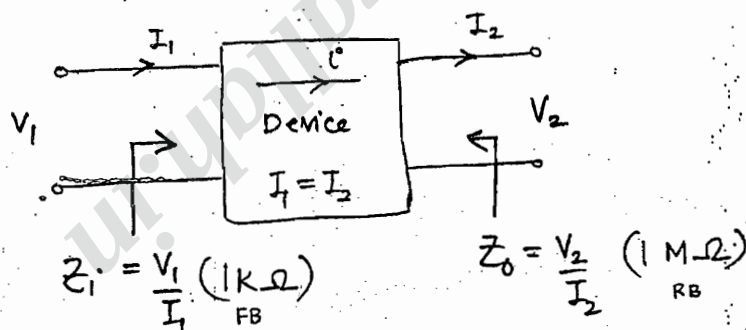
(1). Problems with diode:

Diode cannot be used as an inverter

Diode cannot be used as an amplifier.



Two port N/w:



$$P_v = \frac{P_o}{P_i} = \frac{I_2^2 Z_o}{I_1^2 Z_i} = \frac{Z_o}{Z_i}$$

$$= \frac{1 \text{ M}\Omega}{1 \text{ K}\Omega} = 10^3$$

$$P_o = 1000 P_i \rightarrow \text{Amplifier}$$

* Transistor is a transfer of current.

(1). Emitter injection efficiency-

$$\begin{aligned}\gamma &= \frac{I_{EP}}{I_E} \\ &= \frac{I_{EP}}{I_{EP} + I_{EN}} \\ &= \frac{1}{1 + \frac{I_{EN}}{I_{EP}}} \approx 1\end{aligned}$$

(2). Base transport factor (β^*)

$$\beta^* = \frac{I_{CP}}{I_{EP}} = \frac{I_{EP} - I_R}{I_{EP}} = 1 - \frac{I_R \downarrow}{I_{EP} \uparrow} \approx 1$$

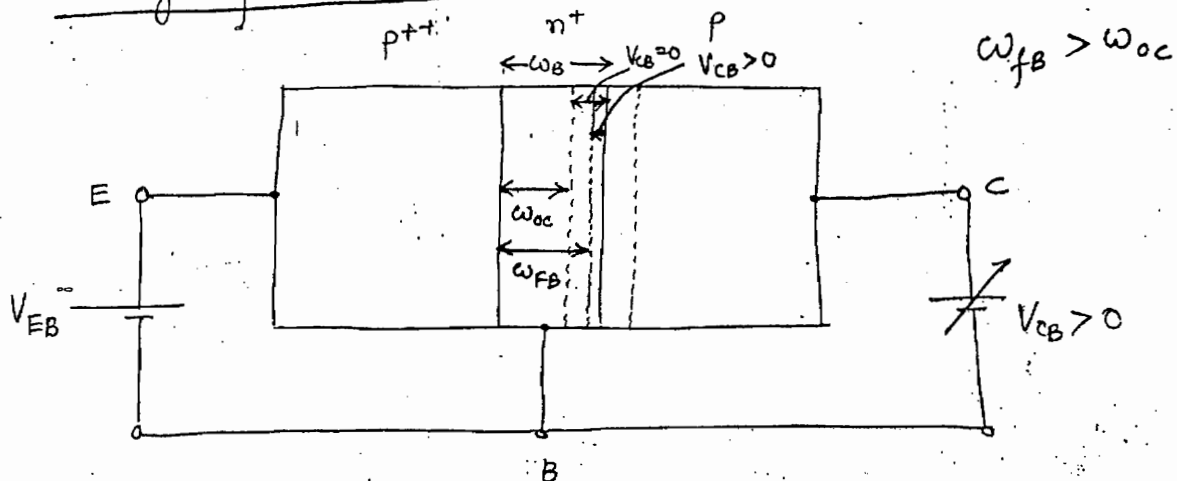
(3). Large s/g current gain (α)

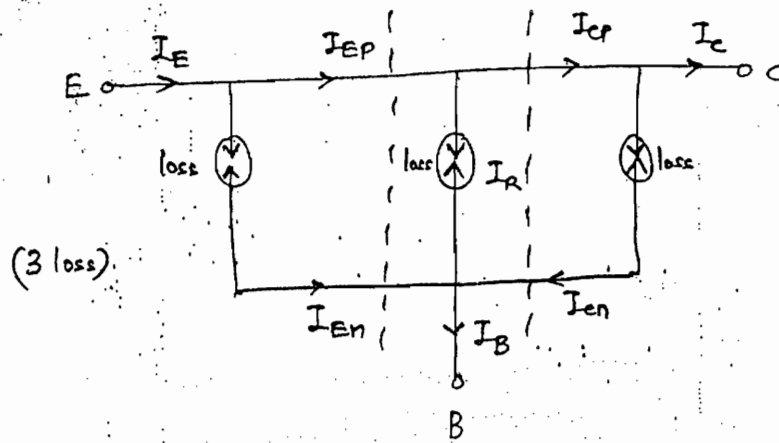
$$\alpha = \frac{I_C}{I_E} = \frac{I_{CP}}{I_{EP}} \cdot \frac{I_{EP}}{I_E}$$

$$\therefore \alpha = \beta^* \gamma$$

In Best transistors, $\alpha = 0.98$ i.e. $I_C = 0.98 I_E$

Testing of BJT in Saturation:





$$V_{EB} = \text{const.}; \quad V_{CB} > 0; \quad \frac{\omega_B}{L_B} \ll 1$$

$$\downarrow I_E = I_{EP} \downarrow + I_{EN} (\text{const.})$$

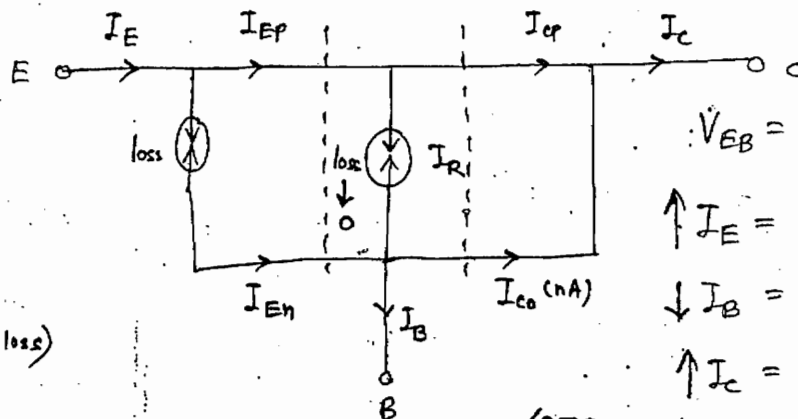
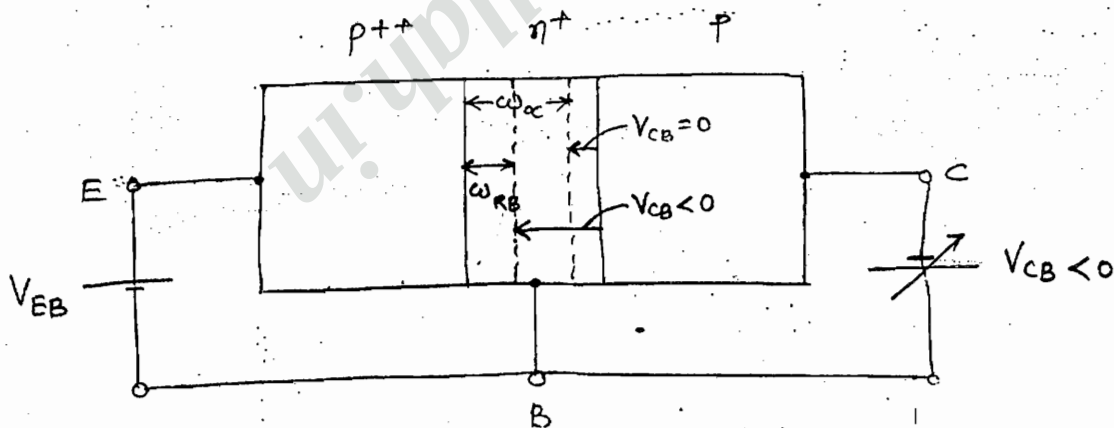
$$\downarrow I_C = I_{CP} \downarrow - I_{CN} \uparrow$$

$$\uparrow I_B = I_{EN} + I_R \uparrow + I_{CN} \uparrow$$

(const.)

BJT is a failure in Saturation

Testing of BJT in Active Region:



$$V_{EB} = \text{const.}; \quad V_{CB} < 0; \quad \frac{\omega_B}{L_B} \ll 1$$

$$\uparrow I_E = I_{EP} \uparrow + I_{EN}$$

$$\downarrow I_B = I_{EN} (\text{const.}) + I_R \downarrow - I_{C0} (\text{const.})$$

$$\uparrow I_C = I_{CP} \uparrow + I_{C0} (\text{const.})$$

(1). Doping levels

$$N_E \gg N_B$$

$$N_B > N_C$$

(2). W_B should be narrower (Recombination losses reduce)

$$I_E = I_B + I_C$$

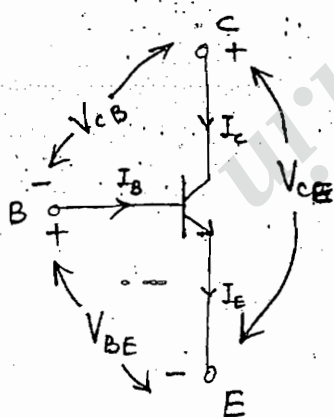
$$I_E \approx I_C$$

(3). (Area)_{Collector} > (Area)_{emitter} > (Area)_{Base}
 (70%) (25%) (5%)

Becoz heat generation is more in collector region due to R_B , Hence, area has to be more for collector.

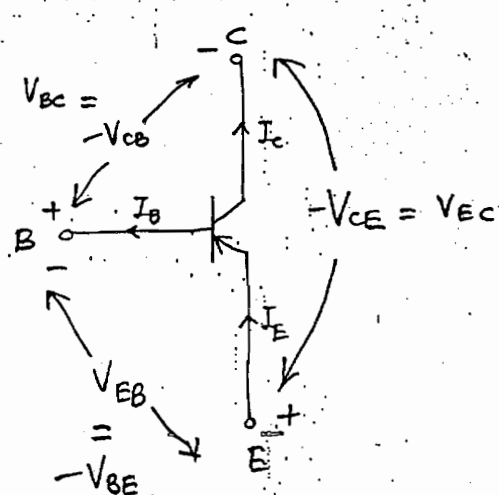
Symbols & Notations of BJT:

"npn"

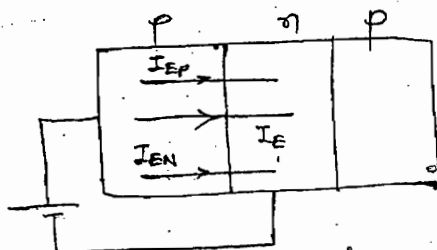


$$V_{CB} + V_{BE} = V_{CE} \text{ always}$$

"pnp"

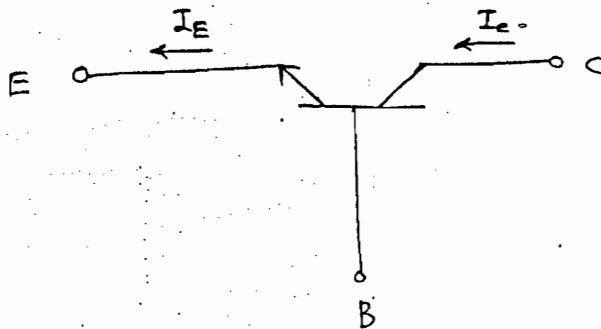


(Testing of symbol pnp)

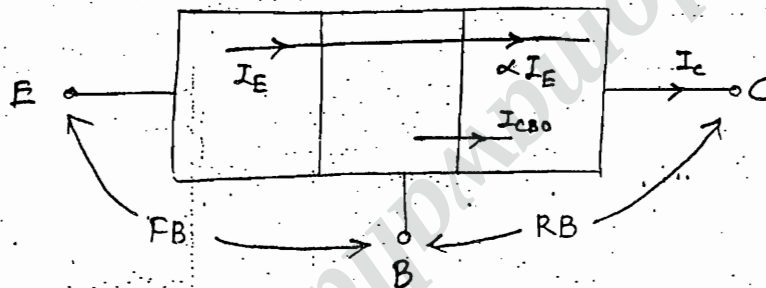


BJT configuration:

Common Base (CB):



$$\alpha = \frac{I_C}{I_E} ; \alpha \rightarrow (0.90 \text{ to } 0.99)$$



I_{CBO} → Collector to Base leakage current when emitter is open.

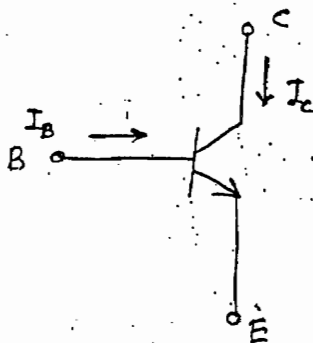
$$I_C = \alpha I_E + I_{CBO}$$

$$I_C = \alpha (I_B + I_C) + I_{CBO}$$

$$I_C (1 - \alpha) = \alpha I_B + I_{CBO}$$

$$I_C = \left(\frac{\alpha}{1 - \alpha} \right) I_B + \left(\frac{1}{1 - \alpha} \right) I_{CBO}$$

Common Emitter (CE):



$$\beta = \frac{I_C}{I_B} ; \beta \rightarrow (20 \text{ to } 500)$$

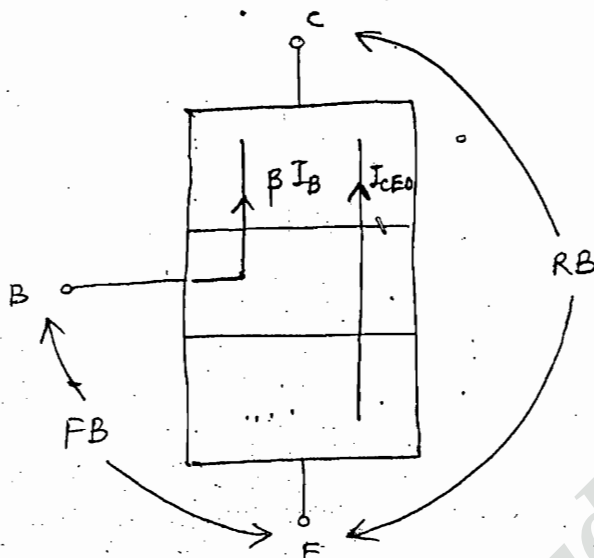
$$\beta = \frac{I_C}{I_E - I_C}$$

$$\beta = \frac{I_C / I_E}{1 - I_C / I_E} = \frac{\alpha}{(1 - \alpha)}$$

$$\therefore \beta = \frac{\alpha}{1-\alpha}$$

$$\alpha = \frac{\beta}{1+\beta}$$

and $\frac{1}{1-\alpha} = 1 + \beta$



$I_{CE0} \rightarrow$ Collector to emitter leakage current when base is open.

$$I_c = \beta I_B + I_{CE0}$$

Relation b/w I_{CB0} and I_{CE0} :

CB: $I_c = \left(\frac{\alpha}{1-\alpha}\right) I_B + \left(\frac{1}{1-\alpha}\right) I_{CB0}$

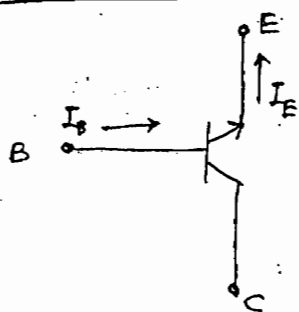
$$I_c = \beta I_B + (1+\beta) I_{CB0} \quad \text{--- (1)}$$

CE: $I_c = \beta I_B + I_{CE0} \quad \text{--- (1')}$

On comparing, $I_{CE0} = (1+\beta) I_{CB0}$

$$I_{CE0} = \left(\frac{1}{1-\alpha}\right) I_{CB0}$$

Common Collector (CC):



$$\gamma = \frac{I_E}{I_B} ; \gamma \rightarrow (20 \text{ to } 500)$$

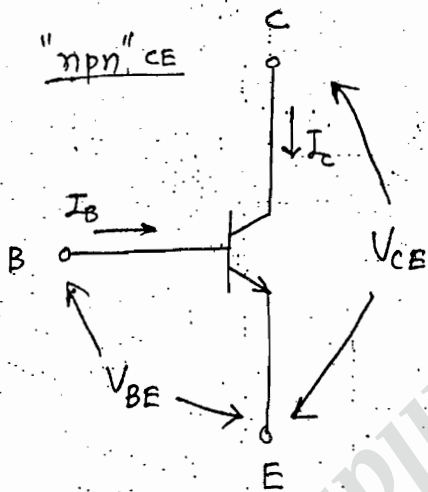
$$\gamma = \frac{I_E}{I_E - I_C}$$

$$\gamma = \frac{1}{1-\alpha} = 1 + \beta$$

$$\gamma \approx \beta$$

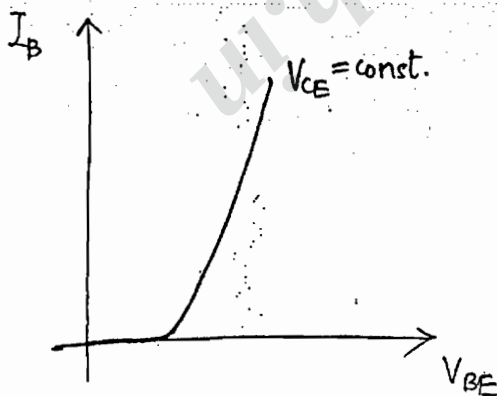
* BJT operating Regions:

Regions	I_E	I_C	Applications
(1) Active Region	FB	RB	Amplifiers
(2) Saturation Region	FB	FB	"on" } switching "OFF" }
(3) Cut-off Region	RB	RB	
(4) Reverse active Region	RB	FB	X

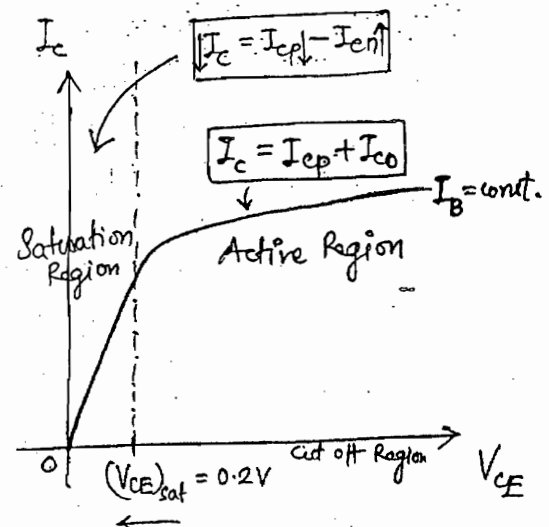


i/p parameters $\rightarrow V_{BE}, I_B$

o/p parameters $\rightarrow V_{CE}, I_C$

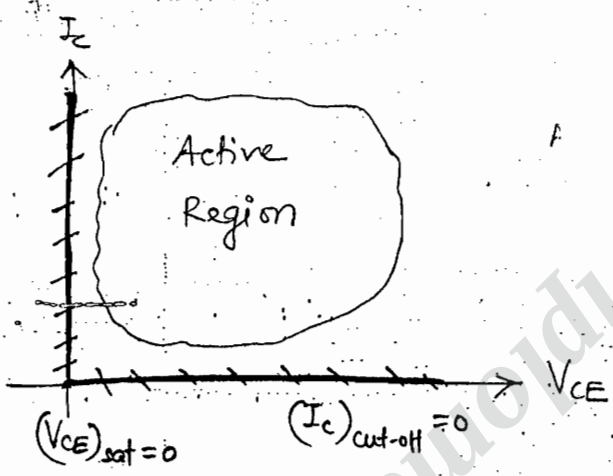
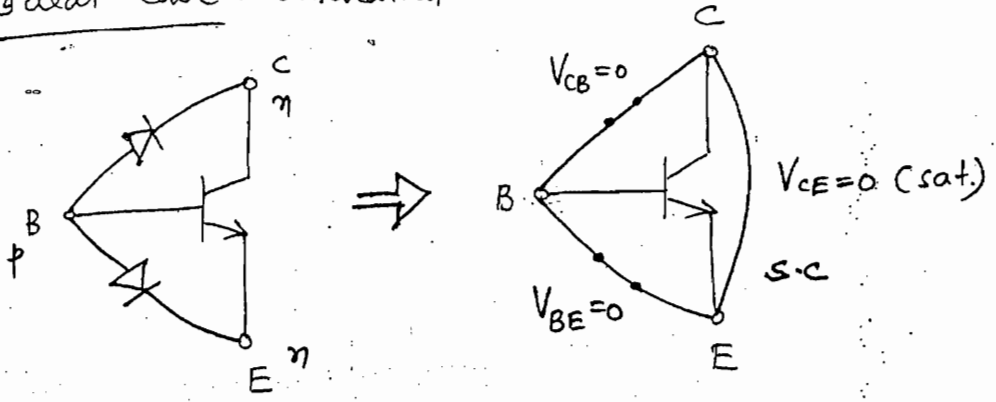


(i/p characteristics shows the diode characteristics and it is of no use, it does not represent BJT.)

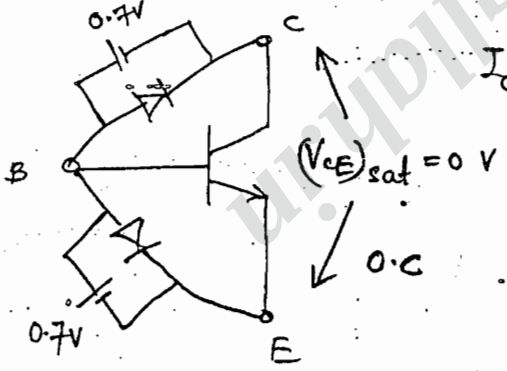


(o/p characteristics are used to study BJT and BJT is represented by these characteristics).

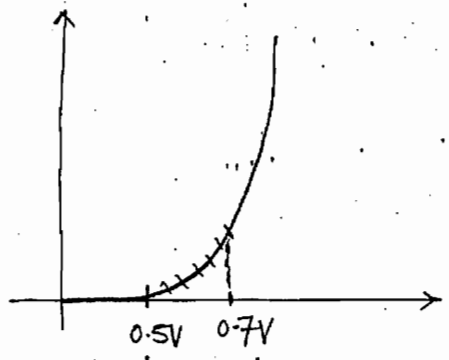
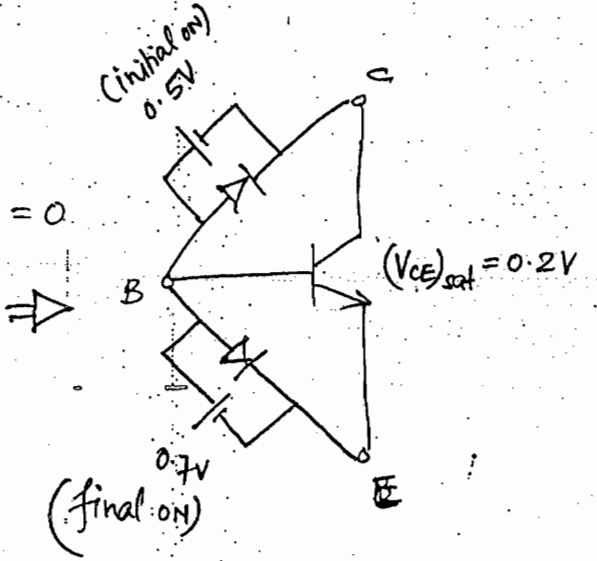
Ideal case: saturation



Practical case:

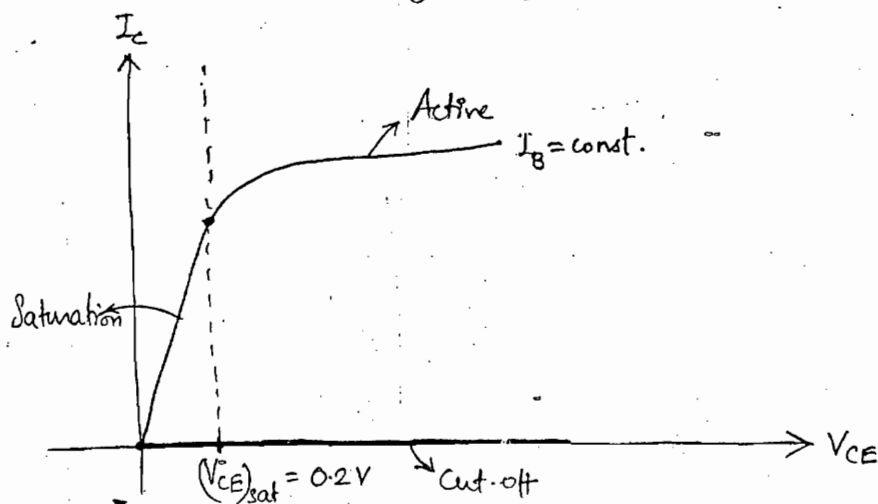
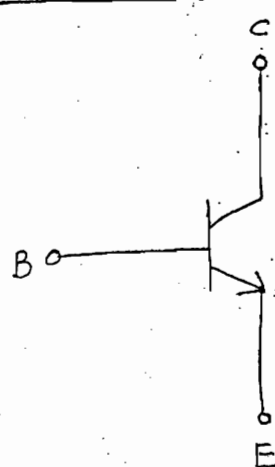


$$I_C = I_{cp} - I_{cn} = 0$$

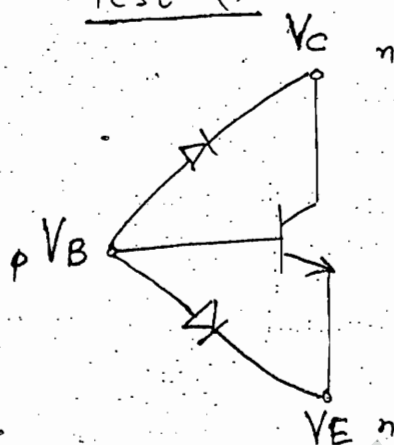


Diode $\rightarrow$ "ON" $\leftarrow$ 0.5V (Initial ON)
0.7V (Final ON)

Testings of BJT in different operating Regions:



Test-(1):



Saturation →

$$V_B > V_E$$

$$V_B > V_C$$

Cut-off →

$$V_B < V_E$$

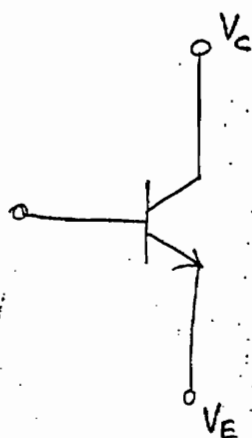
$$V_B < V_C$$

Active Region →

$$V_B > V_E$$

$$V_B < V_C$$

Test -(2):



$$V_{CE} = V_C - V_E$$

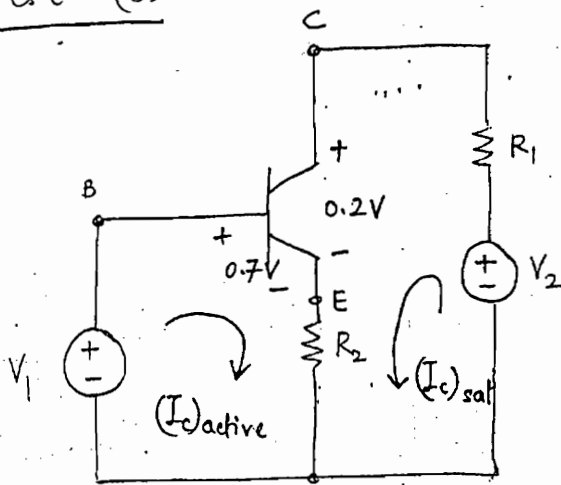
Saturation →

$$V_{CE} < (V_{CE})_{sat.}$$

Active Region →

$$V_{CE} > (V_{CE})_{sat.}$$

Test - (3) :

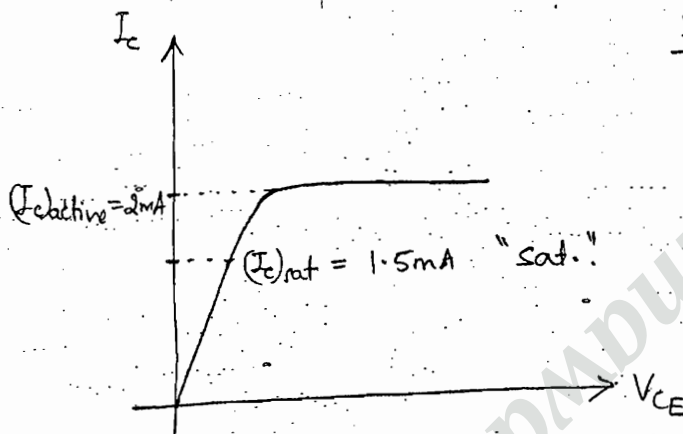


$$(V_{BE})_{\text{active}} = 0.7V \text{ (i/p loop)}$$

$$(V_{CE})_{\text{sat.}} = 0.2V \text{ (o/p loop)}$$

$$(I_c)_{\text{active}} = \frac{V_1 - 0.7}{R_2} = 2\text{mA (say)}$$

$$(I_c)_{\text{sat}} = \frac{V_2 - 0.2}{R_1 + R_2} = 1.5\text{mA (say)}$$



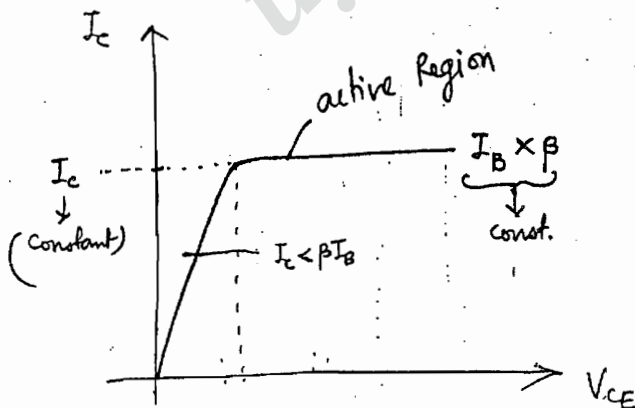
Saturation →

$$(I_c)_{\text{sat}} < (I_c)_{\text{active}}$$

Active Region →

$$(I_c)_{\text{sat}} > (I_c)_{\text{active}}$$

Test - (4) :



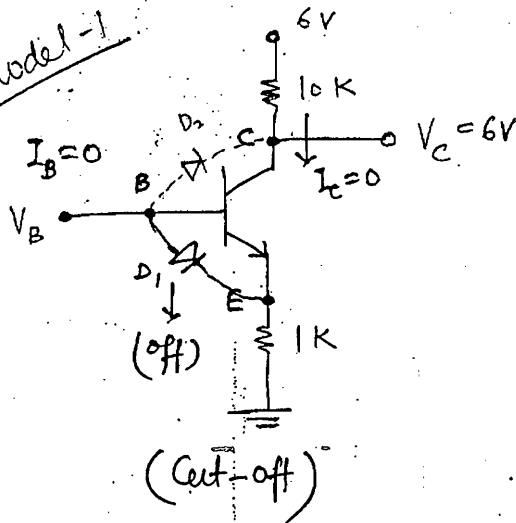
Saturation →

$$I_c < \beta I_B$$

Active Region →

$$I_c = \beta I_B \text{ (const.)}$$

Q2. Model-1



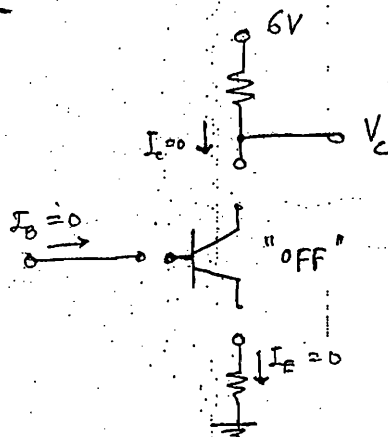
$$\text{For } V_B = 0V$$

 $D_1 \rightarrow (\text{off})$
 $D_2 \rightarrow (\text{off})$ automatically.

There is no chance of reverse active.

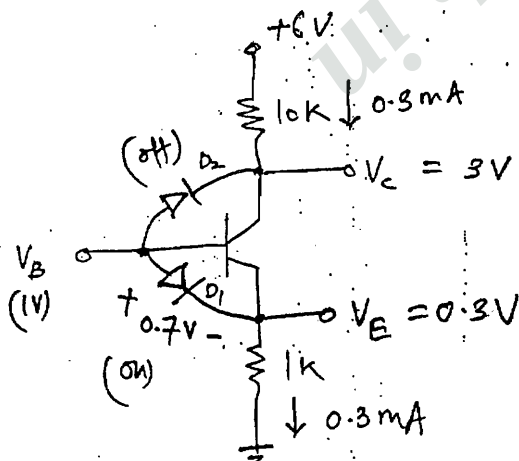
Hence, there is a cut-off.

$$I_B = I_C = I_E = 0.$$



$$V_C = 6V \text{ (directly)}$$

$$\text{For } V_B = 1V$$



Active Region:

 $D_1: (\text{on})$
 $D_2: (\text{off})$

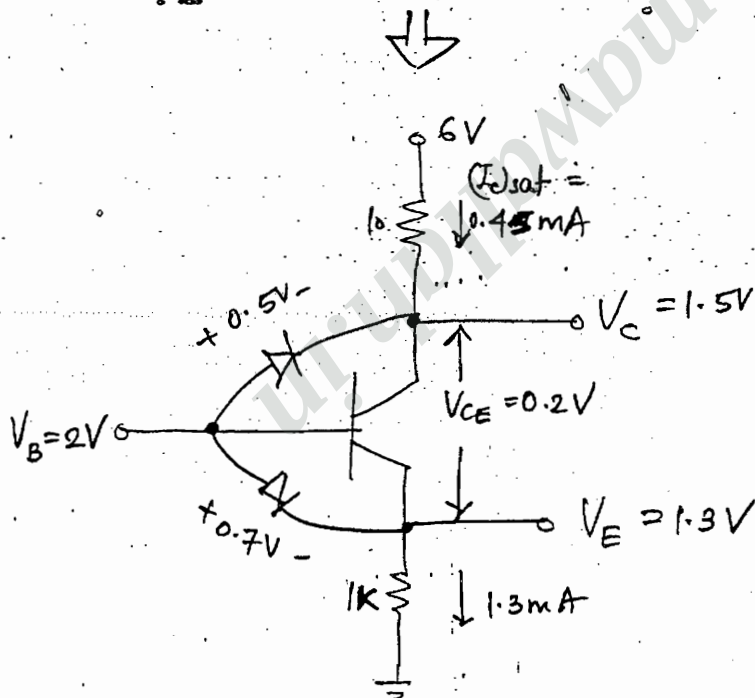
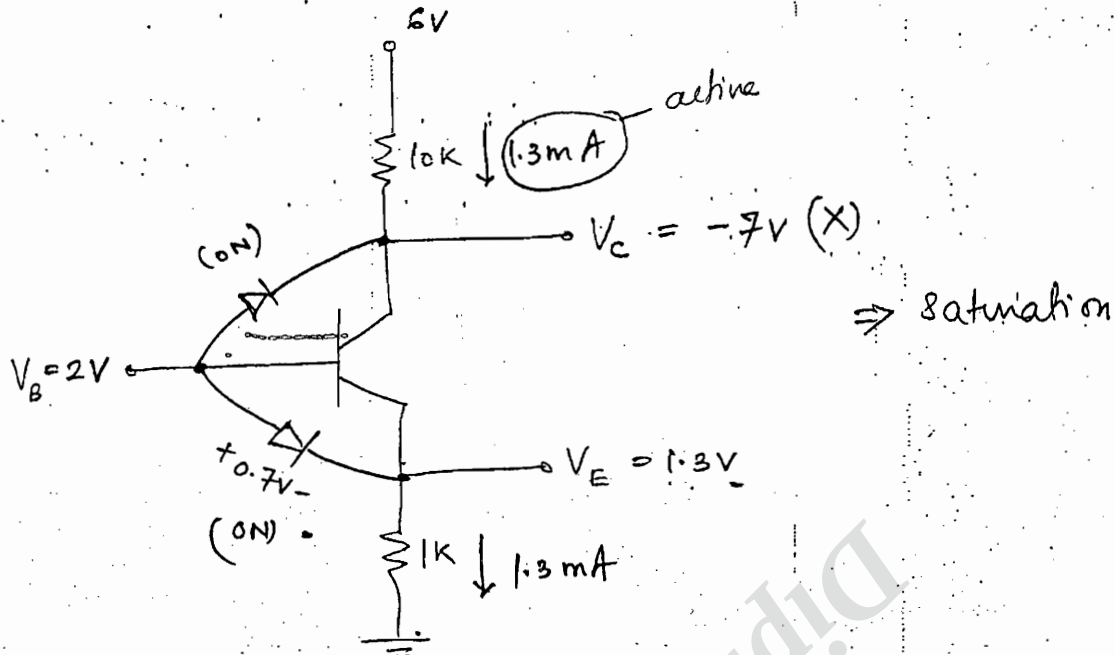
$$\begin{aligned} V_C &= 6 - 0.3mA \times 10K \\ &= 6 - 3 \\ &= 3 \text{ volts.} \end{aligned}$$

$$I_E = \frac{V_E}{1K} = \frac{0.3}{1K} = 0.3mA$$

$$I_C \approx I_E = 0.3mA$$

For $V_B = 2V$

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Saturation

$$V_C = 2 - 0.5$$

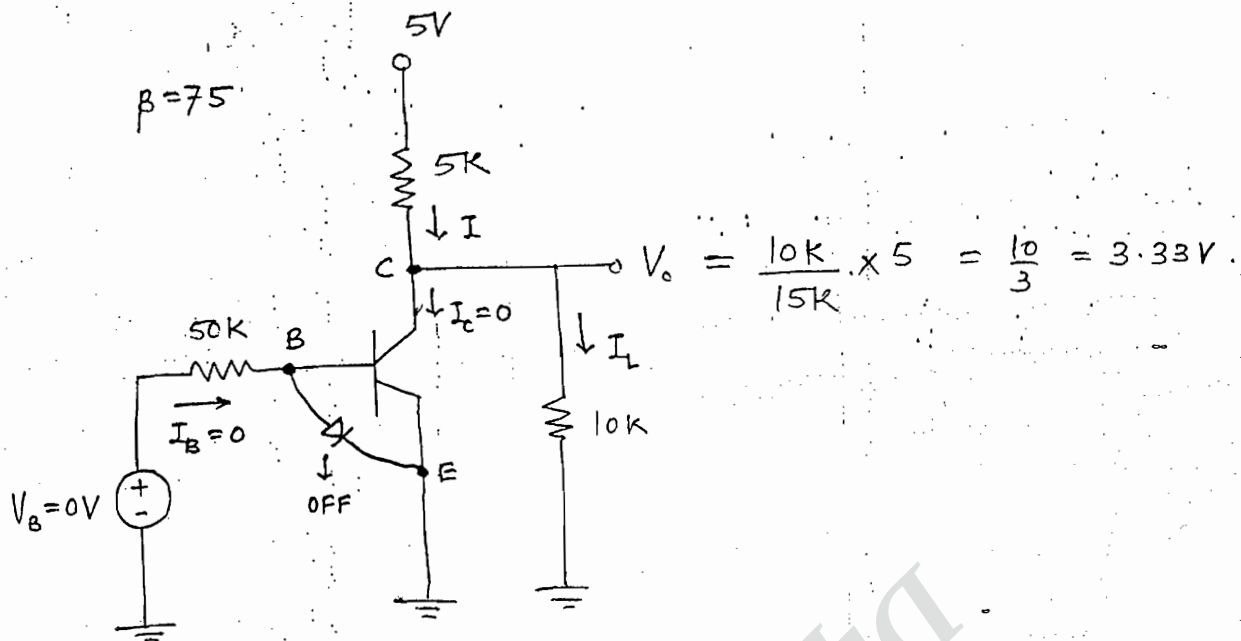
$$= 1.5V$$

$$(I_{E,sat}) = \frac{6 - 1.5}{10k}$$

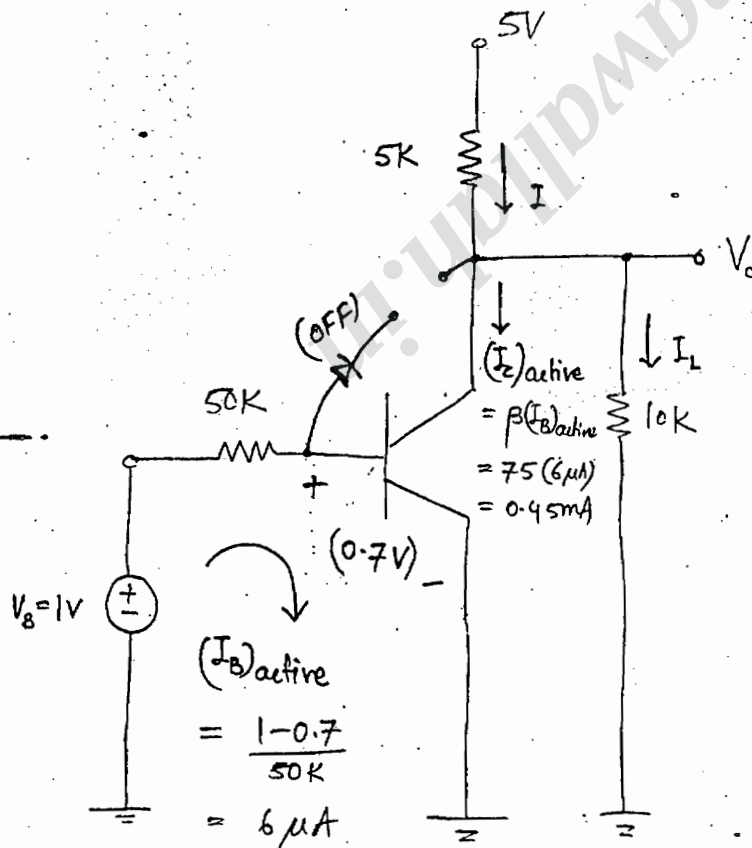
$$= 0.45mA$$

Model - (2):

for $V_B = 0V$: (cut off)

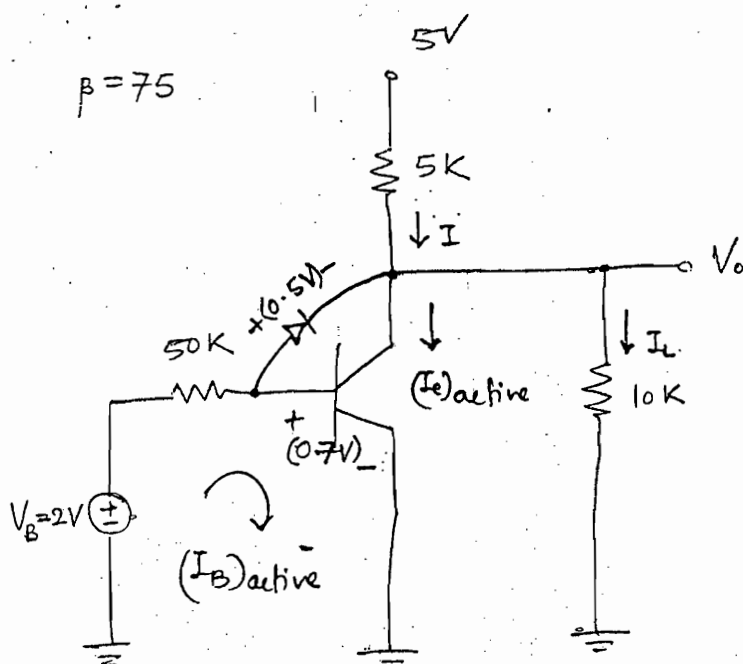


for $V_B = 1V$: (Active)



for $V_B = 2V$

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V_o is ?

- (a) 3V (b) 5V
 (c) 0.2V (d) 1.3V
 (practical dkt)

Ideally diode $\rightarrow$ SC.
 then $(V_{CE})_{sat} = 0V$.

$$(I_B)_{active} = \frac{2 - 0.7}{50K} = 26 \mu A$$

$$(I_C)_{active} = \beta \cdot (I_B)_{active} = 75 \times 26 \mu A = 1.95 mA$$

Now,
$$\frac{5 - V_o}{5K} = 1.95 + \frac{V_o}{10K}$$

$$1mA - \frac{V_o}{5} = 1.95mA + \frac{V_o}{10K} \Rightarrow \boxed{V_o = -ve}$$

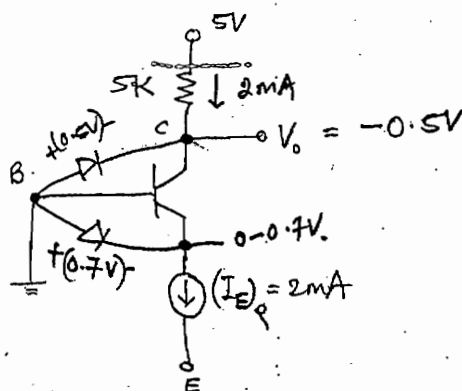
↓
saturation

$$\Rightarrow V_o = -3.16V$$

Saturation $\rightarrow V_o = (V_{CE})_{sat} = 0V$ (ideally)

$V_o = (V_{CE})_{sat} = 0.2V$ (practically)

Q1.



$$V_o = 5 - 5K \times 2mA$$

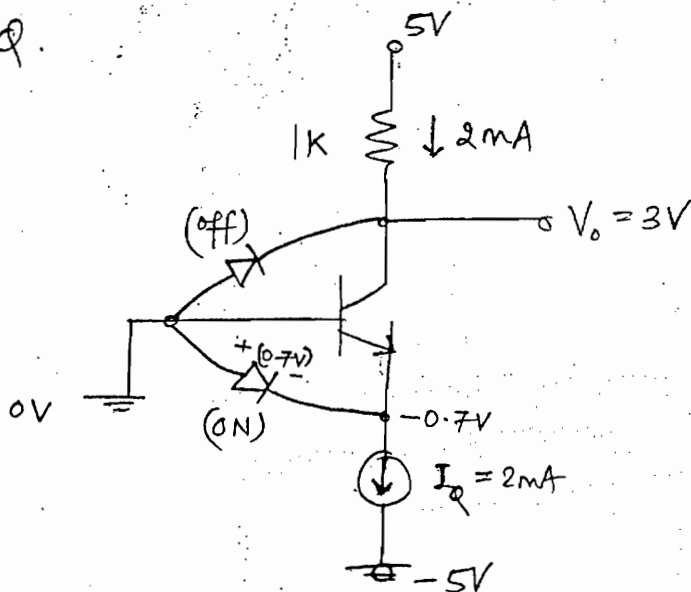
$$V_o = 5 - 10$$

$$V_o = -5V (X)$$

$$\boxed{V_o = -ve} \rightarrow \text{saturation}$$

$$\therefore V_o = -0.5V$$

Q.



$$V_o = 5 - 2\text{mA} \times 1\text{K}$$

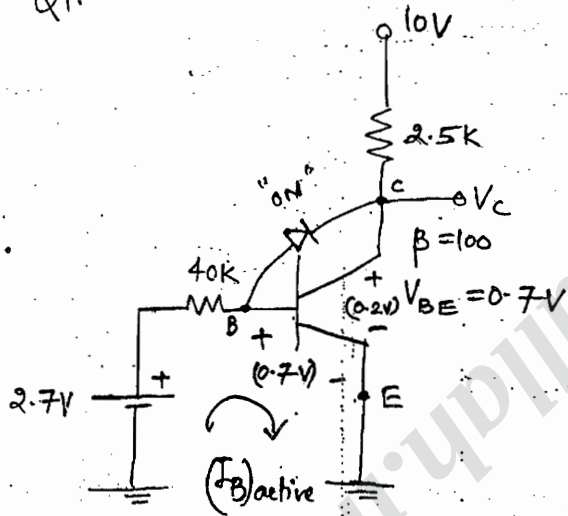
$$= 5 - 2$$

$$= 3\text{V}$$

$$V_o = 3\text{V}$$

active Region.

Q1.



$$(I_B)_{\text{active}} = \frac{2.7 - 0.7}{40\text{K}}$$

$$= \frac{1}{20} \text{mA}$$

$$(I_c)_{\text{active}} = \beta (I_B)_{\text{active}}$$

$$= 100 \times \frac{1}{20}$$

$$= 5\text{mA}$$

$$V_c = 10 - 2.5\text{K} \times 5\text{mA}$$

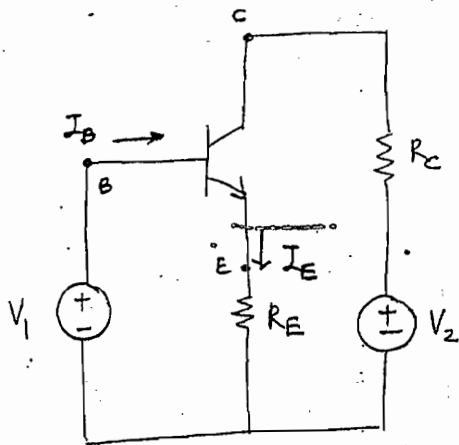
$$V_c = -2.5\text{V}$$

Saturation

$$\therefore V_{CE} = 0.2\text{V}$$

$$\frac{10 - 0.2}{2.5\text{K}} = (I_c)_{\text{sat.}}$$

$$(I_c)_{\text{sat.}} = \frac{9.8}{2.5\text{K}} = 3.9\text{mA}$$



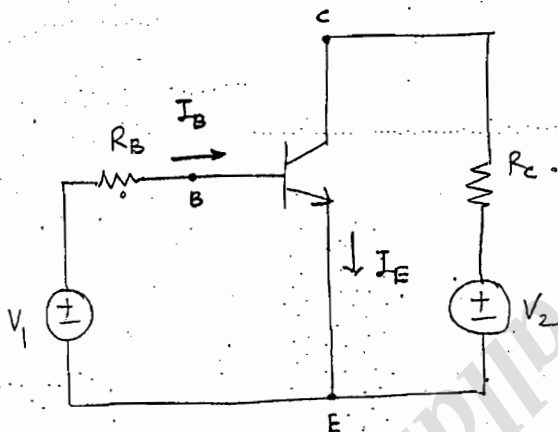
$$\begin{aligned} I_E &= I_B + I_C \\ &= I_B + \beta I_B \\ &= (1 + \beta) I_B \end{aligned}$$

$$V_1 = V_{BE} + I_E R_E$$

(or)

$$V_1 = V_{BE} + I_B R_E (1 + \beta)$$

Model (2) :



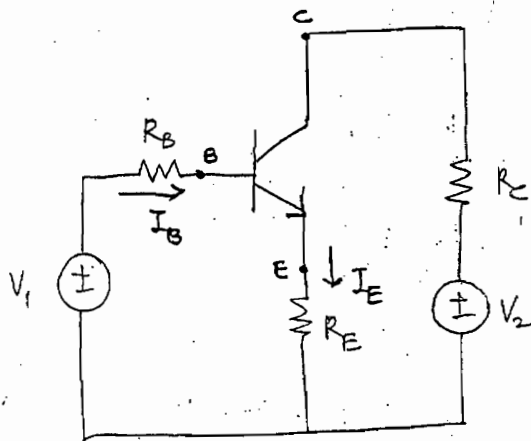
$$V_1 = I_B R_B + V_{BE}$$

(or)

$$V_1 = \frac{I_E R_B}{(1 + \beta)} + V_{BE}$$

$$= I_E \left(\frac{R_B}{1 + \beta} \right) + V_{BE}$$

Model (3) :

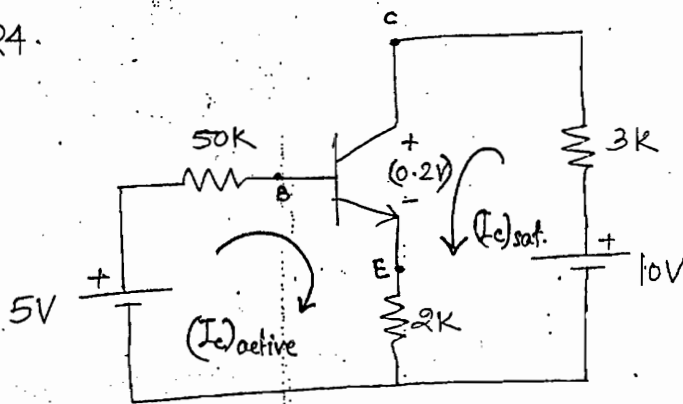


$$V_1 = I_B R_B + V_{BE} + I_B R_E (1 + \beta)$$

(or)

$$V_1 = I_E \left(\frac{R_B}{1 + \beta} \right) + V_{BE} + I_E R_E$$

Q4.

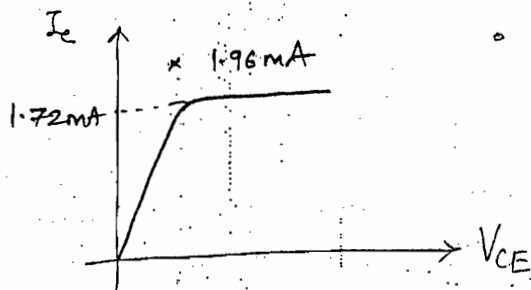


$$(I_c)_{\text{active}} = \frac{5 - 0.7}{\left(\frac{50K}{1+100}\right) + 2K}$$

$$= 1.72 \text{ mA}$$

$$(I_c)_{\text{sat}} = \frac{10 - 0.2}{5K}$$

$$= 1.96 \text{ mA}$$

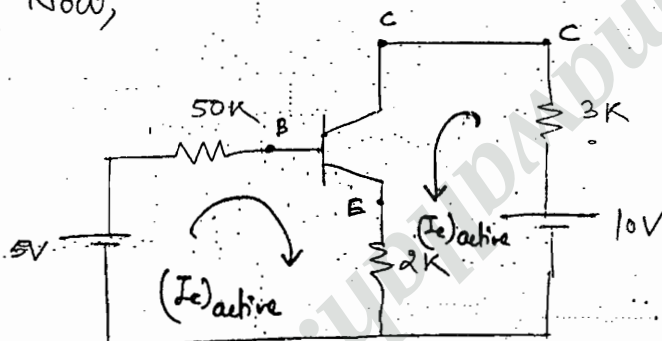


$$(I_c)_{\text{sat}} < 1.72 \text{ mA}$$

$$\text{But } (I_c)_{\text{sat}} > 1.72 \text{ mA}$$

$\therefore$ Active Region.

Now,



$$V_E = 1.72 \text{ mA} \times 2K$$

$$= 3.44 \text{ V}$$

$$V_C = 10 - 3K \times 1.72 \text{ mA}$$

$$= 4.84 \text{ V}$$

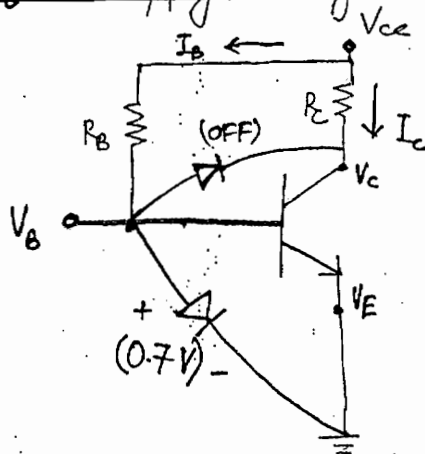
$$V_{CE} = V_C - V_E$$

$$= 4.84 - 3.44$$

$$= 1.4 \text{ V}$$

Single supply design in BJT to operate in Active Region:

(1).

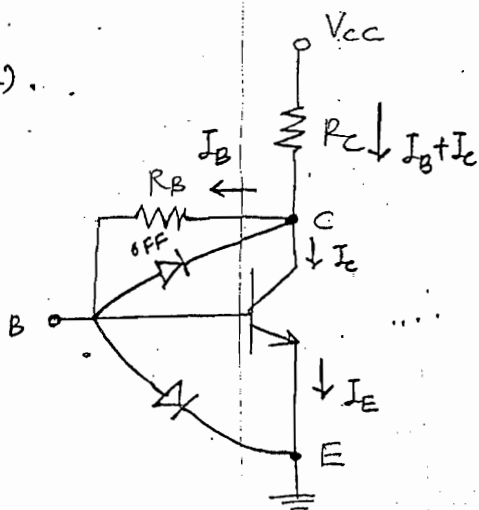


$$R_B > R_C$$

$$V_B < V_C \rightarrow \text{D Coff}$$

Active Region.

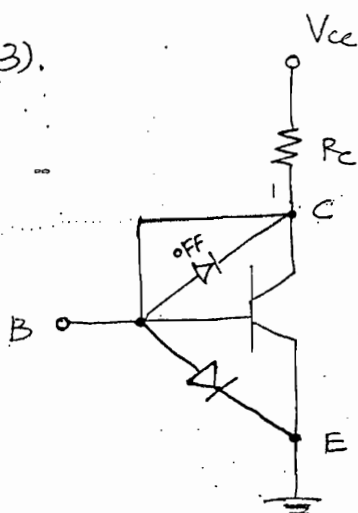
(2)



$$V_B < V_C$$

$D \rightarrow \text{OFF}$ (Active Region)

(3)



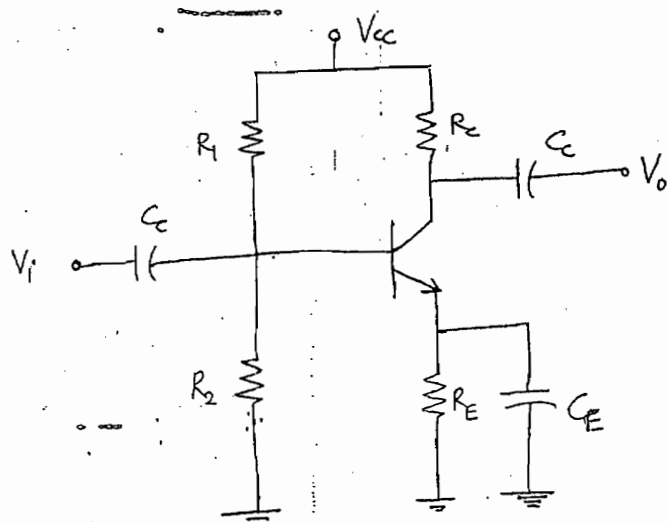
$$V_B = V_C ; D \rightarrow \text{OFF} \text{ (Active Region)}$$

(Active Region)

BJT as a Diode.

DC load line and operating point (Q):

CE Amplifier:

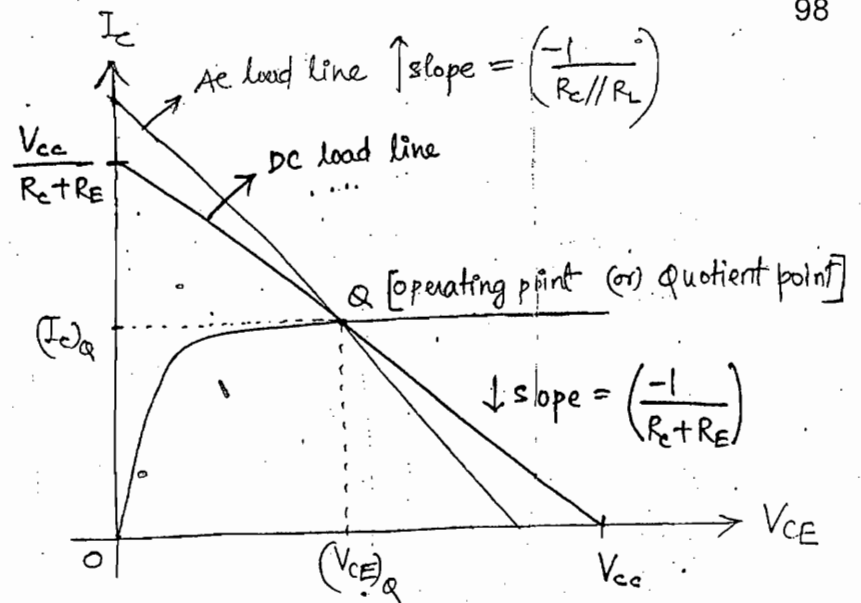
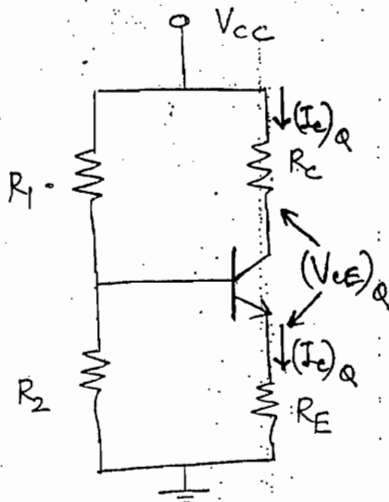


(practical ckt)

DC Analysis:

(1). AC should be grounded.

$$(2). X_C \propto \frac{1}{f} \rightarrow 0 \text{ (DC)} \rightarrow \infty \text{ (C} \rightarrow \infty \text{)}$$



O/p loop:

$$V_{CC} = I_C (R_C + R_E) + V_{CE}$$

$$I_C = \frac{-1}{(R_C + R_E)} V_{CE} + \frac{V_{CC}}{R_C + R_E}$$

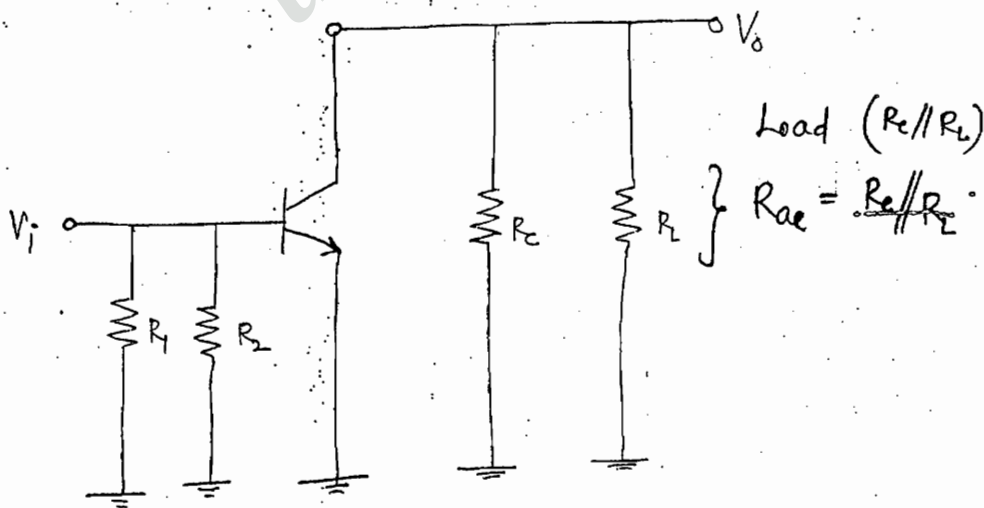
(Quotient $\rightarrow$ greek word
 $\downarrow$
 meaning $\rightarrow$ Inactive)
 Load $(R_C + R_E)$

AC Analysis:

AC load line —

(1). ~~DC~~ should be grounded.

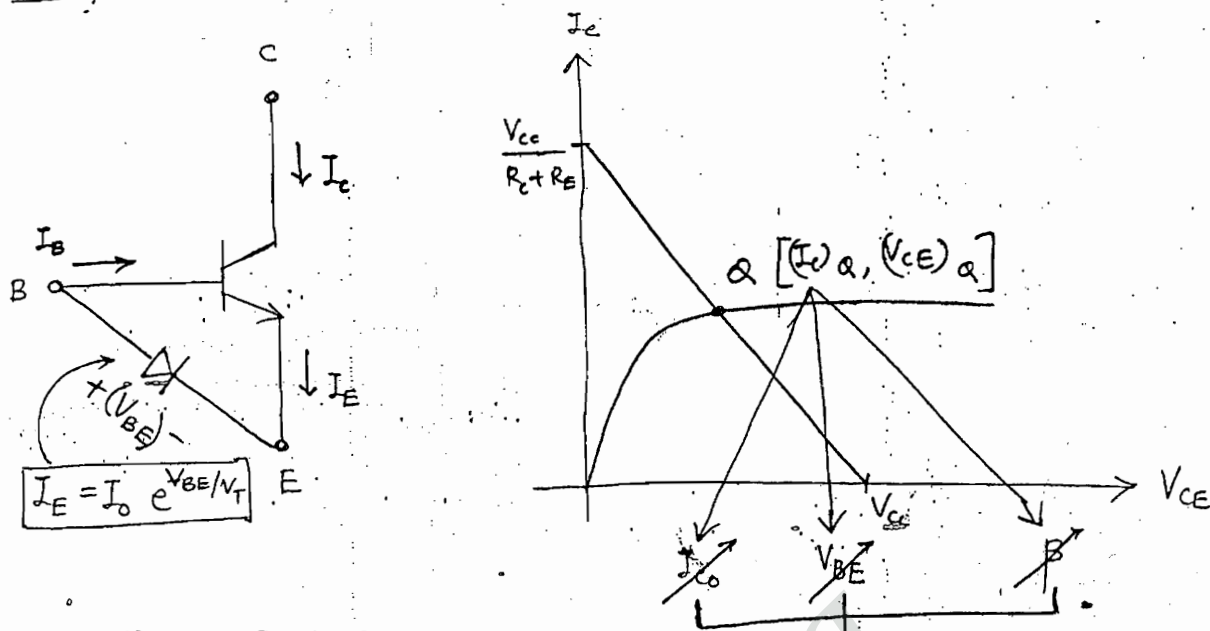
(2). $X_C \propto \frac{1}{f \rightarrow \infty} \rightarrow 0$ ($C \rightarrow SC$)



Range: $R_C, R_E, R_L \rightarrow (0 \text{ to } 10k\Omega)$

Temperature dependence on BJT parameters:

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$$(1) \cdot I_C = \beta I_B + (1 + \beta) I_{CBO}$$

$$(2) \cdot I_C = I_0 \cdot e^{V_{BE}/V_T}$$

$$(3) \cdot I_C = \beta I_B$$

"Biasing" is a ckt or technique which makes the the Q point stable w.r.t temperature variations.

I_{C0} Vs Temperature:

' I_{C0} ' increases by 7% for every $^{\circ}C$ rise in temp.

' I_{C0} ' doubles for every $10^{\circ}C$ rise in temp.

V_{BE} Vs Temp.:

$$\frac{dV_{BE}}{dT} = -2.5mV/^{\circ}C$$

$$T \uparrow, \quad V_{BE} \downarrow, \quad I_C \downarrow$$

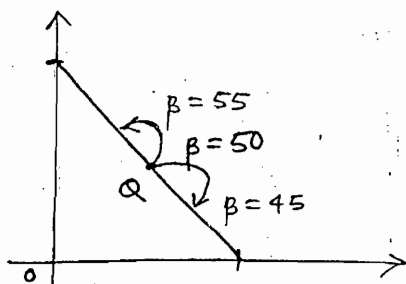
β Vs temp.:

$$\beta = \frac{I_C}{I_B} \quad ; \quad T \uparrow, \quad I_{C0} \uparrow, \quad I_C \uparrow, \quad \beta \uparrow$$

neglected

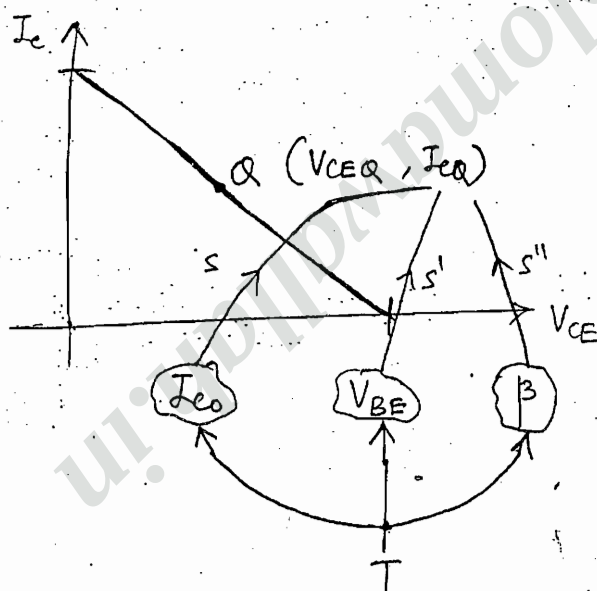
β replacement problem:

β value changes and hence point Q changes.
Replacement of transistor shift the Q point because of mismatching.



Stability factor (S):

"It is a measure of variation of Q point w.r.t. temp."



$$S \propto S' \propto S''$$

$$S = \left. \frac{\partial I_c}{\partial I_{co}} \right|_{V_{BE} \text{ \& } \beta = \text{const.}}$$

$$S' = \left. \frac{\partial I_c}{\partial V_{BE}} \right|_{I_{co} \text{ \& } \beta = \text{const.}}$$

$$S'' = \left. \frac{\partial I_c}{\partial \beta} \right|_{I_{co} \text{ \& } V_{BE} = \text{const.}}$$

Conclusion:

- (1) The most dominating parameter w.r.t temp. is I_{co} .
- (2) The stability factor ideally it should be zero, practically it should be min^m as possible

$$I_c = \beta I_B + (1+\beta) I_{co}$$

Differentiate w.r.t. I_c . We get,

$$1 = \beta \frac{\partial I_B}{\partial I_c} + (1+\beta) \cdot \frac{\partial I_{co}}{\partial I_c}$$

$$1 = \beta \frac{\partial I_B}{\partial I_c} + \frac{(1+\beta)}{S}$$

$$S = \frac{1+\beta}{1 - \beta \frac{\partial I_B}{\partial I_c}}$$

$$S=0 \rightarrow S=1$$

(ideal) (practical)

Compensation

Biasing ckts:

Stabilization techniques

Fixed Bias
($S \approx 101$)

Collector to base
Bias
($S \approx 45$)

Voltage divider
Bias
($S \approx 1$)
(or)
Self Bias
(or)
Emitter Bias
(or)
Universal Bias

Compensation techniques

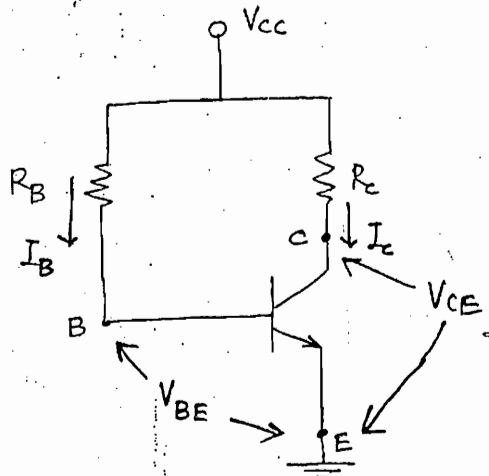
Diode

Sensistor

Thermistor

temp. sensitive devices

Fixed Bias :



i/p loop :

$$V_{cc} = I_B R_B + V_{BE}$$

o/p loop :

$$V_{cc} = I_C R_C + V_{CE}$$

$$I_B = \frac{V_{cc} - V_{BE}}{R_B}$$

If $V_{cc} \gg V_{BE}$

$$I_B \approx \frac{V_{cc}}{R_B} \rightarrow \text{fixed}$$

$$I_C = \beta I_B$$

↓ ↓
fixed fixed

$$I_B \approx \frac{V_{cc}}{R_B}$$

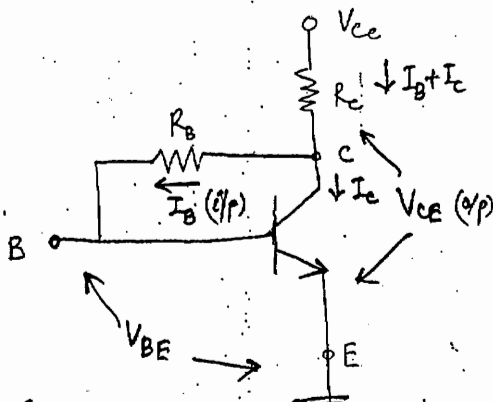
$$\frac{\partial I_B}{\partial I_C} = 0$$

$$S = \frac{1 + \beta}{1 - \beta \frac{\partial I_B}{\partial I_C}}$$

$$S = 1 + \beta$$

↓
Stability is not there for the ckt

Collector to Base Bias : (-ve feedback control s/s)



i/p loop :

$$V_{cc} = (I_B + I_C) R_C + I_B R_B + V_{BE}$$

$$V_{cc} = I_B (R_B + R_C) + I_C R_C + V_{BE}$$

$$I_B = \frac{V_{cc} - I_C R_C - V_{BE}}{R_B + R_C}$$

(-ve shunt feedback ckt)

o/p loop:

$$V_{ce} = (I_B + I_c) R_c + V_{CE}$$

$$V_{cc} - I_c R_c = V_{CE} + I_B R_c$$

$$\therefore I_B = \frac{V_{CE} + I_B R_c - V_{BE}}{R_B + R_c}$$

↓
i/p

Stabilization:

$$T \uparrow, I_B \uparrow, I_c \uparrow, (I_B + I_c) R_c \uparrow, V_{CE} \downarrow$$

$$I_B \downarrow, I_c \downarrow$$

Now, $V_{cc} = I_B (R_B + R_c) + I_c R_c + V_{BE}$

Differentiate w.r.t I_c : We get,

$$0 = (R_B + R_c) \frac{\partial I_B}{\partial I_c} + R_c + 0$$

$$\frac{\partial I_B}{\partial I_c} = \frac{-R_c}{(R_B + R_c)}$$

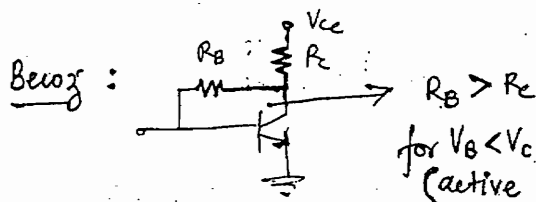
$$S = \frac{1 + \beta}{1 - \beta \frac{\partial I_B}{\partial I_c}}$$

$$S = \frac{1 + \beta}{1 + \beta \frac{R_c}{R_B + R_c}} = \frac{(1 + \beta)(R_B + R_c)}{R_B + (1 + \beta)R_c} = \frac{(1 + \beta) \left(1 + \frac{R_B}{R_c}\right)}{\left(1 + \beta + \frac{R_B}{R_c}\right)}$$

$$S = \frac{1 + \beta}{1 + \beta \left(\frac{R_B}{R_c} + 1\right)}$$

$$\rightarrow \approx 1 \text{ if } \frac{R_B}{R_c} = 0 \begin{cases} R_B = 0 \\ R_c = \infty \end{cases}$$

$$\therefore \boxed{R_B/R_c \ll 1} \rightarrow \text{failure condition}$$



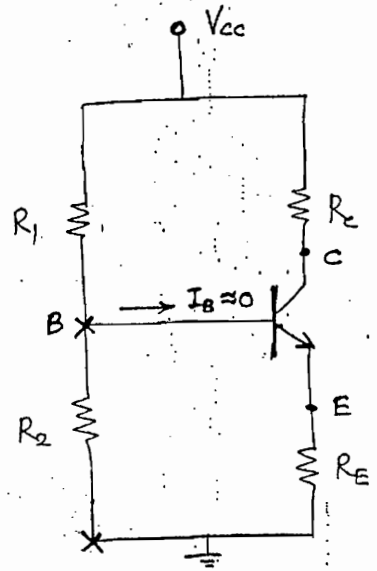
$$R_B > R_c$$

$$\text{for } V_B < V_c$$

(active Region requirement condition)

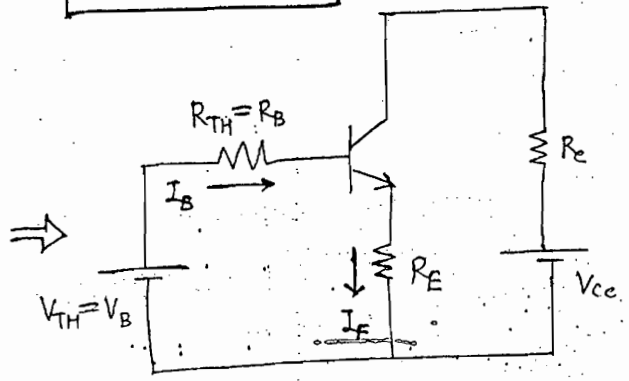
$$\boxed{\frac{R_B}{R_c} \gg 1} \text{ — ckt condition}$$

Voltage Divider Bias-



$$V_B = \frac{V_{CC} \times R_2}{R_1 + R_2}$$

$$R_B = R_1 // R_2$$



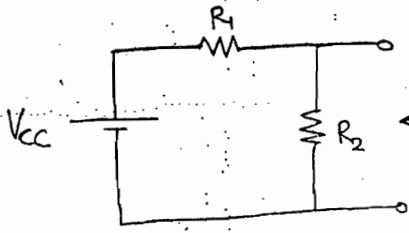
Q/p loop :

$$V_B = I_B \cdot R_B + V_{BE} + (I_B + I_C) R_E$$

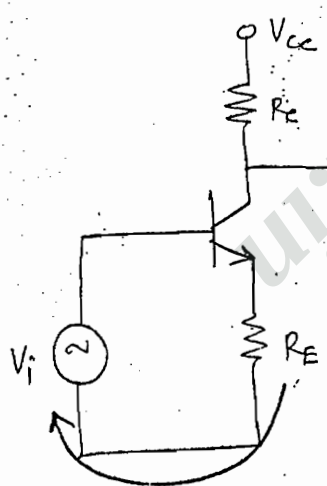
$$V_B = I_B (R_B + R_E) + I_C R_E + V_{BE}$$

$$0 = (R_B + R_E) \frac{\partial I_B}{\partial I_C} + R_E = 0$$

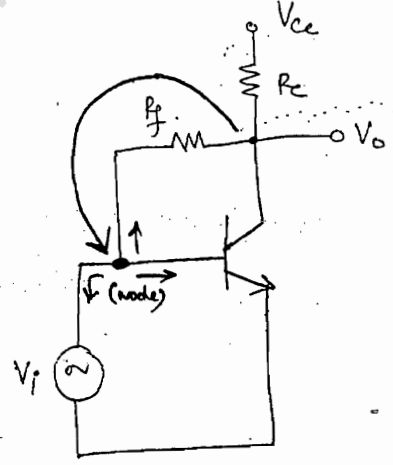
$$\frac{\partial I_B}{\partial I_C} = \frac{-R_E}{R_B + R_E}$$



V_{TH}, R_{TH}
(V_B, R_B)



Series feedback



Shunt feedback

$$S = \frac{1 + \beta}{1 - \beta \frac{\partial I_B}{\partial I_C}} = \frac{1 + \beta}{1 + \beta \left(\frac{R_E}{R_B + R_E} \right)} = \frac{1 + \beta}{1 + \beta \left(\frac{1}{1 + \frac{R_B}{R_E}} \right)}$$

$$\text{If } R_B / R_E \ll 1 \Rightarrow S \rightarrow 1$$

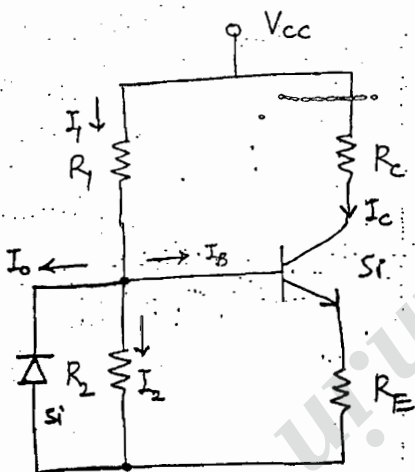
Design conditions:

105

- (1). If $\frac{R_B}{R_E} \ll 1$, then $S \rightarrow \text{unity}$.
- (2). To make R_B less, take a condition $R_1 > R_2$.
- (3). If R_E increases, -ve fb in the ckt increases which will affect the s/g gain in AC analysis. To solve this problem always keep a capacitor (bypass capacitor C_E) in shunt to R_E .

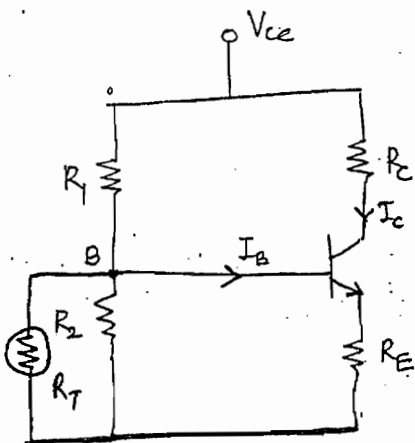
Compensation techniques -

Compensation through Diode -



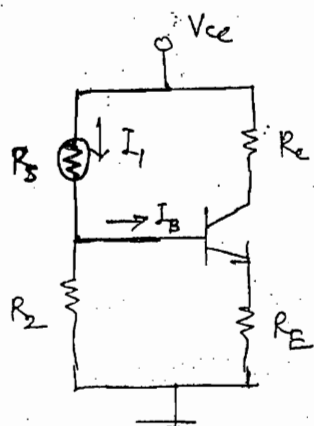
$$T \uparrow \quad I_{CO} \uparrow \quad \underbrace{I_O \uparrow \quad I_B \downarrow \quad I_C \downarrow}_{\text{Compensation}}$$

Compensation through thermistor:



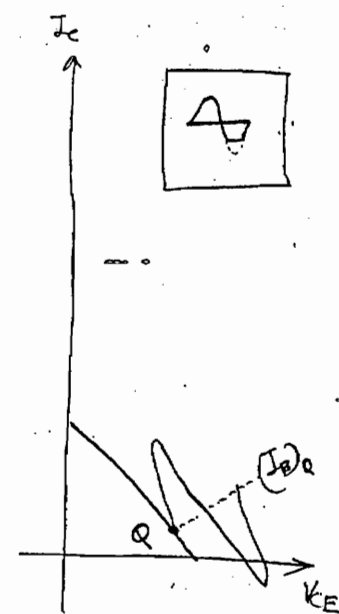
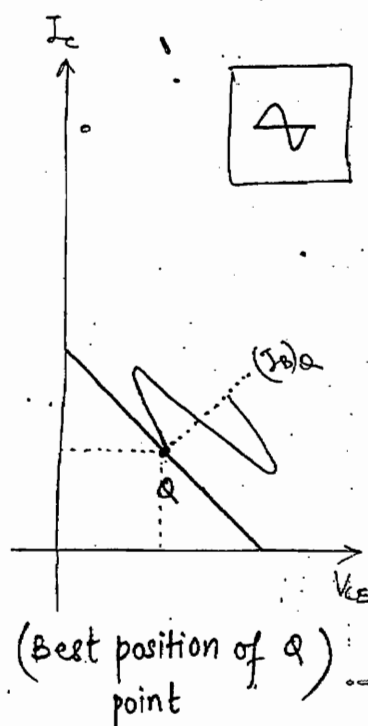
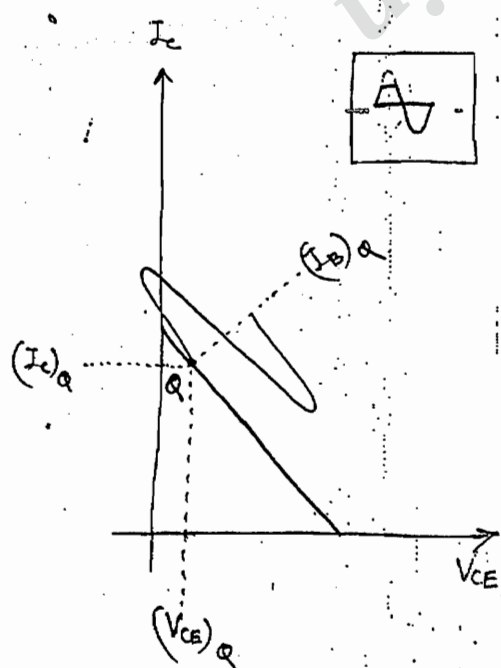
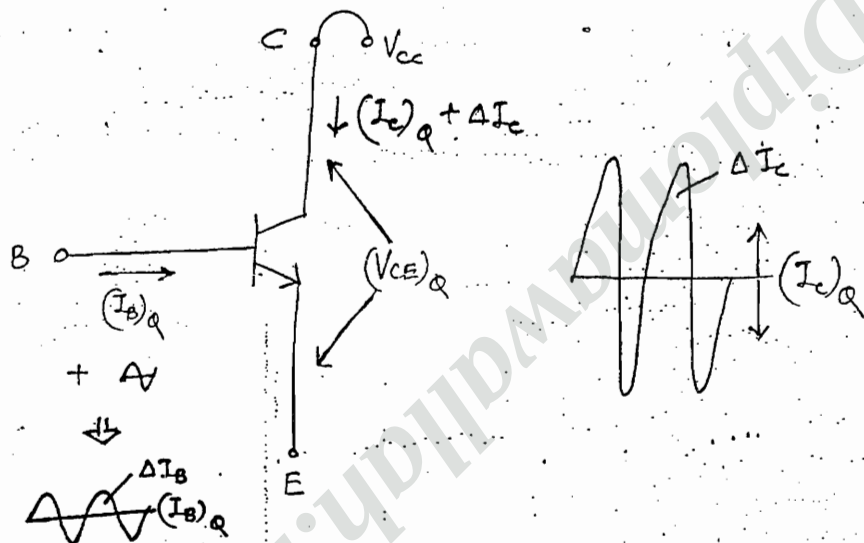
$$T \uparrow, I_{CO} \uparrow, \quad \underbrace{R_T \downarrow \quad V_B \downarrow \quad I_B \downarrow \quad I_C \downarrow}_{\text{compensation}}$$

Compensation through senseistor :



$$T \uparrow \quad I_{CQ} \uparrow \quad \underbrace{R_s \uparrow \quad I_1 \downarrow \quad I_B \downarrow \quad I_C \downarrow}_{\text{Compensation}}$$

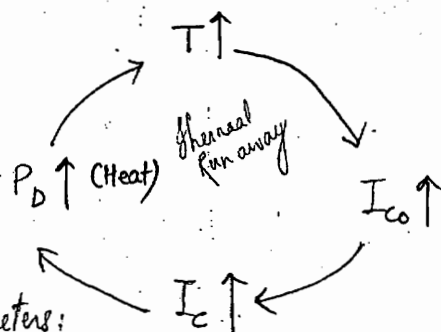
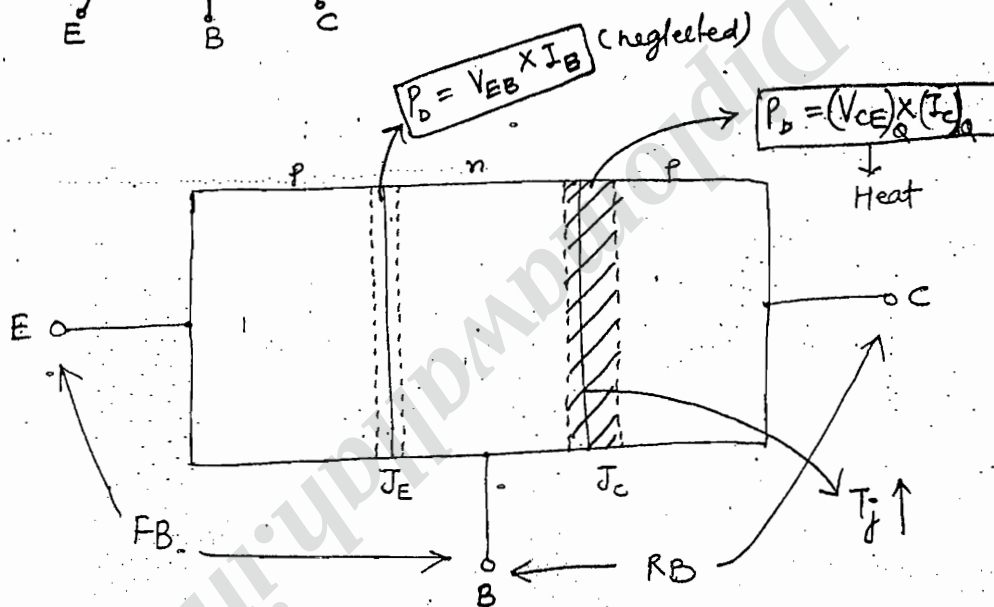
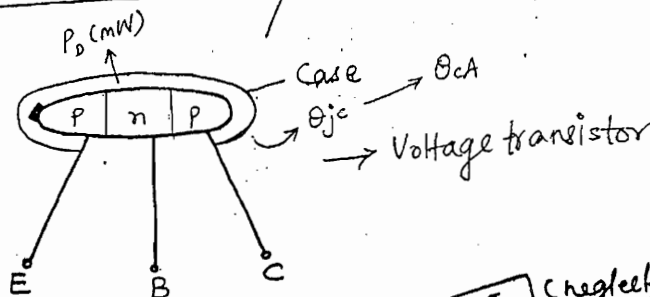
Position of Q point on DC load line :



The amplifier analysis of the Q point should be design at the centre of DC load line.

$$(V_{CE})_Q = \frac{V_{CC}}{2}$$

Thermal Runaway:

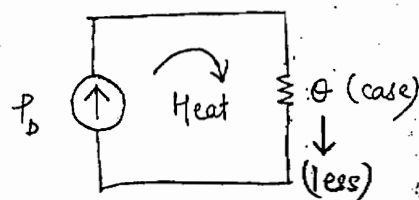


Thermal parameters:

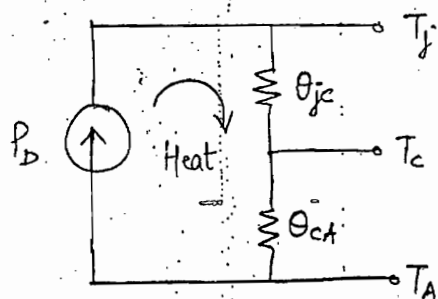
$$T_J - T_A \propto P_D$$

$$T_J - T_A = \theta \cdot P_D$$

$$V = RI \text{ (analogue)}$$



$$\theta = \text{thermal resistance} = \frac{T_J - T_A}{P_D} \text{ } ^\circ\text{C/mW}$$

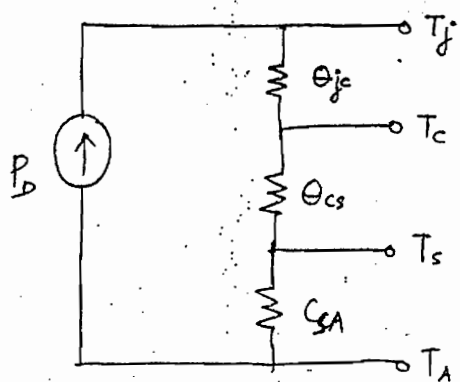
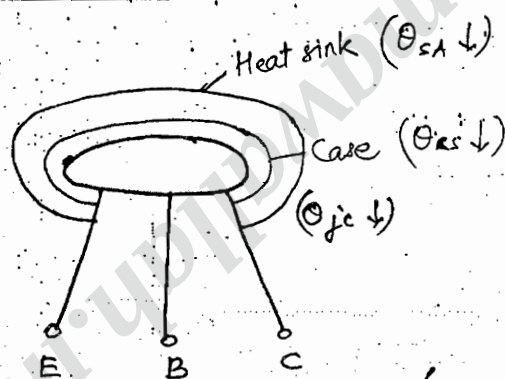


Problem with cases:

θ_{jc} can be less, but θ_{ca} is not much less, it is somewhat more. ($\theta_{jc} \downarrow$ & $\theta_{ca} \uparrow$)

Heat must be delivered to atmosphere simultaneously by absorbing from device.

Power transistor:



for power transistor,

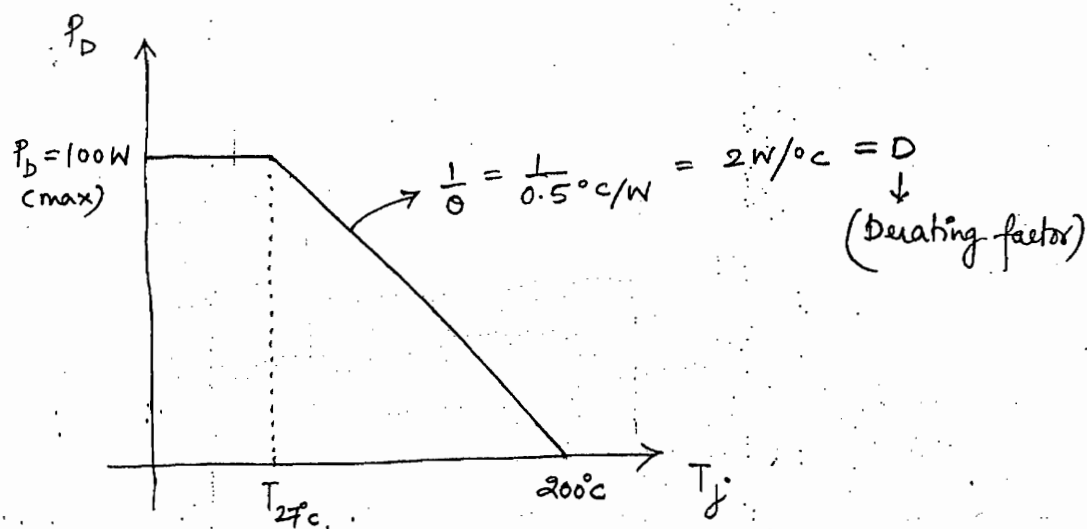
$$P_D = \frac{T_j - T_A}{\theta_{jc} + \theta_{cs} + \theta_{sa}}$$

for voltage transistor,

$$P_D = \frac{T_j - T_A}{\theta_{jc} + \theta_{ca}}$$

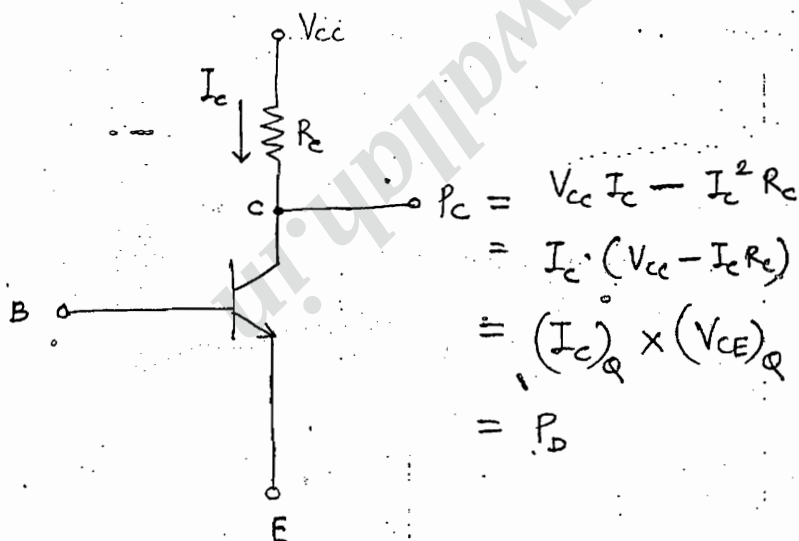
* Heat sink can deliver heat as fast as case absorbs from the device. Hence, there is no problem for total $\theta_{JA} = \theta_{jc} + \theta_{cs} + \theta_{sa}$ to be less.

Specification graph for BJT -



Condition for thermal stability:

$$\frac{\partial P_C}{\partial T_j} < \frac{\partial P_D(\text{steady state})}{\partial T_j}$$



"The heat generated at the collector jxn should not exceed the heat dissipated at the collector jxn."
 (max)

$$T_j - T_A = \theta \cdot P_D$$

Differentiate w.r.t T_j . We get,

$$1-0 = 0 \cdot \frac{\partial P_D}{\partial T_j}$$

$$\frac{\partial P_D}{\partial T_j} = \frac{1}{0} = \text{slope}$$

$$\therefore \frac{\partial P_C}{\partial T_j} < \frac{1}{0}$$

$$\frac{\partial P_C}{\partial I_C} \cdot \frac{\partial I_C}{\partial I_{C0}} \cdot \frac{\partial I_{C0}}{\partial T_j} = \frac{\partial P_C}{\partial T_j}$$

Now, $P_C = V_{CC} I_C - I_C^2 R_C$

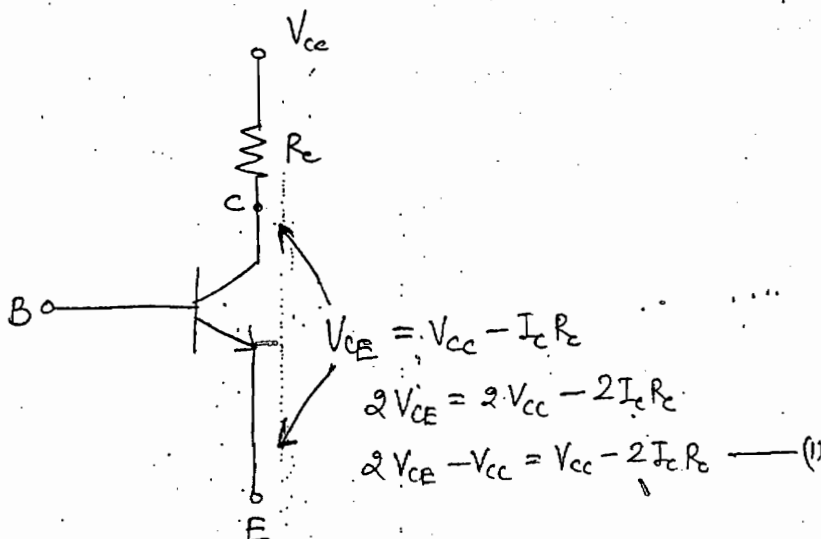
Differentiate w.r.t. I_C

$$(1) \quad \frac{\partial P_C}{\partial I_C} = V_{CC} - 2 I_C R_C$$

$$(2) \quad \frac{\partial I_C}{\partial I_{C0}} = S$$

$$(3) \quad \frac{\partial I_{C0}}{\partial T_j} = 0.07 I_{C0}$$

$$\therefore (V_{CC} - 2 I_C R_C) \times (S) \times (0.07 I_{C0}) < \frac{1}{0}$$



$$\therefore (2 V_{CE} - V_{CC}) (S) \cdot (0.07 I_{C0}) < \frac{1}{0}$$

Case - (1) : $(V_{CE})_Q = 0$

$$\underbrace{(-V_{CE}) \cdot (S) \cdot (0.07 I_{CQ})}_{(-ve)} < \underbrace{\frac{1}{\theta}}_{(+ve)}$$

$\therefore$ thermally stable if $(V_{CE})_Q = 0$

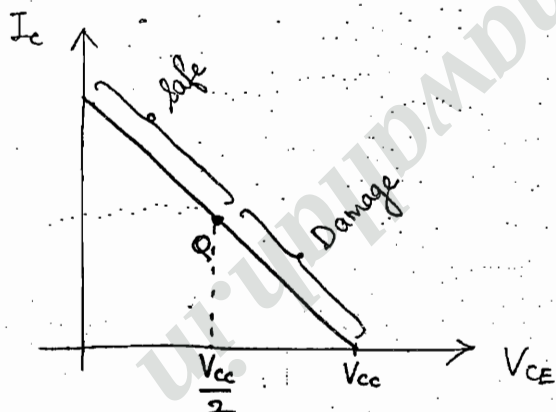
Case - (2) : $(V_{CE})_Q = V_{CC}/2$

$0 < \frac{1}{\theta} \Rightarrow$ thermally stable if $(V_{CE})_Q \leq V_{CC}/2$

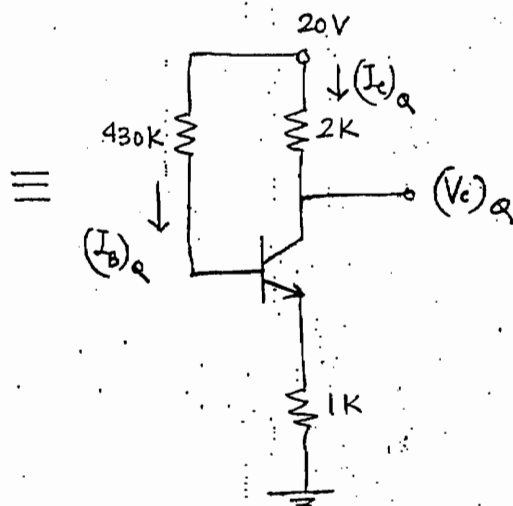
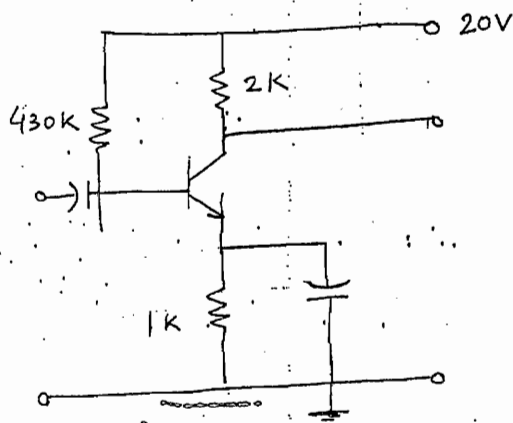
Case - (3) : $V_{CE} > V_{CC}/2$

there are chances of thermal runaway.

Thermal Runaway if $(V_{CE})_Q > V_{CC}/2$



Q9.

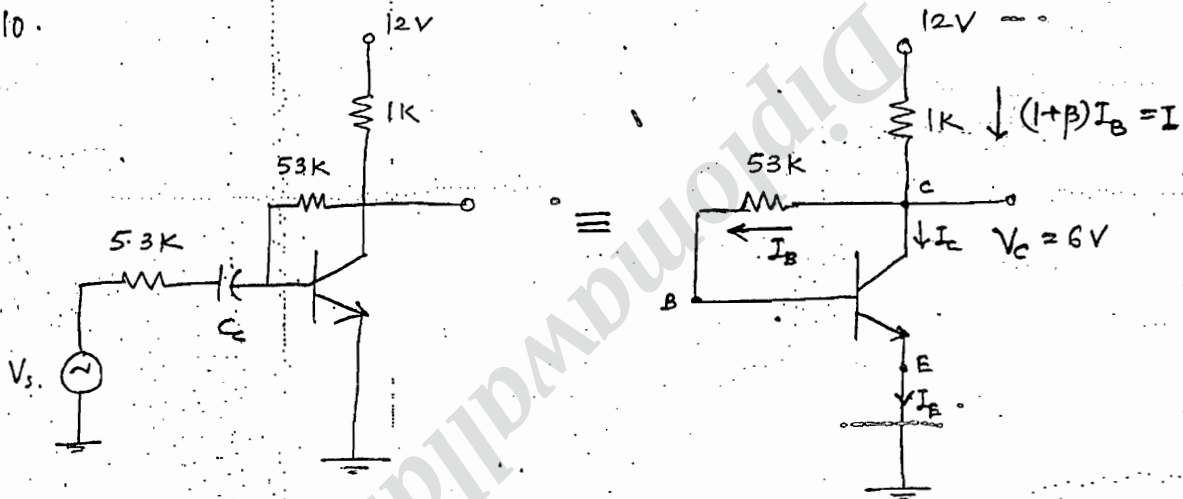


$$(I_B)_Q = \frac{20 - 0.7}{430K + (1+50)1K} = 40 \mu A$$

$$\begin{aligned}(I_C)_Q &= \beta (I_B)_Q \\ &= 50 \times 40 \mu A \\ &= 2 \text{ mA}\end{aligned}$$

$$\begin{aligned}(V_C)_Q &= V_{CC} - (I_C)_Q \cdot R_C \\ &= 20 - 2 \text{ mA} \times 9K \\ &= 16 \text{ V}\end{aligned}$$

Q10.



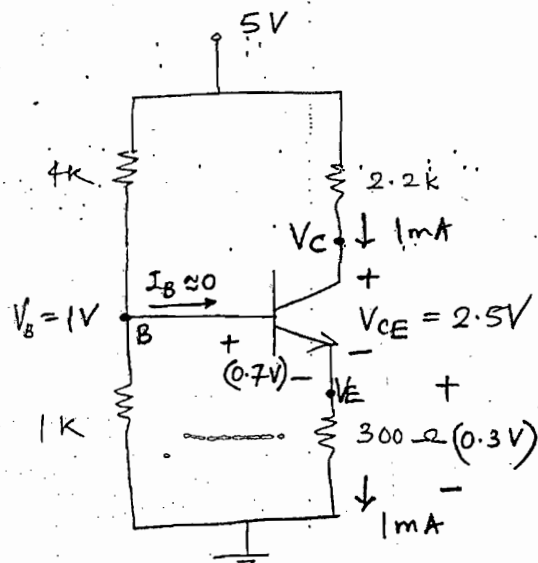
$$(I_B)_Q = \frac{12 - 0.7}{53K + (1+60)1K} = 99 \mu A$$

$$\begin{aligned}(I_C)_Q &= \beta (I_B)_Q \\ &= 60 \times 99 \mu A \\ &= 5.94 \text{ mA}\end{aligned}$$

$$I_E = (1+60) \times 99 \mu A = 6 \text{ mA}$$

$$\begin{aligned}V_C &= 12 - 6 \text{ mA} \times 1K \\ &= 6 \text{ V}\end{aligned}$$

Q11.



$$V_{CE} = 5 - 2.2K \times 1mA - 0.3K \times 1mA$$

$$= 5 - 2.2 - 0.3$$

$$= 2.5V$$

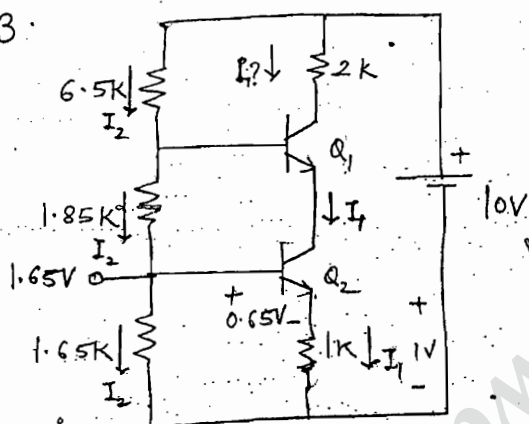
$$V_B = \frac{1}{5} \times 5 = 1V$$

$$V_E = 1 - 0.7 = 0.3V$$

$$I_E = \frac{0.3}{300} = 1mA$$

$$\therefore I_C = 1mA$$

Q3.



$\beta \rightarrow$ High value (given)

$$\beta = \frac{I_C}{I_B}$$

Assume $I_B \approx 0$

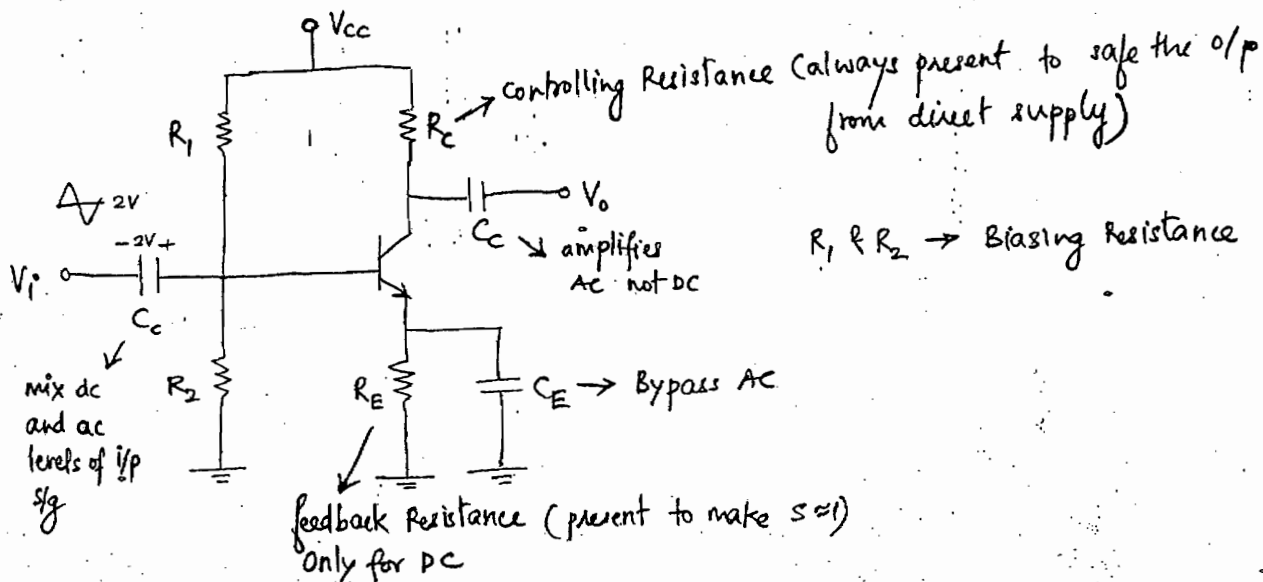
$$1.65 - 0.65 = 1V$$

$$I_E = \frac{1V}{1K} = 1mA$$

BJT Applications:

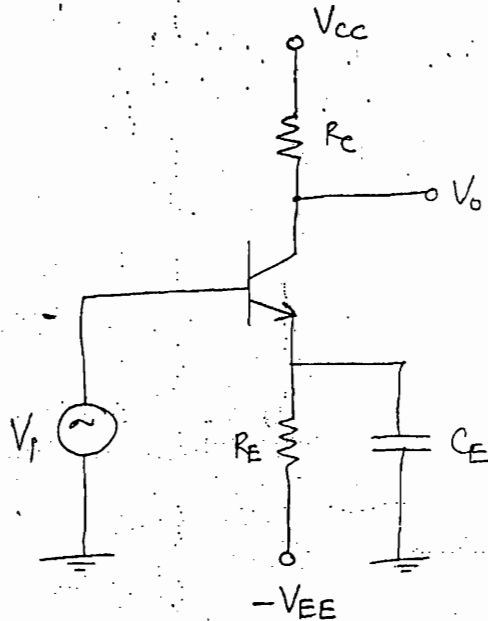
CE Amplifier:

RC coupled BJT Amplifier / AC amplifiers:



- (1). It is amplifier AC wise.
- (2). App - Voltage Amplifier

Direct coupled BJT Amplifier (DC amplifiers):

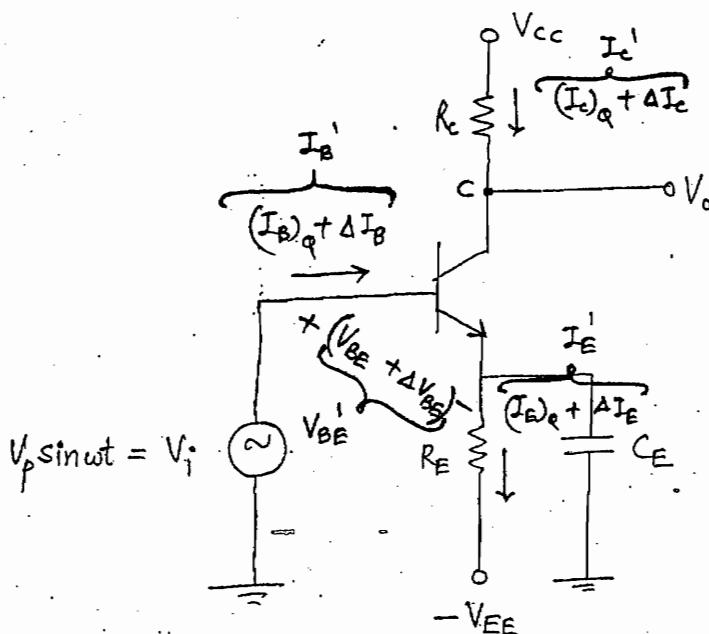


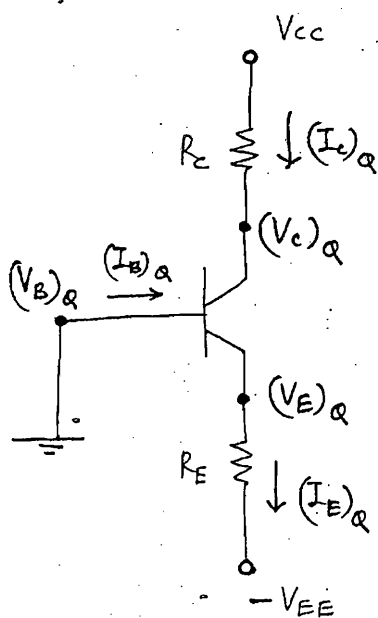
Stable Biasing

↓
Does not require biasing resistance.

- (1). App - Integrated ckt (OP-Amp)
- (2). They have high gain (very high)
- (3). It is amplifier DC wise

Analysis of CE amplifier:





$$(V_E)_Q = -V_{BE}$$

$$(V_B)_Q = 0$$

$$(V_C)_Q = V_{CC} - (I_C)_Q R_C$$

$$(I_E)_Q = \frac{V_{EE} - V_{BE}}{R_E}$$

$$(I_C)_Q = \alpha (I_E)_Q$$

$$(I_B)_Q = \frac{(I_E)_Q}{(1 + \beta)}$$

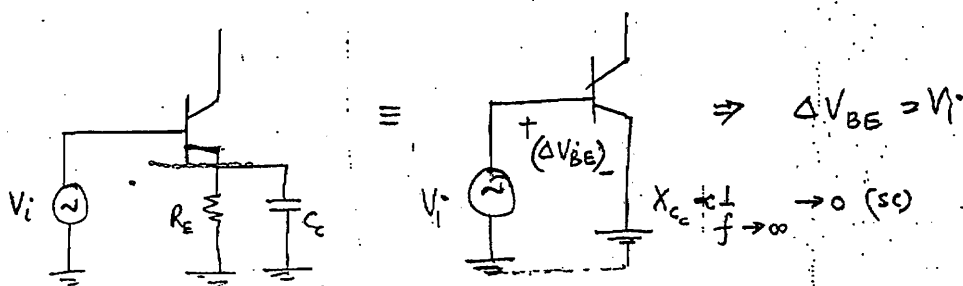
$$(I_E)_Q = I_{E0} \cdot e^{V_{BE}/V_T}$$

$$(I_E)_Q + \Delta I_E = I_{E0} \cdot e^{(V_{BE} + \Delta V_{BE})/V_T}$$

$$(I_E)_Q + \Delta I_E = I_{E0} \cdot e^{V_{BE}/V_T} \cdot e^{\Delta V_{BE}/V_T}$$

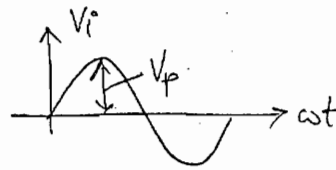
$$I_E' = (I_E)_Q \cdot e^{\Delta V_{BE}/V_T}$$

Ac analysis -



$$I_E' = (I_E)_Q \cdot e^{V_i/V_T} \rightarrow \text{Non-linear device}$$

Assume: $V_i = V_p \sin \omega t$



$$I_E' = (I_E)_Q \cdot e^{\frac{V_p \sin \omega t}{V_T}}$$

$$I_E' = (I_E)_Q \left\{ 1 + \frac{V_p \sin \omega t}{V_T} + \frac{1}{2!} \frac{V_p^2 \sin^2 \omega t}{V_T^2} + \dots \right\}$$

$$I_E' = (I_E)_Q \left\{ 1 + \frac{V_p \sin \omega t}{V_T} + \frac{V_p^2}{2 V_T^2} \frac{(1 - \cos 2\omega t)}{2} + \dots \right\}$$

$$I_E' = \underbrace{(I_E)_Q \left(1 + \frac{V_p^2}{4 V_T^2} \right)}_{\text{DC Drift (unwanted)}} + \underbrace{\frac{(I_E)_Q}{V_T} V_p \sin \omega t}_{\text{fundamental comp. (useful)}} - \underbrace{\frac{(I_E)_Q}{4 V_T^2} V_p^2 \cos 2\omega t + \dots}_{\text{Distortions (2nd Harmonic comp.) + ... etc.}}$$

$$\begin{aligned} \% 2^{\text{nd}} \text{ Harmonic distortion} &= \frac{|B_2|}{|B_1|} \\ &= \frac{(I_E)_Q \cdot V_p^2 / 4 V_T^2}{(I_E)_Q \cdot V_p / V_T} \end{aligned}$$

Harmonics are not
be cos of DC

$$\longrightarrow \frac{(I_E)_Q \cdot V_p}{V_T}$$

$$= \frac{V_p}{4 V_T}$$

$$= \frac{V_p}{100 \text{ mV}} \quad (\text{at Room temp.})$$

$$\begin{aligned} \text{s/g } \left\{ \begin{array}{l} X_1 \rightarrow 1 \text{ mV } \sin \omega t \Rightarrow \% D = 1\% \\ X_2 \rightarrow 10 \text{ mV } \sin \omega t \Rightarrow \% D = 10\% \end{array} \right. \end{aligned}$$

Harmonics are be cos of s/g

$\therefore 1 \text{ mV}$ is a good choice for Ac analysis.

Now,

if

$$V_p \lll 4 V_T$$

$$V_p / 4 V_T \lll 1 \quad (\text{neglected})$$

$$I_E' = (I_E)_Q + \left[\frac{(I_E)_Q}{V_T} \right] V_p \sin \omega t$$

And now,

$$I_C' = \alpha I_E'$$

$$= \alpha \cdot (I_E)_Q \left(1 + \frac{V_p \sin \omega t}{V_T} \right)$$

$$V_o = V_{CC} - I_C' R_C$$

$$= V_{CC} - \alpha (I_E)_Q R_C - \alpha \frac{(I_E)_Q}{V_T} R_C V_p \sin \omega t$$

$$= \underbrace{V_{CC} - (I_C)_Q R_C}_{(V_{CQ})_Q} - \underbrace{\left(\frac{(I_C)_Q}{V_T} R_C \right)}_{A_v = g_m R_C} \underbrace{V_p \sin \omega t}_{V_i}$$

$$V_o = (V_C)_Q - g_m R_C \cdot V_p \sin \omega t$$

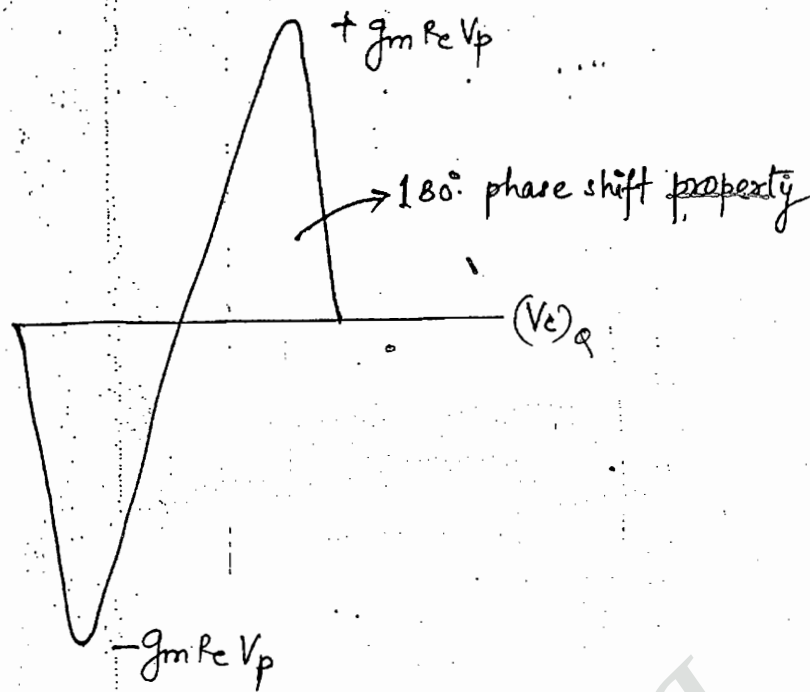
Amplification $\rightarrow A_v = g_m R_C$ and $g_m = \frac{(I_C)_Q}{V_T} \propto (I_C)_Q$

Hence, amplification in AC necessitates the requirement of DC i.e. $(I_C)_Q$ is becoz of DC and the factor which is responsible for gain.

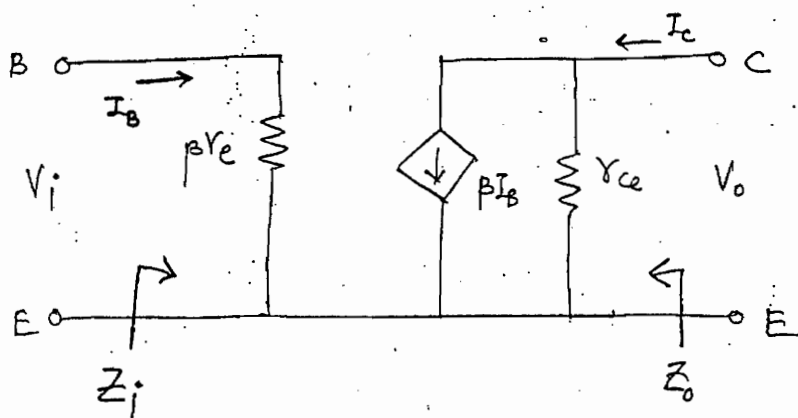
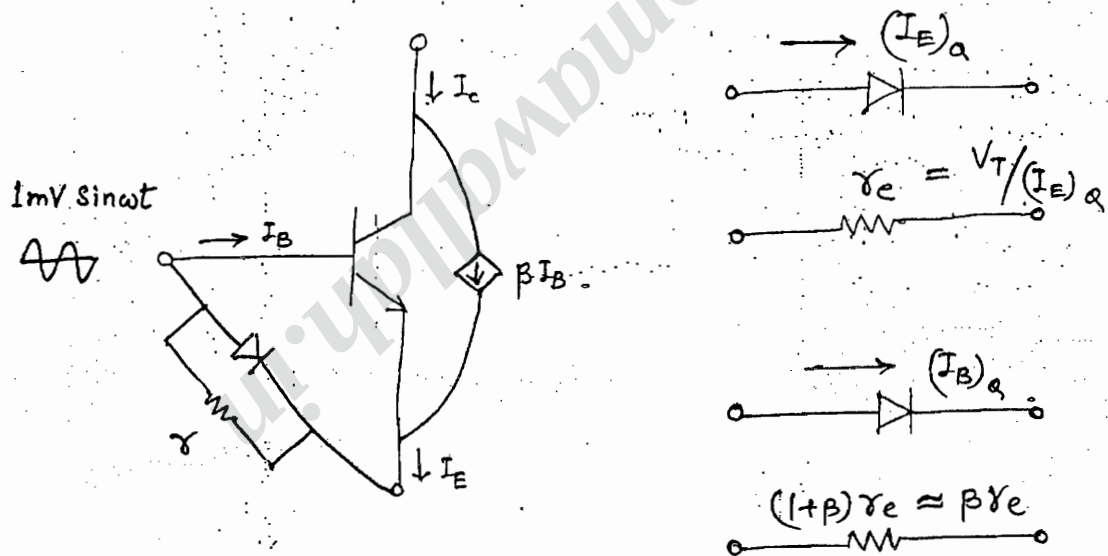
$$V_o = \underbrace{(V_C)_Q}_{\text{Direct coupled}} - \underbrace{g_m R_C \cdot V_p \sin \omega t}_{\text{AC coupled}}$$

for AC coupled, $V_o = -g_m R_C \cdot V_p \sin \omega t$

for Direct coupled, $V_o =$



Properties of CE amplifier -



$$(1) Z_i = \beta r_e$$

Assume, $\beta = 100$
 $r_e = 10 \Omega$

$$\therefore Z_i = 1 k\Omega$$

$$(2) Z_o = r_{ce} = 80 k\Omega$$

$$(3) V_o = (-\beta I_B) \cdot r_{ce}$$

$$= -\beta \cdot \frac{V_i}{\beta r_e} \cdot r_{ce}$$

$$V_o = -\frac{r_{ce}}{r_e} \cdot V_i$$

$$\therefore A_v = \frac{V_o}{V_i} = -\frac{r_{ce}}{r_e} > 1$$

↓
180° phase shift

$$(4) A_i = \frac{I_E}{I_B} = \beta > 1$$

Conclusions:

- (1) The i/p impedance is moderate.
 - (2) The o/p impedance is also moderate.
 - (3) The voltage gain is greater than one.
 - (4) The current gain is greater than one.
- Hence, power gain is also greater than one.

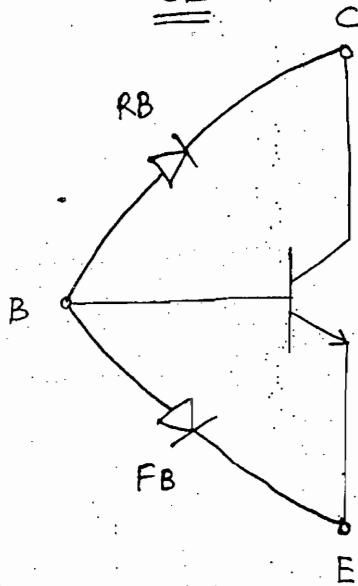
Note: In most of practical applications, CE is used bcoz the power gain is high.

* 95% applications we use CE configuration.

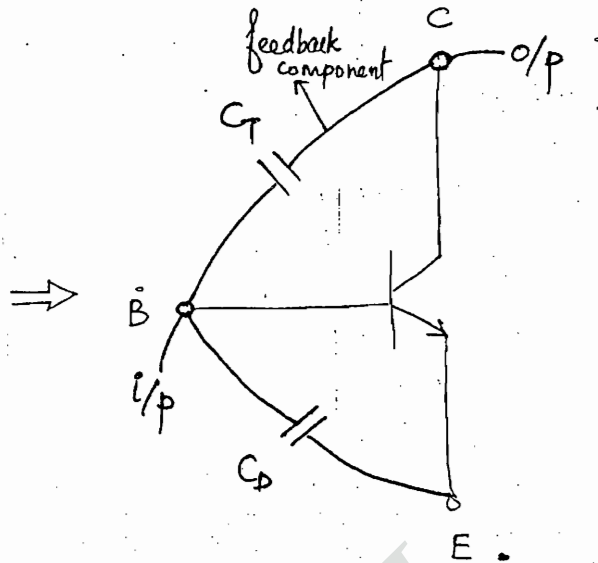
* CE is not used for high frequency s/g - CB is preferred in such cases.

CB amplifiers in High frequency:

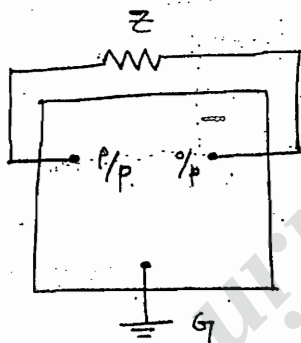
CE:



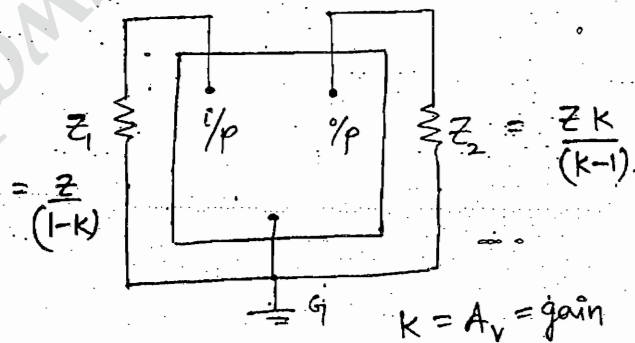
(Active)



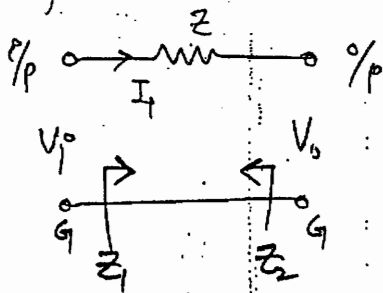
Millers theorem:



$\Rightarrow$



Proof:



$$I_1 = \frac{V_i - V_o}{Z}$$

$$\text{OR } I_1 = -\frac{V_o}{Z} \left(1 - \frac{V_i}{V_o}\right)$$

$$Z = \frac{V_i}{I_1} \left(1 - \frac{V_o}{V_i}\right)$$

$$Z = Z_2 \left(1 - \frac{1}{K}\right)$$

$$\boxed{Z = Z_1 (1-K)}$$

$$\boxed{Z_2 = \frac{ZK}{(K-1)}}$$

$$Z_1 = \frac{Z}{(1-K)} \dots$$

$$\frac{1}{C_{T1}} = \frac{1}{C_T (1-A_v)}$$

$$C_{T1} = C_T (1-A_v)$$

$$Z_2 = \frac{Z K}{(K-1)} = \frac{Z}{(1-\frac{1}{K})}$$

for high value of K (generally)

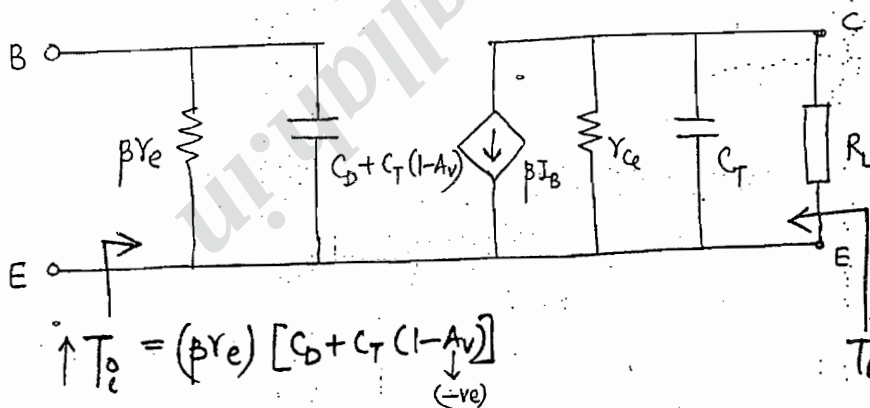
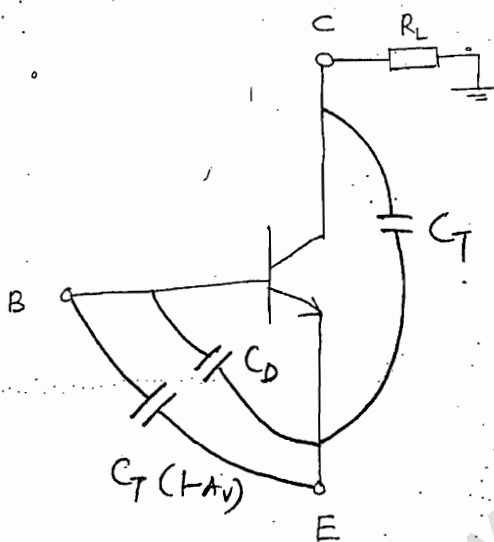
$$K-1 \approx K$$

$$\therefore Z_2 = Z$$

$$\frac{1}{C_{T2}} = \frac{1}{C_T}$$

$$C_{T2} = C_T$$

$$C_{in} = C_D + C_T (1-A_v) \quad (\text{high value})$$



$(R_c / (1-A_v) \approx R_c)$
That's why
 R_c is not shown

$$\uparrow T_i = (\beta r_e) [C_D + C_T (1-A_v)]$$

$$T_o \approx C_T R_L$$

$$\omega_{BW} = \frac{1}{T_{total}}$$

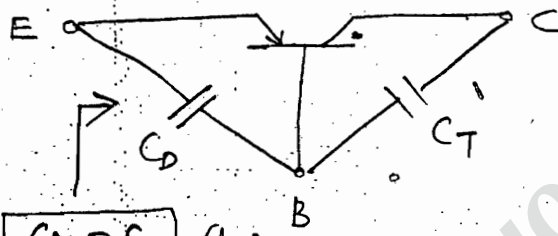
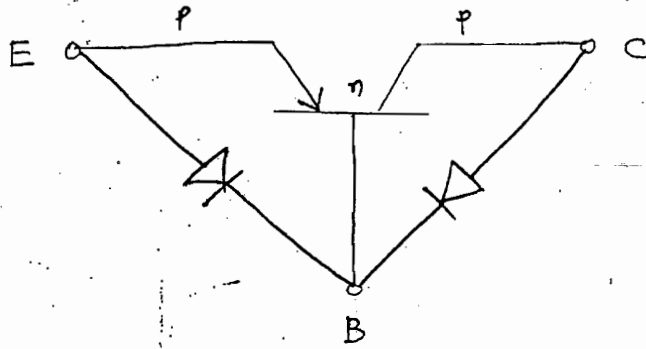
$$T_{total} = T_i + T_o \approx T_i$$

$$\text{and } T_i \gg T_o$$

$$\therefore \omega_{BW} = \frac{1}{T_i} \quad (\text{Hence, not preferred for high frequency})$$

$$\omega_{BW} = \frac{1}{(\beta r_e) [C_D + C_T (1-A_v)]}$$

CB:

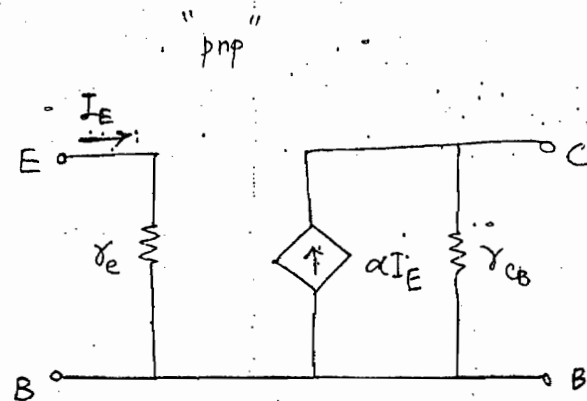
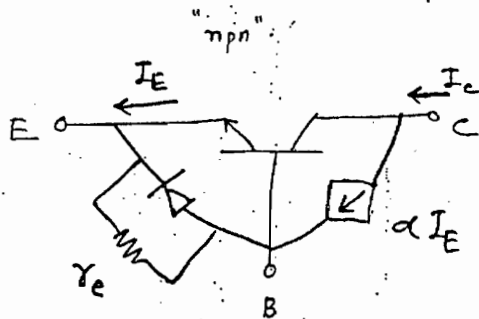


$$C_{in} = C_D \text{ (low)}$$

$$\uparrow \omega_{BW} = \frac{1}{T_i \downarrow} \text{ (Hence, preferred for high frequency)}$$

$$\omega_{BW} = \frac{1}{(\beta r_e)(C_D)}$$

Properties of CB amplifier:



$$(1). Z_i = r_e$$

$$r_e \rightarrow (1 \Omega \text{ to } 25 \Omega)$$

$$Z_i \approx 10 \Omega$$

$$(2). Z_o = r_{cb}$$

$$= 4 M\Omega$$

$$(3) \cdot A_V = \frac{r_{CB}}{r_e} > 1 \text{ (No phase shift)}$$

$$= \frac{4 \text{ M}\Omega}{10 \Omega}$$

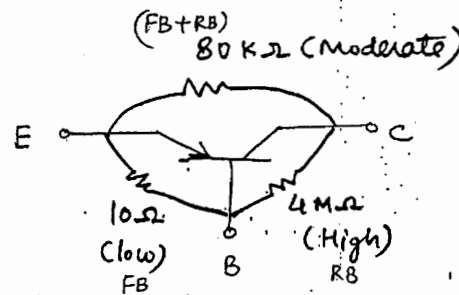
$$\approx 400 \text{ K}\Omega$$

$$(4) \cdot A_I = \frac{I_C}{I_E} = \alpha \leq 1 \quad 123$$

$$\text{If } A_I = 1$$

$$I_C = I_E$$

Current Buffer



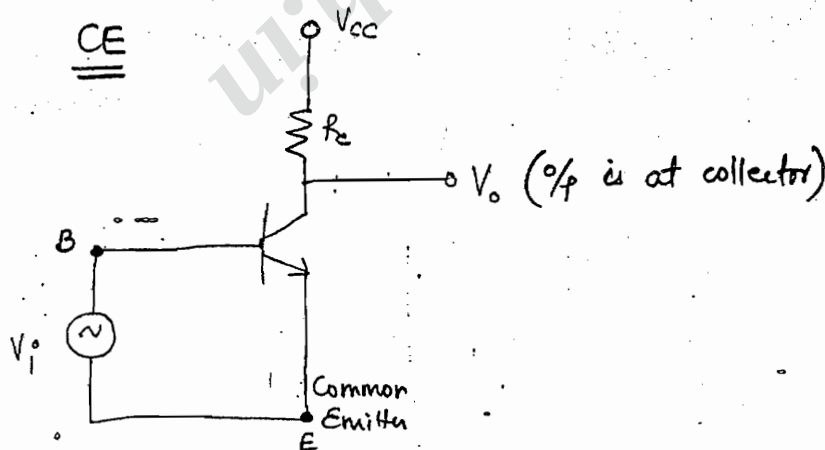
Conclusions:

- (1) the i/p impedance is low.
- (2) the o/p impedance is high.
- (3) the voltage gain is greater than one
- (4) the current gain is less than or equal to one (Buffer).

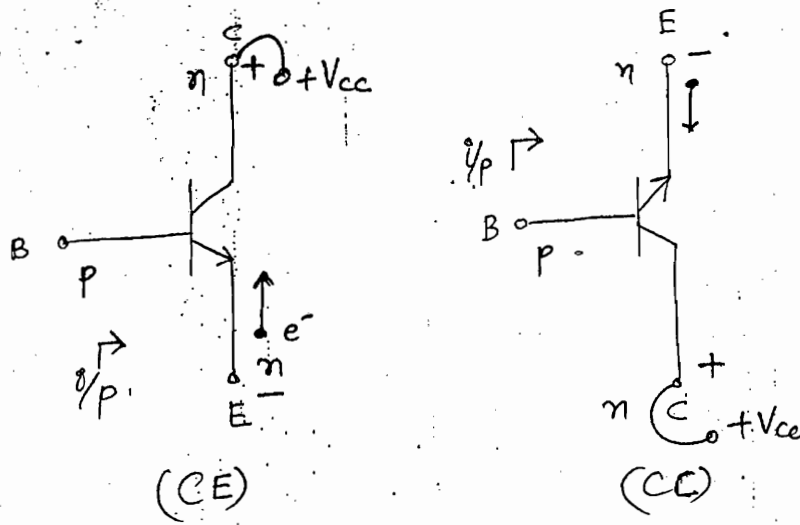
Note: In practical applications, CB amplifier is used whenever high BW is required.

* Power gain is not much high becoz of $A_I \leq 1$.

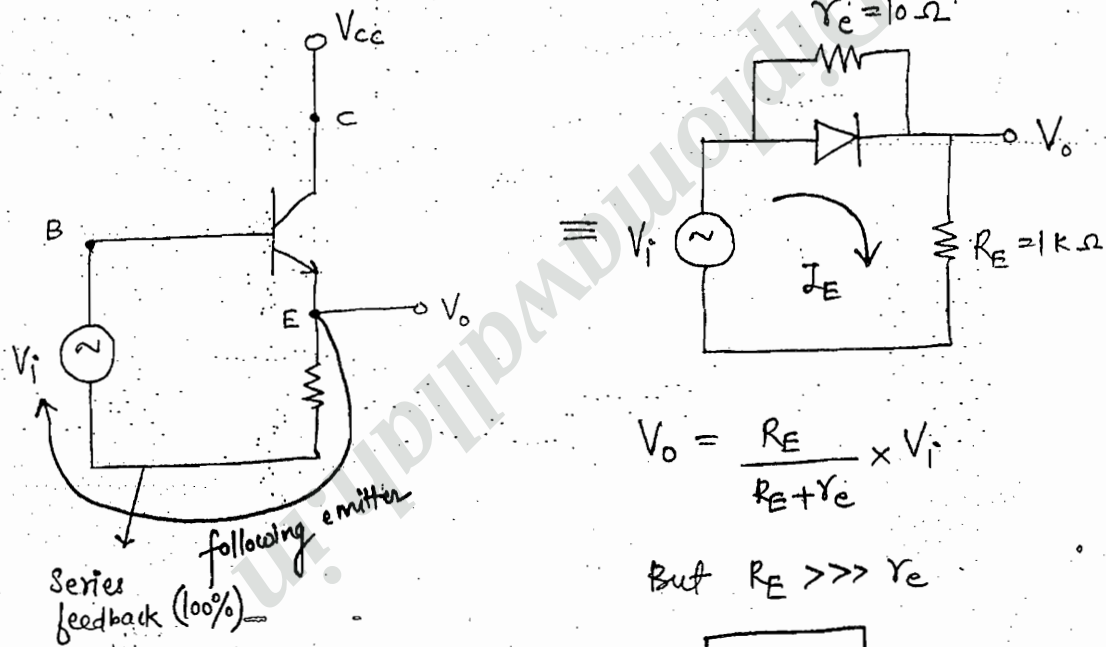
CC Amplifier: (or) emitter follower



for CC, o/p must be at emitter (simultaneously load will also be at emitter)



CC:

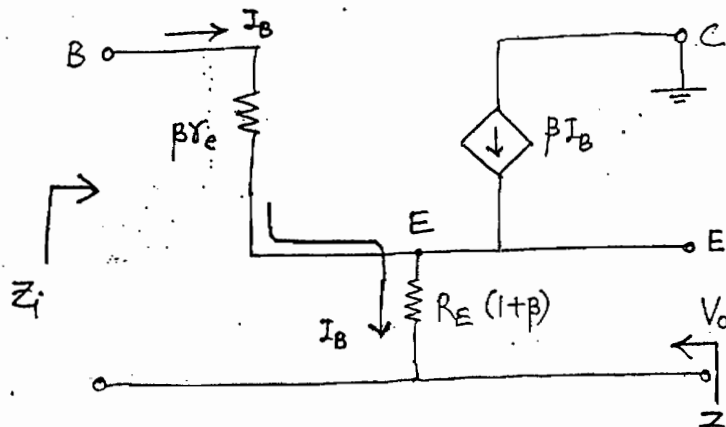


$$V_o = \frac{R_E}{R_E + r_e} \times V_i$$

But $R_E \gg r_e$

$$\therefore V_o = V_i \rightarrow \text{Voltage Buffer}$$

Properties of CC amplifiers:



$$(1) \cdot Z_{i-} = \beta r_e + (1+\beta)R_E$$

Assume, $\beta = 100$

$$r_e = 10 \Omega$$

$$R_E = 10 \text{ K}\Omega$$

$$Z_i = (100)(10) + (100)(10 \text{ K})$$

$$= 10 \text{ K} + 10^3 \text{ K}\Omega$$

$$\approx 10^3 \text{ K}\Omega \approx 1 \text{ M}\Omega$$

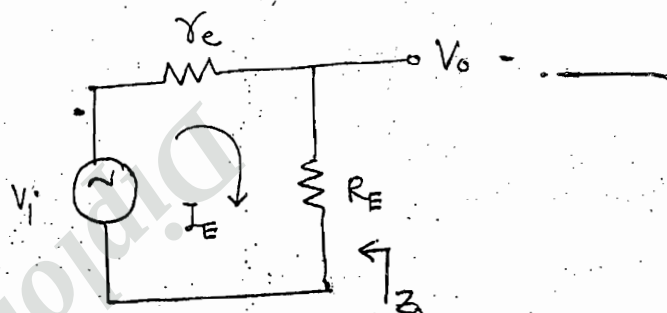
i.e. $Z_i = \beta R_E$

$$(2) \cdot Z_o = \frac{V_o}{I_E}$$

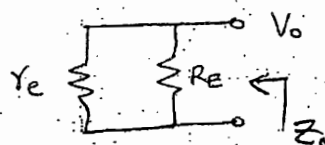
$$Z_o = r_e \parallel R_E$$

$$\approx r_e$$

$$\approx 10 \Omega$$



$$\Downarrow V_i = 0$$



$$(3) \cdot A_V = \frac{V_o}{V_i} \leq 1$$

$$(4) \cdot A_I = \frac{I_E}{I_B}$$

$$= 1+\beta$$

$$\approx \beta > 1$$

Conclusions:

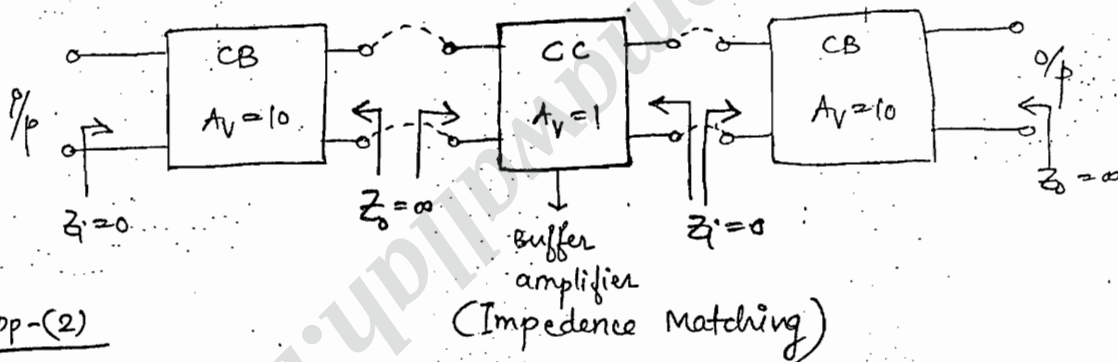
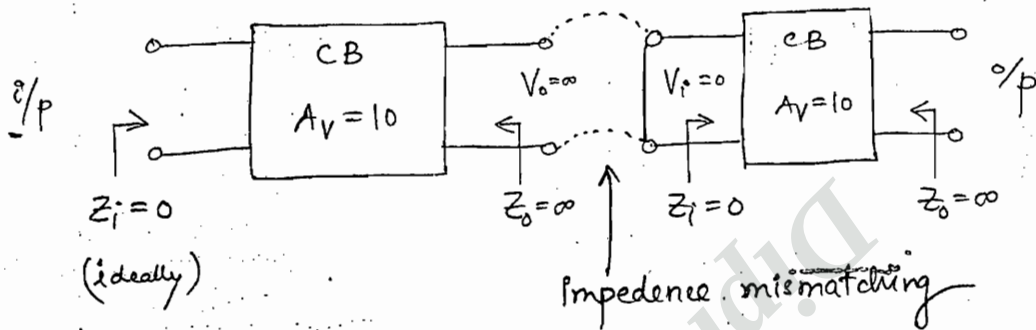
- (1). the i/p impedance is high.
- (2). the o/p impedance is low (impedance matching)
- (3). the voltage gain is unity (voltage buffer)
- (4). the current gain is greater than unity.

Note: In practical applications, CC is used for "impedance matching".

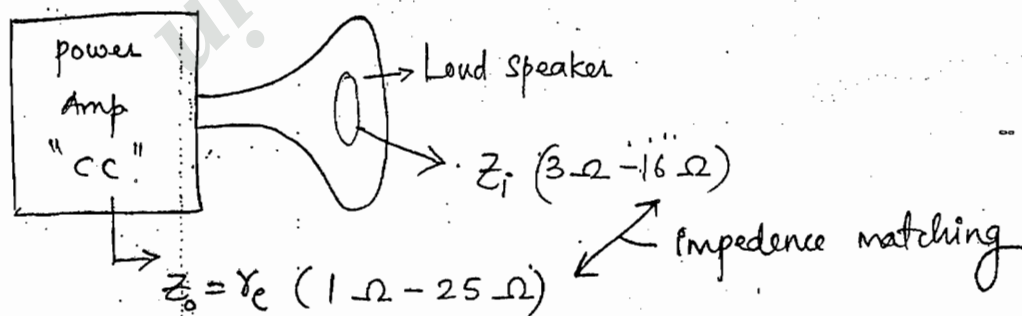
* Here also, power gain is not much high.

Common collector in Impedance Matching :

App-(1)

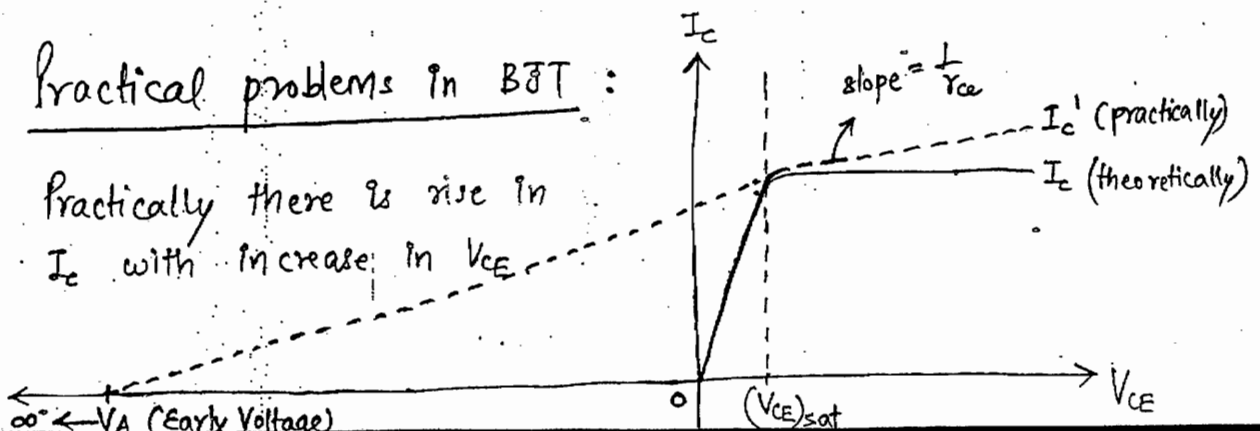


App-(2)



Practical problems in BJT :

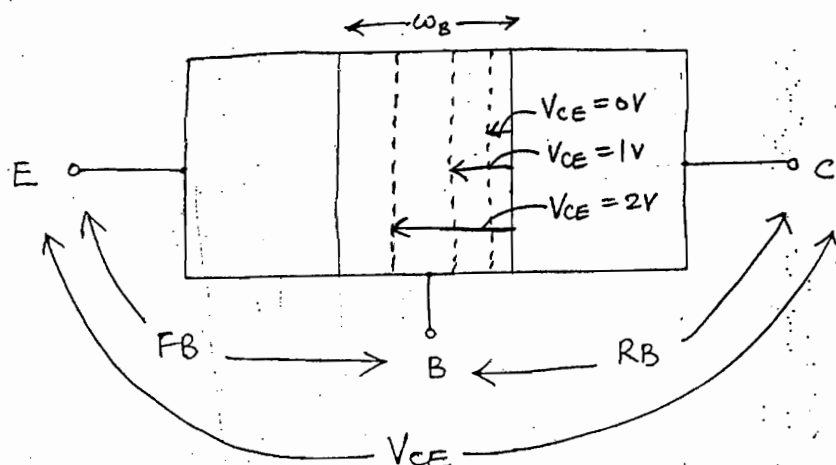
Practically there is rise in I_c with increase in V_{CE} .



Analysis-(1): Block diagram analysis

Early Effect (on Base width modulation):

(Early $\rightarrow$ Scientist name)



As $V_{CE} \uparrow$, $w_B \downarrow$, $I_A \downarrow$, $I_B \downarrow$, $I_{EP} \uparrow$, $I_E \uparrow$, $I_C \uparrow$

$$I_C = I_0 \cdot e^{V_{BE}/V_T} \rightarrow \text{Theoretical eqn}$$

$$I_C' = I_0 \cdot e^{V_{BE}/V_T} \left(1 + \frac{V_{CE}}{V_A}\right) \rightarrow \text{Practical eqn}$$

o/p Resistance:

$$r_{ce} = r_o = \frac{\partial V_{CE}}{\partial I_C'} \quad \left[r_e = \frac{V_T}{(I_E)_Q} \right]$$

$$\frac{1}{r_o} = \frac{\partial I_C'}{\partial V_{CE}} = \frac{\partial}{\partial V_{CE}} \left[I_0 \cdot e^{V_{BE}/V_T} \left(1 + \frac{V_{CE}}{V_A}\right) \right]$$

$$= \frac{I_0 \cdot e^{V_{BE}/V_T}}{V_A}$$

$$= \frac{(I_C)_Q}{V_A}$$

$$\therefore r_o = \frac{V_A}{(I_C)_Q}$$

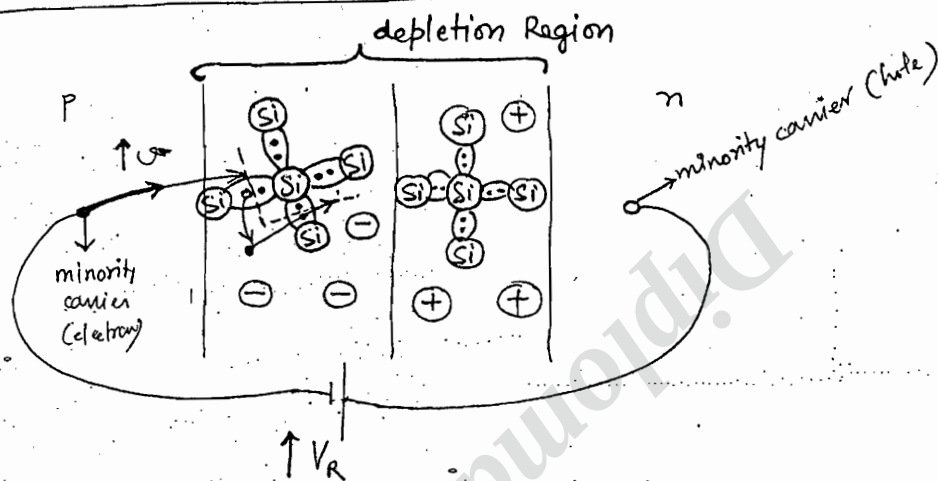
Now, Assume: $V_A = 1000$, $(I_C)_Q = 1 \text{ mA}$

$$\therefore r_o = \frac{1000 \text{ V}}{1 \text{ mA}} = 1 \text{ M}\Omega$$

* Early voltage (V_A) tells the quality of BJT (good or bad).

- (1). Thermal runaway.
- (2). Punch Through (or) Reach through.
- (3). Avalanche breakdown.

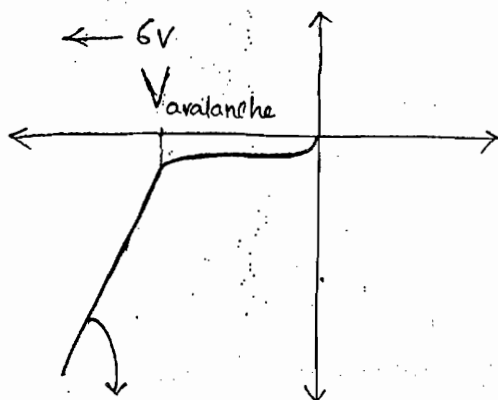
Avalanche breakdown in a Diode :



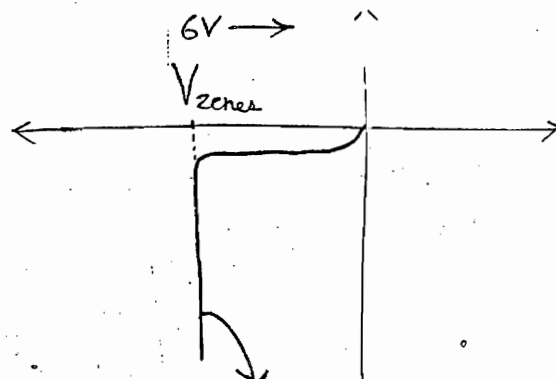
Explanation :

- (1). In the depletion region, there will be some stable atoms along with immobile ions.
- (2). When the RB voltage $\uparrow$ ses, the velocity of minority charge carrier $\uparrow$ ses which will break the covalent bond in the depletion region.
- (3). Because of this breakdown new electron hole pairs will be generated which will increase the minority population, this type of mechanism is called as avalanche breakdown. (Avalanche multiplication).

* Becoz of high level of doping in zener, breakdown takes place by electric field applied (RB) directly. There is no such multiplication of minority charge carriers.



Linear Breakdown
(or)
Avalanche Breakdown

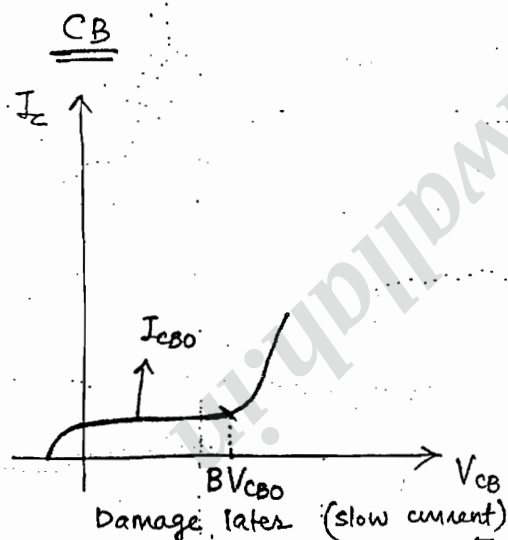


Instant Breakdown
(or)

Zener Breakdown
(Very fast phenomena)
Rapid current

$$V_Z < 6V$$

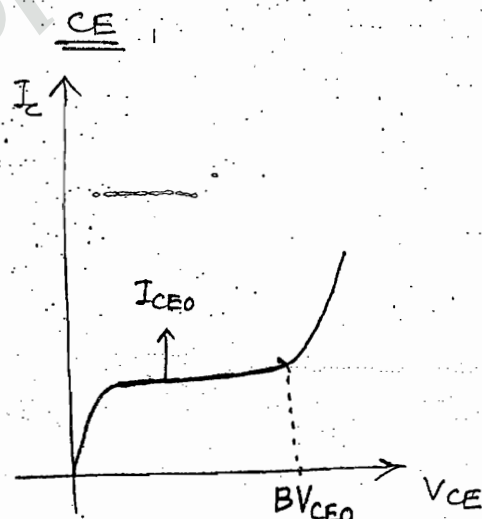
Above 6V, $V_{avalanche} \rightarrow$



$$I_{CEO} > I_{CBO} \quad [I_{CEO} = (1 + \beta) I_{CBO}]$$

$$BV_{CEO} < BV_{CBO}$$

$$BV_{CEO} = \frac{BV_{CBO}}{\sqrt{\beta}}$$



$\beta \rightarrow$ current gain in CE

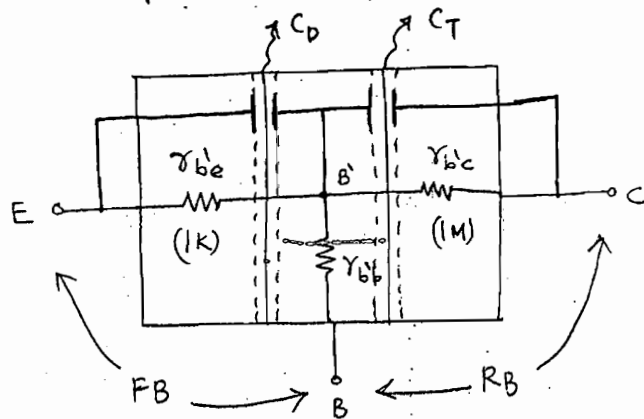
$\eta \rightarrow$ avalanche multiplication factor

$\eta \rightarrow (3 \text{ to } 6)$

Small s/g Analysis of BJT :

131

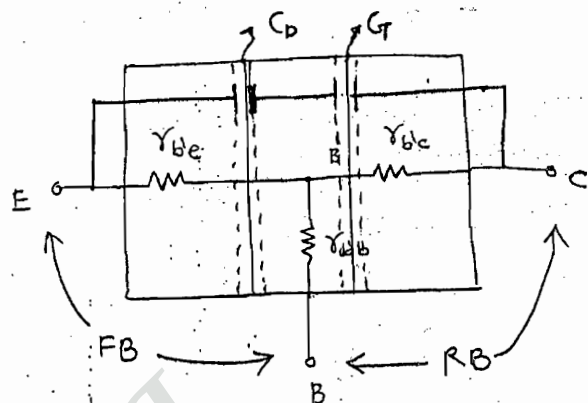
Low frequency Analysis: $f \rightarrow 0$



$$X_C \propto \frac{1}{f} \rightarrow \infty \text{ (OC)}$$

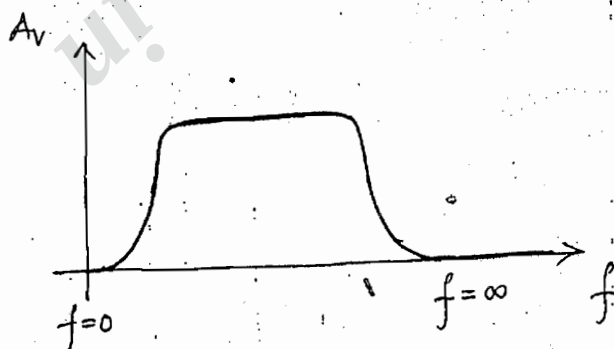
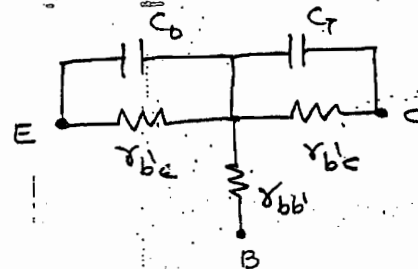
At low frequency analysis,
 C_D & C_T are assumed to
be open (neglect).

High frequency Analysis
 $f \rightarrow \infty$



$$X_C \propto \frac{1}{f} \rightarrow 0 \text{ (SC)}$$

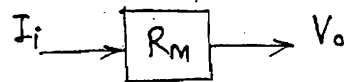
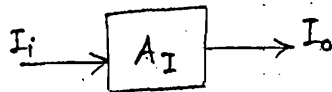
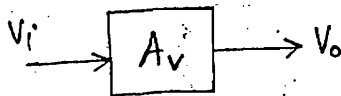
At high frequency analysis,
 C_D & C_T are taken in
to account. (practically).



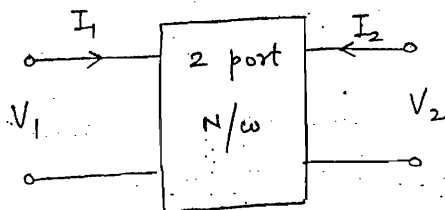
* Types of Basic amplifiers:

- (1). Voltage amplifiers
- (2). Current amplifiers

- (3). Transconductance amplifier
- (4). Transresistance amplifier



Linear 2 port N/w -



Z parameters:

$$V_1 = Z_i I_1 + Z_r I_2$$

$$V_2 = Z_f I_1 + Z_o I_2$$

$$V_1 = f(I_1, I_2)$$

$$V_2 = f(I_1, I_2)$$

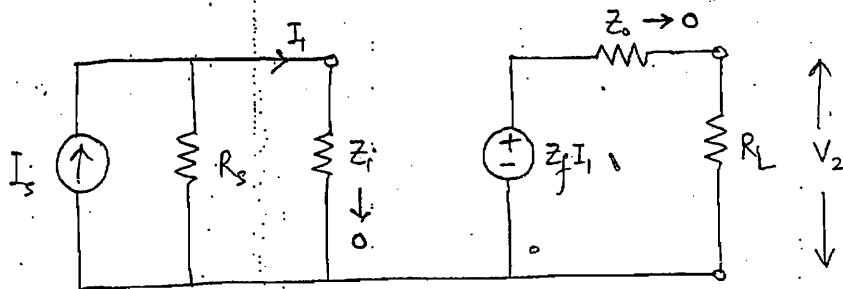
$$\begin{bmatrix} V_1 \\ V_2 \end{bmatrix} = \begin{bmatrix} Z_i & Z_r \\ Z_f & Z_o \end{bmatrix} \begin{bmatrix} I_1 \\ I_2 \end{bmatrix}$$

An ideal amplifier is a unilateral device

$$Z_r = 0 ; Z_i = 0 ; Z_o = 0$$

$$\begin{bmatrix} V_1 \\ V_2 \end{bmatrix} = \begin{bmatrix} 0 & 0 \\ Z_f & 0 \end{bmatrix} \begin{bmatrix} I_1 \\ I_2 \end{bmatrix}$$

$$V_2 = Z_f I_1 \rightarrow \text{"CCVS"}$$



Transresistance Amplifier "CCVS"

Y parameters:

$$I_1 = f(V_1, V_2)$$

$$I_2 = f(V_1, V_2)$$

$$I_1 = Y_i V_1 + Y_r V_2$$

$$I_2 = Y_f V_1 + Y_o V_2$$

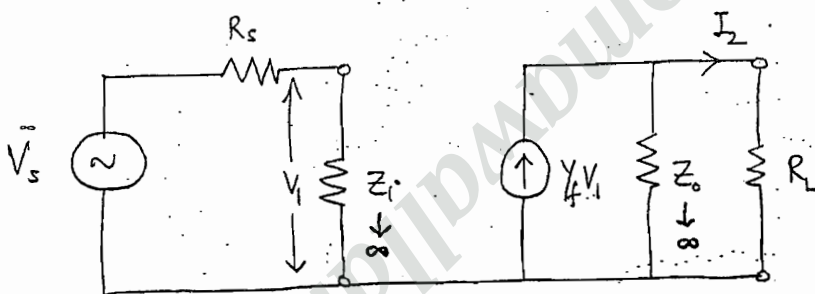
$$\begin{bmatrix} I_1 \\ I_2 \end{bmatrix} = \begin{bmatrix} Y_i & Y_r \\ Y_f & Y_o \end{bmatrix} \begin{bmatrix} V_1 \\ V_2 \end{bmatrix}$$

An ideal amplifier is a unilateral device

$$Y_i = 0 ; Y_r = 0 ; Y_o = 0$$

$$\begin{bmatrix} I_1 \\ I_2 \end{bmatrix} = \begin{bmatrix} 0 & 0 \\ Y_f & 0 \end{bmatrix} \begin{bmatrix} V_1 \\ V_2 \end{bmatrix}$$

$$I_2 = Y_f V_1 \rightarrow \text{"VCCS"}$$



Trans conductance amplifier "VCCS" (FET/MOSFET)

H parameters:

$$V_1 = f(I_1, V_2)$$

$$I_2 = f(I_1, V_2)$$

$$V_1 = h_i I_1 + h_r V_2$$

$$I_2 = h_f I_1 + h_o V_2$$

Case (1): $V_2 = 0$

Case (1): $V_2 = 0$

$$h_i = \frac{V_1}{I_1} \rightarrow \text{i/p impedance}$$

$$h_f = \frac{I_2}{I_1} \rightarrow \text{forward current gain}$$

Case (2): $I_1 = 0$

$$h_r = \frac{V_1}{V_2} \rightarrow \text{Reverse voltage gain}$$

$$h_o = \frac{I_2}{V_2} \rightarrow \text{o/p admittance}$$

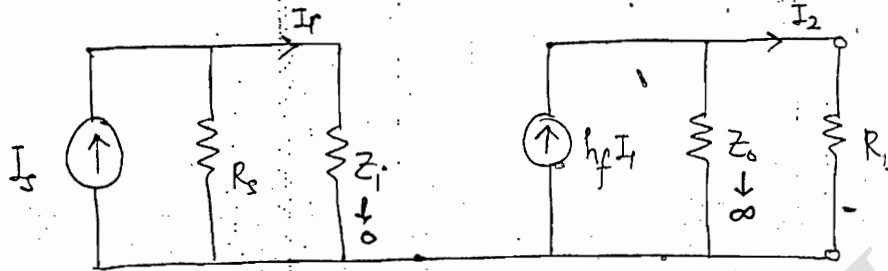
An ideal amplifier is a unilateral device

$$h_i = 0 ; h_r = 0 ; h_o = 0$$

$$\begin{bmatrix} V_1 \\ I_2 \end{bmatrix} = \begin{bmatrix} 0 & 0 \\ h_f & 0 \end{bmatrix} \begin{bmatrix} I_1 \\ V_2 \end{bmatrix}$$

$$I_2 = h_f I_1 \rightarrow \text{"CCCS"}$$

$$I_c = \beta I_B$$



Current Amplifier "CCCS" (BJT)

g parameters:

$$I_1 = f(V_1, I_2)$$

$$V_2 = f(V_1, I_2)$$

$$I_1 = g_i V_1 + g_r I_2$$

$$V_2 = g_f V_1 + g_o I_2$$

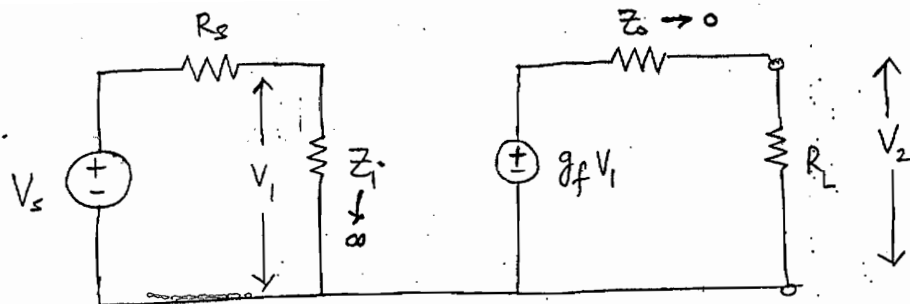
$$\begin{bmatrix} I_1 \\ V_2 \end{bmatrix} = \begin{bmatrix} g_i & g_r \\ g_f & g_o \end{bmatrix} \begin{bmatrix} V_1 \\ I_2 \end{bmatrix}$$

An ideal amplifier is a unilateral device

$$g_i = 0 ; g_r = 0 ; g_o = 0$$

$$\begin{bmatrix} I_1 \\ V_2 \end{bmatrix} = \begin{bmatrix} 0 & 0 \\ g_f & 0 \end{bmatrix} \begin{bmatrix} V_1 \\ I_2 \end{bmatrix}$$

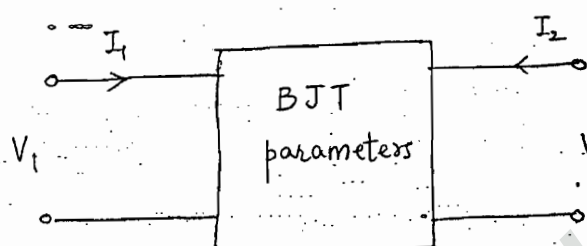
$$V_2 = g_f V_1 \rightarrow \text{"VCVS"}$$



∴ Voltage Amplifier "VCVS" (OP-Amp)

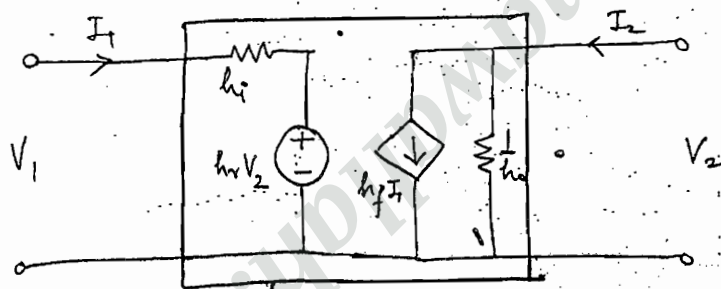
Low frequency analysis of BJT :

BJT as a h-parameter model :



$$V_1 = f(I_1, V_2)$$

$$I_2 = f(I_1, V_2)$$



Low frequency model of BJT

Typical values of h-parameters :

h-parameters	CE	CC	CB
h_i	1100 Ω	1100 Ω	22 Ω
h_f	50	-51	-0.98
h_r	2.4×10^{-4}	≈ 1	2.9×10^{-4}
h_o	24×10^{-6} mho	24×10^{-6} mho	0.49×10^{-6} mho

Conversion formulae:

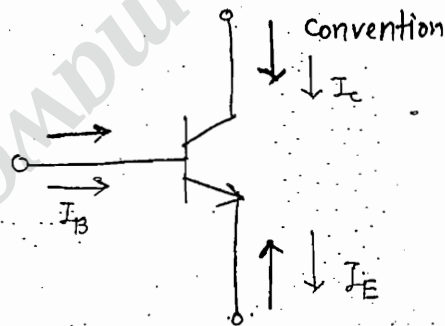
CE to CB	CB to CE:	CE to CC
$h_{ib} = \frac{h_{ie}}{1+h_{fe}}$	$h_{ie} = \frac{h_{ib}}{1+h_{fb}}$	$h_{ic} = h_{ie}$
$h_{fb} = \frac{-h_{fe}}{1+h_{fe}}$	$h_{fe} = \frac{-h_{fb}}{1+h_{fb}}$	$h_{fc} = -(1+h_{fe})$
$h_{rb} = \frac{h_{ie} \cdot h_{oe}}{1+h_{fe}} - h_{re}$	$h_{re} = \frac{h_{ib} \cdot h_{ob}}{1+h_{fb}} - h_{rb}$	$h_{rc} \equiv 1$
$h_{ob} = \frac{h_{oe}}{1+h_{fe}}$	$h_{oe} = \frac{h_{ob}}{1+h_{fb}}$	$h_{oc} = h_{oe}$

Concept of -ve gain:

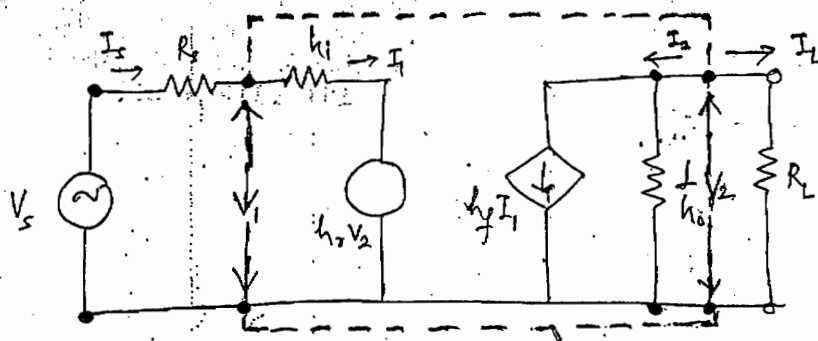
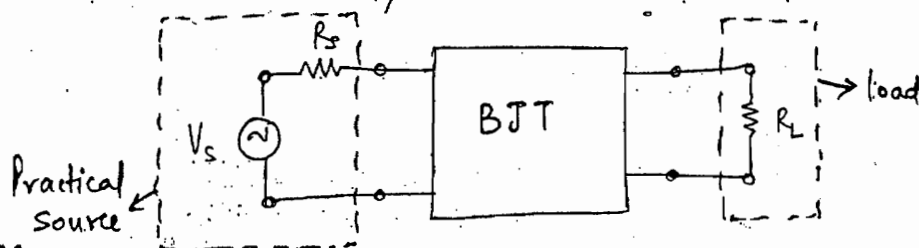
gain (I_B, I_C) = +ve

gain (I_B, I_E) = -ve

gain (I_C, I_E) = -ve



136* Amplifier analysis at low frequencies : (Exact model):



Characteristics of Amplifier (BJT)

ckt parameters (ckt)

(1) i/p impedance, $Z_i = \frac{V_1}{I_1}$

(2) o/p impedance, $Z_o = \frac{V_2}{I_2}$

(3) Volt. gain, $A_v = \frac{V_2}{V_1}$

(4) Current gain, $A_I = \frac{I_L}{I_1}$

(1) Effective i/p impedance, Z_i'

(2) Effective o/p impedance, Z_o'

(3) Voltage amplification, $A_{v_s} = \frac{V_2}{V_s}$

(4) Current amplification, $A_{I_s} = \frac{I_L}{I_s}$

BJT characteristic Analysis:

(1) Current gain, A_I :

$$-I_2 = h_f I_1 + h_o V_2$$

$$V_2 = -I_2 R_L$$

$$I_2 = h_f I_1 - h_o R_L I_2$$

$$I_2 (1 + h_o R_L) = h_f I_1$$

$$\frac{I_2}{I_1} = \frac{h_f}{1 + h_o R_L}$$

$$A_I = \frac{I_L}{I_1} = \frac{-I_2}{I_1} = \frac{-h_f}{1 + h_o R_L}$$

$$V_1 = h_i I_1 + h_r V_2$$

$$I_2 = h_f I_1 + h_o V_2$$

$$\therefore A_I = \frac{-h_f}{1 + h_o R_L}$$

(2) i/p impedance, Z_i :

$$V_1 = h_i I_1 + h_r V_2$$

$$\frac{V_1}{I_1} = h_i + h_r \left(\frac{-I_2 R_L}{I_1} \right)$$

$$Z_i = h_i + h_r A_I R_L$$

$$\therefore Z_i = h_i + h_r R_L A_I$$

(3) Voltage gain, A_V :

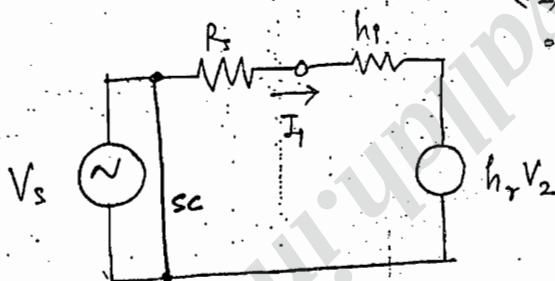
$$\begin{aligned}
 A_V &= \frac{V_2}{V_1} \\
 &= \frac{-I_2 R_L}{V_1} \\
 &= \frac{-I_2/I_1 \cdot R_L}{V_1/I_1}
 \end{aligned}$$

$$A_V = \frac{A_I \cdot R_L}{Z_i}$$

(4) %p impedance, Z_o :

$$Z_o = V_2/I_2$$

$$Y_o = I_2/V_2 = h_f \left(\frac{I_1}{V_2} \right) + h_o$$



$$V_s = (h_i + R_s) I_1 + h_r V_2$$

$$\downarrow 0$$

$$\frac{I_1}{V_2} = -\frac{h_r}{h_i + R_s}$$

$$Y_o = h_f \left(\frac{-h_r}{h_i + R_s} \right) + h_o$$

$$Z_o = \frac{1}{h_o - \frac{h_f h_r}{h_i + R_s}}$$

Conclusions:

Exact model are valid for all types of configurations i.e. CE, CB, CC

Approximate model for CE

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Conditions:

(1). $h_{oe} R_L \leq 0.1$

$R_L < 5K\Omega$

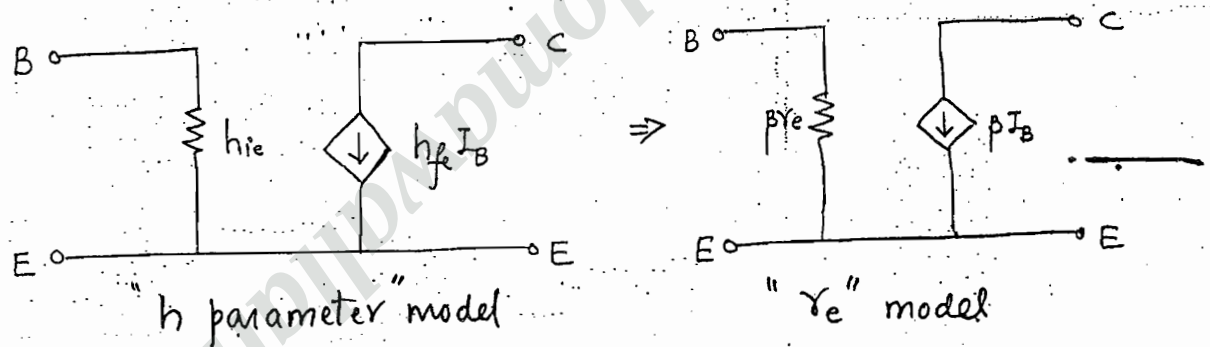
Eg. $A_I = \frac{-h_{fe}}{1+h_{oe}R_L} = \frac{-h_{fe}}{1+0.1} = -0.91 h_{fe}$

* In electronics, less 10% error can be neglected.

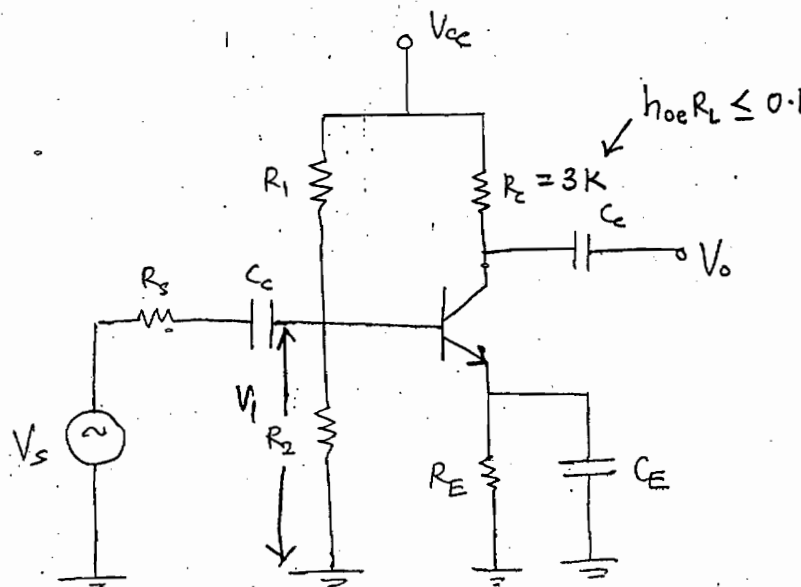
* If $R_L > 5K$, error will come.

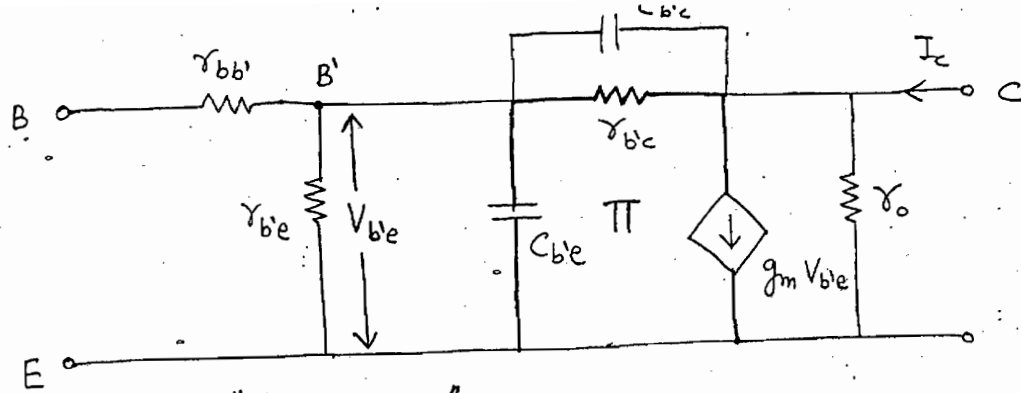
(2). h_{oe} & h_{re} should be neglected.

(3). It is valid only for CE mode.



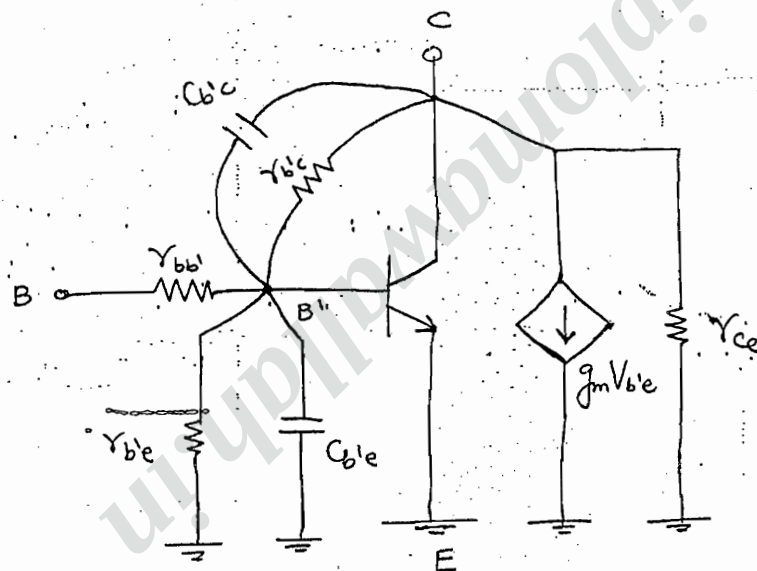
CE amplifier:





"Hybrid- π " model

- * This model is valid for both high and low frequency.
 - But for low frequency, capacitor $\rightarrow$ open
 And for high frequency, ckt is taken as it is.



Hybrid π parameters:

- $r_{bb'}$ $\rightarrow$ Base spreading resistance (less)
 $r_{b'e}$ $\rightarrow$ i/p resistance (r_{π})
 $r_{b'c}$ $\rightarrow$ feedback resistance (high)
 r_{ce} $\rightarrow$ o/p resistance (r_o)
 $C_b \rightarrow C_{b'e}$ $\rightarrow$ diffusion capacitance (stronger)
 $C_t \rightarrow C_{b'c}$ $\rightarrow$ transition capacitance (weaker)
 g_m $\rightarrow$ trans conductance $[I_{cQ}|_{I_{cQ} = 50mA}]$

Typical values

- 100 ($\approx$ sec)
 1 K Ω
 4 M Ω ($\approx$ o.c.)
 80 K Ω
 100 pF
 3 pF
 50 mA/V

Expression for Hybrid π parameters:

Note: Hybrid π parameters depends on 3 important parameters

- (1). Collector current $(I_c)_Q$
- (2). Temperature
- (3). Collector to Emitter voltage V_{CE}

(1). g_m :
$$g_m = \frac{(I_c)_Q}{V_T} = \frac{(I_c)_Q}{T/11,600}$$

As $T \uparrow$, $g_m \downarrow$

As $(I_c)_Q \uparrow$, $g_m \uparrow$

$$A_v = -g_m R_c$$

↓
if load is const.

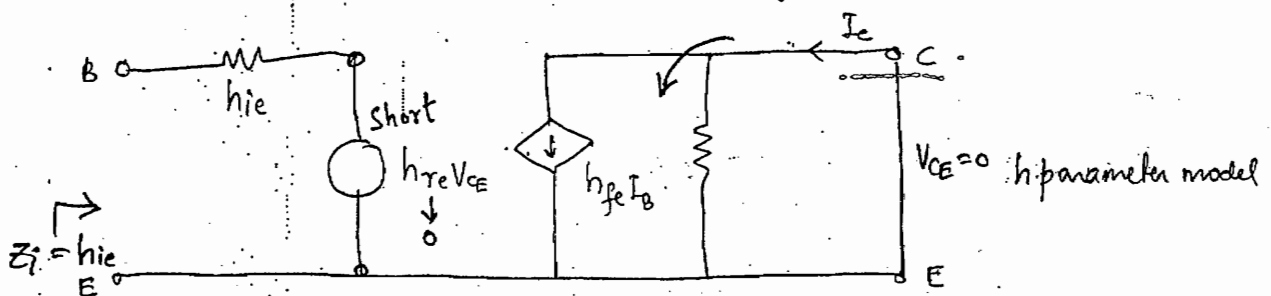
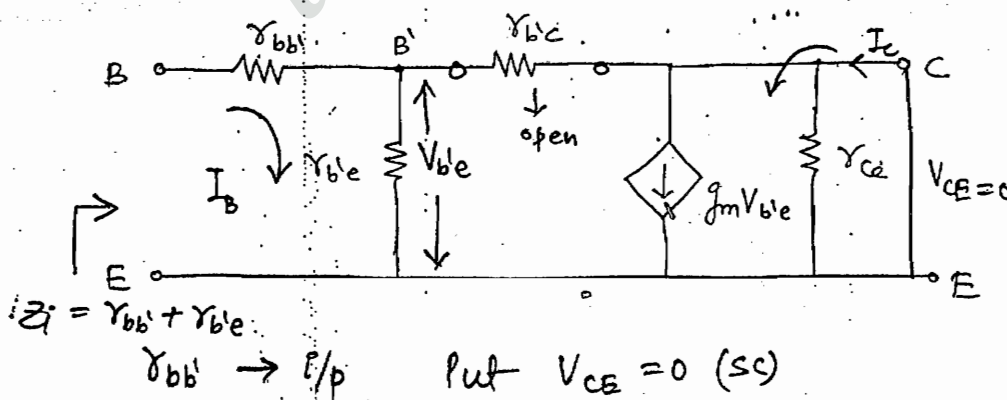
$A_v \uparrow$ if $g_m \uparrow$

(2). $r_{b'e}$:

$$r_{b'e} = \frac{h_{fe}}{g_m} \rightarrow \text{h parameter (at moderate frequency)}$$

↓
PI model

Converting high freq. model into low frequency



$$I_c = g_m V_{b'e}$$

$$I_c = g_m \cdot I_B r_{b'e}$$

$$r_{b'e} = \left(\frac{I_c}{I_B} \right) \frac{1}{g_m} \quad \text{--- (1)}$$

$$I_c = h_{fe} I_B$$

$$\frac{I_c}{I_B} = h_{fe} \quad \text{--- (2)}$$

$$\therefore \boxed{r_{b'e} = \frac{h_{fe}}{g_m}} \dots \dots$$

And,

$$g_m = \frac{(I_c)_Q}{V_T}$$

and $h_{fe} = \beta$

$$\frac{1}{g_m} = \frac{V_T}{(I_c)_Q} \approx \frac{V_T}{(I_E)_Q} = r_e$$

$$\therefore \boxed{r_{b'e} = \frac{h_{fe}}{g_m} = \beta r_e}$$

(3). $r_{bb'}$: Put $V_{CE} = 0$ (again) ($\because r_{bb'} \rightarrow$ i/p parameter)

$$Z_i = r_{bb'} + r_{b'e}$$

$$Z_i = h_{ie}$$

$$r_{bb'} + r_{b'e} = h_{ie}$$

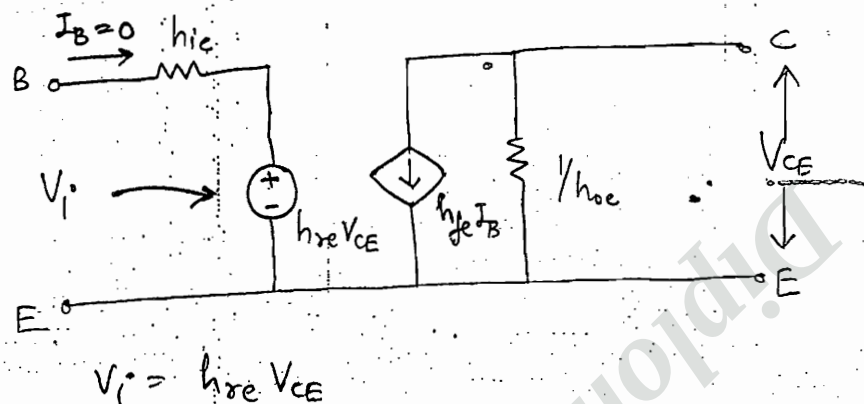
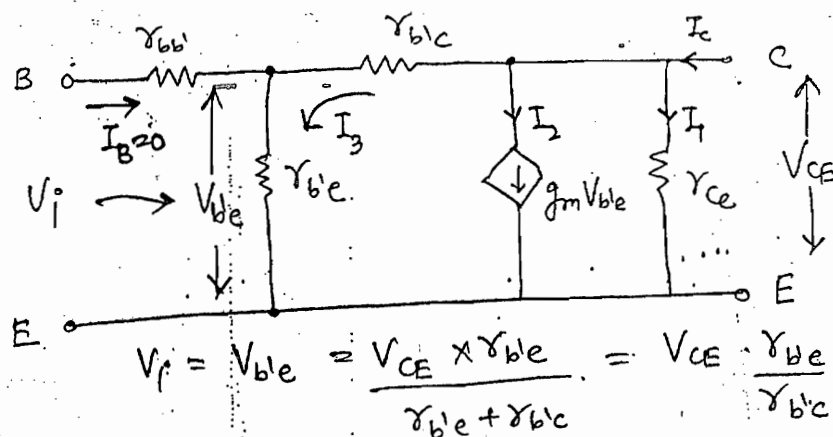
$$\boxed{r_{bb'} = h_{ie} - r_{b'e}}$$

$\downarrow$ $\downarrow$ $\downarrow$
 100Ω $1.1K\Omega$ $1K\Omega$

(4). $r_{b'c}$:

$$\begin{array}{ccc}
 & r_{b'c} & \\
 \swarrow \text{i/p} & & \searrow \text{o/p} \\
 \frac{r_{b'c}}{(1-A_v)} & \frac{r_{b'c} A_v}{(A_v-1)} \approx r_{b'c} & \\
 \downarrow \text{negligible} & & \downarrow \text{(o/p parameter)}
 \end{array}$$

Put $I_B = 0$



$$\therefore V_{CE} \cdot \frac{r_{b'e}}{r_{b'c}} = h_{re} \cdot V_{CE}$$

$$r_{b'c} = \frac{r_{b'e}}{h_{re}}$$

(5) r_{ce} :

$$\therefore \frac{I_c}{V_{CE}} = h_{oe}$$

$$I_c = \frac{V_{CE}}{r_{ce}} + g_m V_{b'e} + \frac{V_{CE}}{r_{b'e} + r_{b'c}}$$

$$I_c = \frac{V_{CE}}{r_{ce}} + g_m \left(\frac{V_{CE} r_{b'e}}{r_{b'e} + r_{b'c}} \right) + \frac{V_{CE}}{r_{b'e} + r_{b'c}}$$

$$\frac{I_c}{V_{CE}} = \frac{1}{r_{ce}} + \frac{g_m r_{b'e} + 1}{r_{b'e} + r_{b'c}}$$

$$\frac{I_c}{V_{CE}} = \frac{1}{r_{ce}} + \frac{h_{fe} + 1}{r_{b'e} + r_{b'c}}$$

$$(\because h_{fe} = g_m r_{b'e})$$

$$(\because r_{b'e} \rightarrow \text{neglected})$$

$$h_{oe} = \frac{1}{r_{ce}} + \frac{h_{fe}}{r_{b'c}} = g_{ce} + h_{fe} g_{b'c}$$

$$(\because r_{ce} = \frac{1}{g_{ce}} \text{ \& } r_{b'c} = g_{b'c})$$

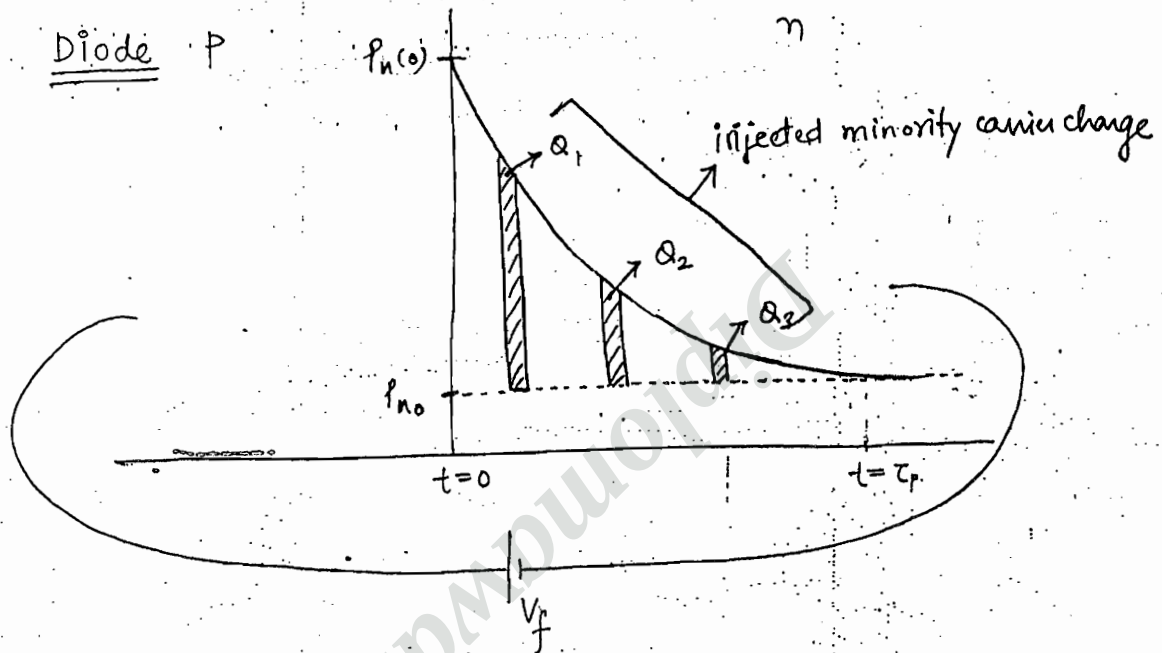
$$r_{ce} = \frac{1}{g_{ce}}$$

$$g_{ce} = h_{oe} - h_{fe} g_{bc}$$

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$$\therefore r_{ce} = \frac{1}{g_{ce}} = \frac{1}{h_{oe} - h_{fe} g_{bc}}$$

(6). Diffusion capacitance, C_D : (C_{be})

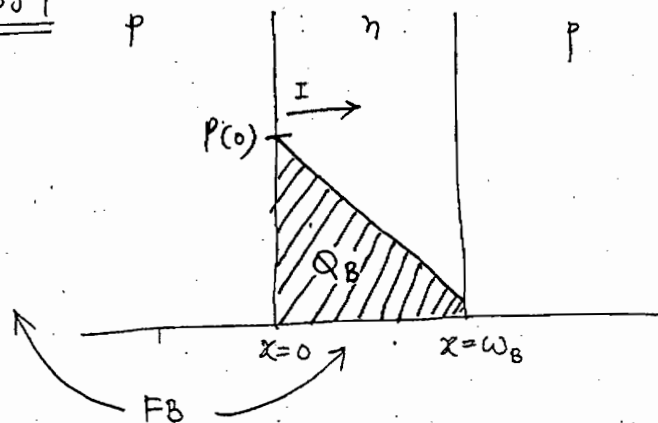


$$Q = I_p \tau_p$$

$$C_D = \frac{dQ}{dV} = \tau_p \cdot \frac{dI_p}{dV}$$

$$\therefore C_D = \frac{\tau_p I_{dc}}{V_T}$$

BJT



$$Q_B = \frac{1}{2} p(0) \cdot (A q w_B) \quad \frac{C_M^3}{M^2}$$

$$I = -A q D_B \frac{dp}{dx}$$

$$= -A q D_B \left\{ \frac{p(0) - 0}{0 - w_B} \right\}$$

$$= \frac{A q D_B \{ p(0) \}}{w_B}$$

$$Q_B = \frac{1}{2} I(0) \cdot A q \omega_B$$

$$\therefore Q_B = \frac{I \omega_B^2}{2 D_B}$$

$$\text{And, } C_D = \frac{dQ_B}{dV} = \frac{d \omega_B^2 I}{dV 2 D_B} = \frac{\omega_B^2}{2 D_B} \cdot \frac{dI}{dV} = \frac{\omega_B^2}{2 D_B} g_m$$

$$C_D = \frac{\omega_B^2}{2 D_B} \cdot \frac{1}{\gamma_e}$$

$$C_D = \frac{\omega_B^2}{2 D_B} \cdot \frac{(I_C) \alpha}{V_T}$$

$$\boxed{T_B = \omega_B^2 / 2 D_B} \rightarrow \text{Base transit time}$$

$$\text{As } \frac{\omega_B^2}{2 D_B} = \frac{\omega_B^2}{2 \mu V_T} \rightarrow \frac{m^2}{m^2/V \cdot \text{sec} \cdot V} \rightarrow \text{sec i.e. time}$$

Q. As $V_{CE} \uparrow$ see what happens to C_D in Hybrid π model.
 Sol. As $V_{CE} \uparrow$, $\omega_B \downarrow$, $C_D \downarrow$ (Early Effect)

$$\omega_B = \omega_B' + \omega_B''$$

$\downarrow$
 this width we are talking about.

(7). $C_{b'c}$: Transition capacitance (C_T) :

$$C_T = \frac{\epsilon A}{\omega}$$

$$C_T \propto \frac{1}{\sqrt{V_R}} \rightarrow \text{Alloy type}$$

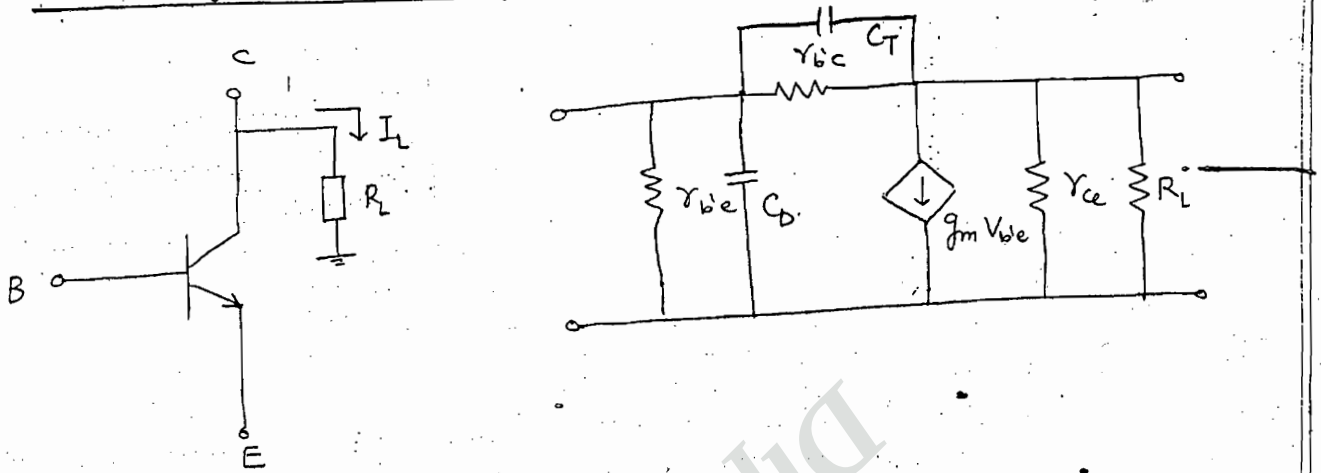
$$C_T \propto \frac{1}{\sqrt[3]{V_R}} \rightarrow \text{Grown jxn type}$$

$$\boxed{C_{b'c} \propto \frac{1}{(V_{CE})^\eta}} \quad ; \quad \eta = 1/2 \rightarrow \text{alloy type}$$

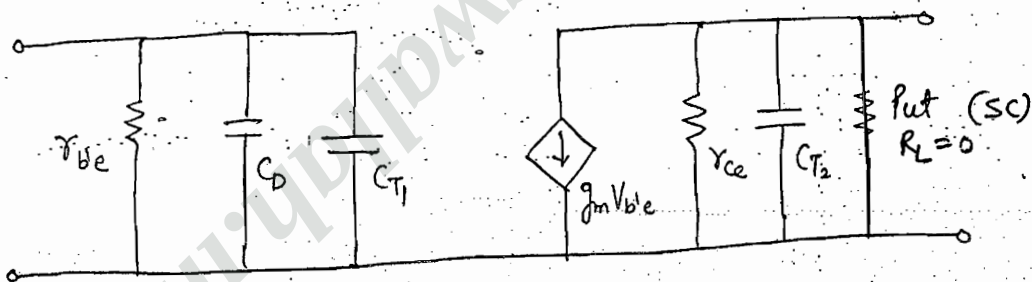
$$\eta = 1/3 \rightarrow \text{grown jxn type}$$

* C_{bc} is independent of temp. & $(I_E)_Q$, it only depends upon V_{CE} supply. (the only parameter).

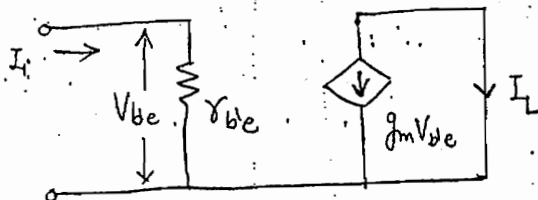
frequency response of an amplifier:



CE SC current gain (max^m in sc condition):

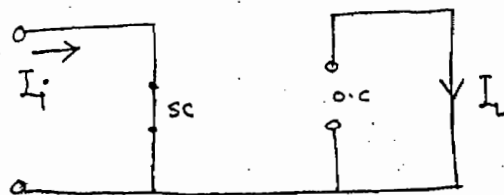


At $f=0$: (cap $\rightarrow$ open)



$$A_I = \frac{I_L}{I_i} = \frac{-g_m V_{be}}{V_{be}/r_{be}} = -g_m r_{be} = -h_{fe}$$

At $f=\infty$: (cap $\rightarrow$ short)

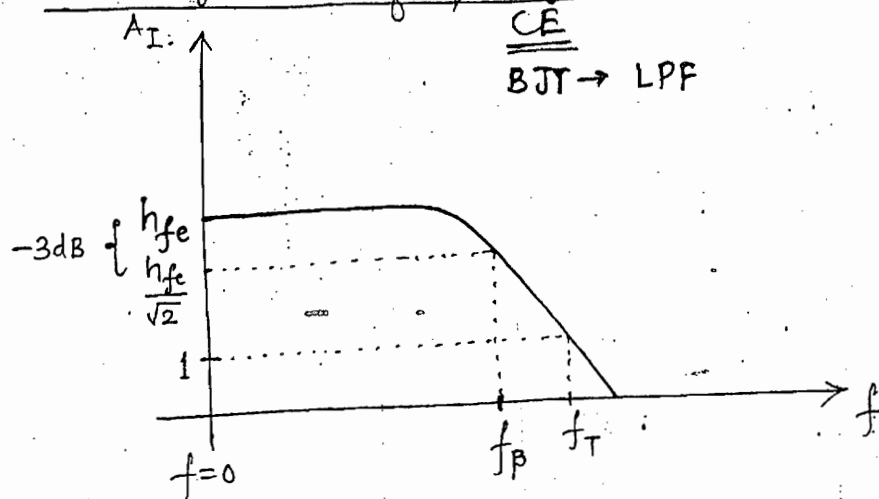


$$A_I = -\beta$$

$$A_I = \frac{I_L}{I_i}$$

$$A_I = 0$$

Current gain vs frequency:

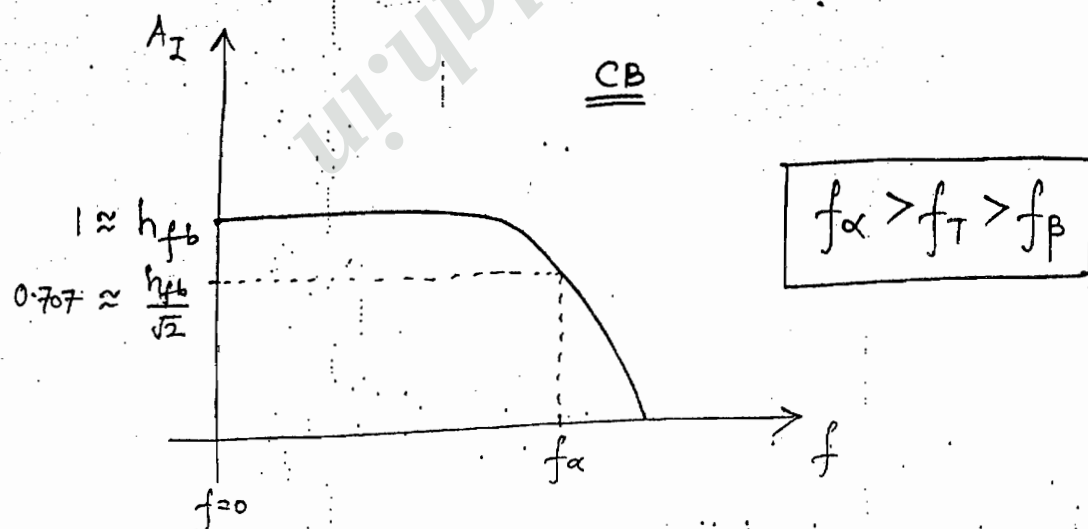


* dB values are used to compress the values of scale markings. ($f \rightarrow$ GHz, MHz.)

Scaling is required for simplicity.

* $20 \log(1) = 0$ i.e. above f_T , the gain is zero.

Hence, f_T is given by manufacturer, upto which a sig can be applied to the given BJT.



$$f_T = h_{fe} f_{\beta}$$

$$f_{\alpha} \approx 1.01 f_T \quad (\text{for } h_{fe} = 100)$$

$$f_{\alpha} = (1 + h_{fe}) f_{\beta} = \left(\frac{1}{h_{fe}} + 1 \right) f_T$$

f_p (p cut-off frequency) :

It is the frequency at which common emitter sc current gain reduces by 3 dB of its value.

f_T (unity gain BW frequency) :

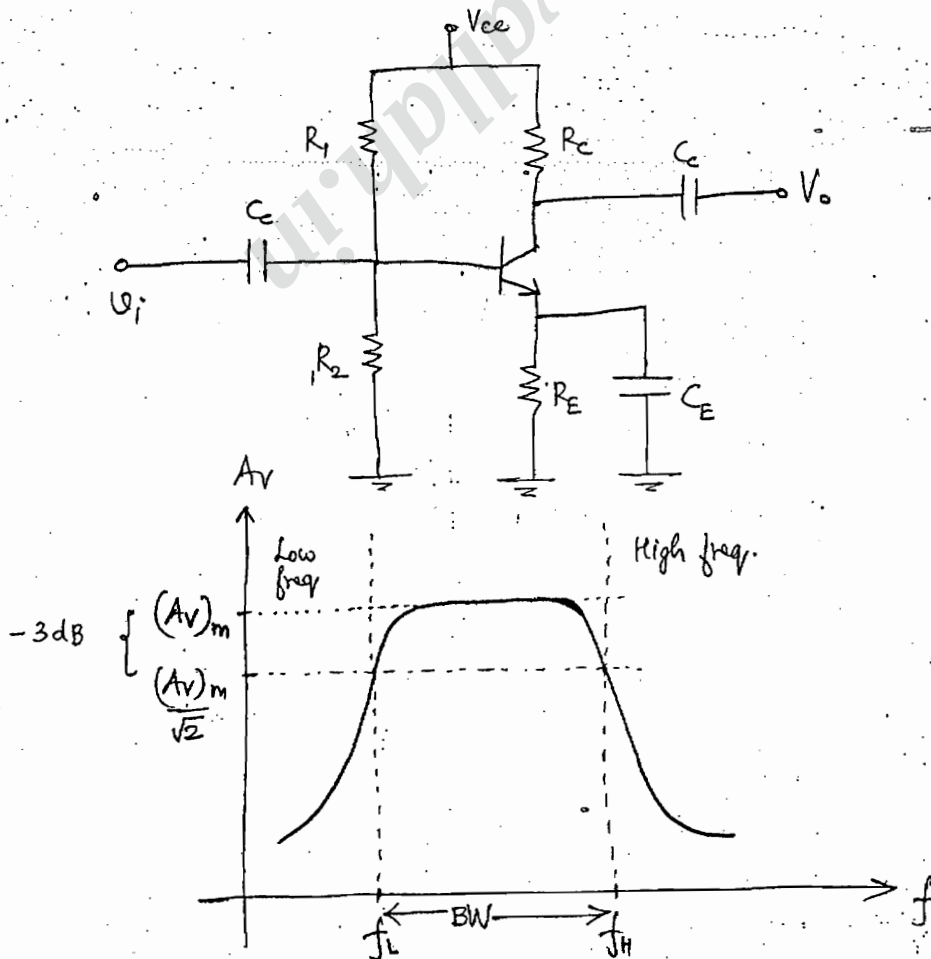
It is the frequency at which CE sc A_I falls to unity that is called as f_T .

f_α (α cut-off frequency) :

It is the frequency at which CB sc A_I reduces by 3 dB of its value.

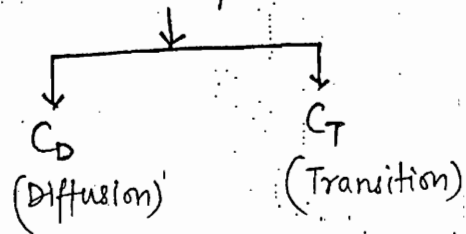
Voltage gain V_o frequency :

CE amplifier :

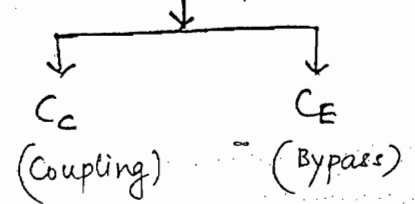


Capacitance

Internal capacitors

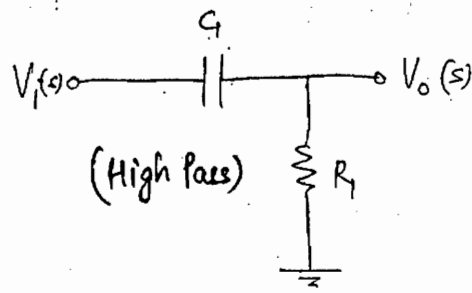


External capacitors



Low frequency Range:

- (1). At low freq. range, internal capacitors C_D & C_T which are assumed to be OC are neglected in the analysis.
- (2). But when the external capacitors C_C & C_E are assumed to be OC, they will create a problem for s/s response.
- (3). The s/s response at low freq. range is a High pass response.
- (4). The equivalent model of HP ckt is given as:



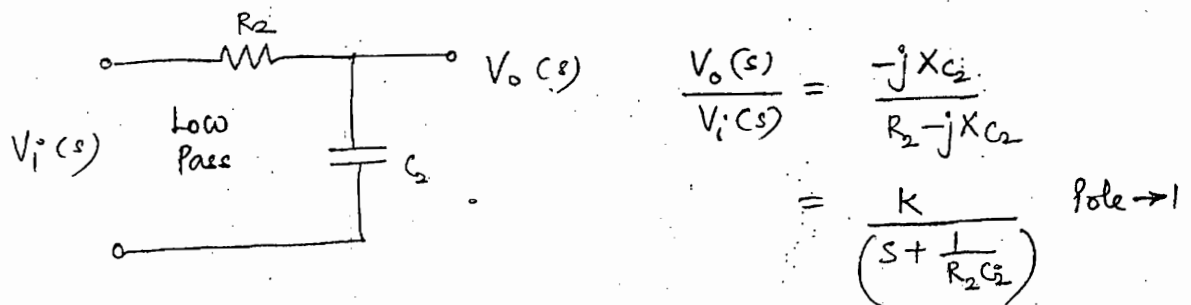
$$\begin{aligned}
 \frac{V_o(s)}{V_i(s)} &= \frac{R_1}{R_1 - jX_C} \\
 &= \frac{s}{\left(s + \frac{1}{R_1 C}\right)} \quad \begin{array}{l} \text{zero} \rightarrow 1 \\ \text{pole} \rightarrow 1 \end{array}
 \end{aligned}$$

High frequency Range:

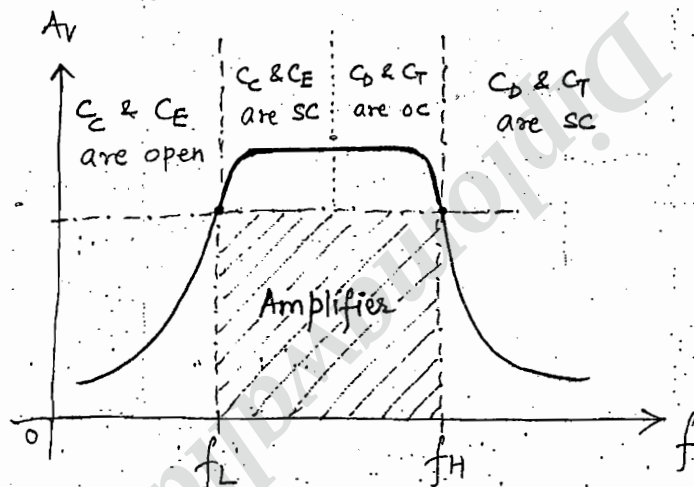
- (1). At high freq. range, external capacitors C_C & C_E which are assumed to be SC are neglected in analysis.
- (2). But when the internal capacitors C_D & C_T are assumed to be SC, they will create a problem for s/s response.

(3). The BJT response at high frequency range is a low pass response.

(4). The equivalent model of LP is given as:



Mid band range:

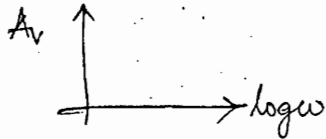


- (1). At low freq. range, mid band range frequencies are considered as high frequencies. Therefore, external capacitors C_C and C_E are assumed to be SC.
- (2). At high freq. range, mid band range frequencies are considered as low frequencies. Therefore, internal capacitors C_D & C_T are assumed to be OC.
- (3). That means in the mid band range all the internal and external capacitors are neglected. Therefore the gain is independent of frequency.

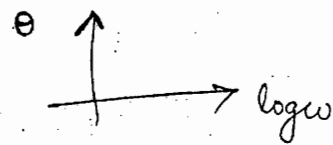
-3 dB concept:

Bode plots

Magnitude plot

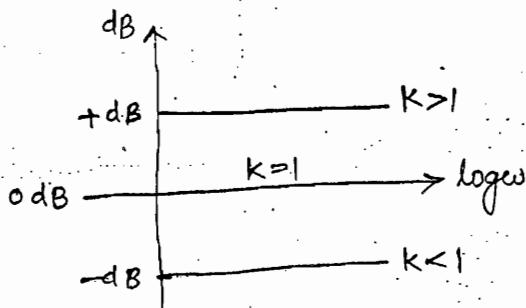


phase plot



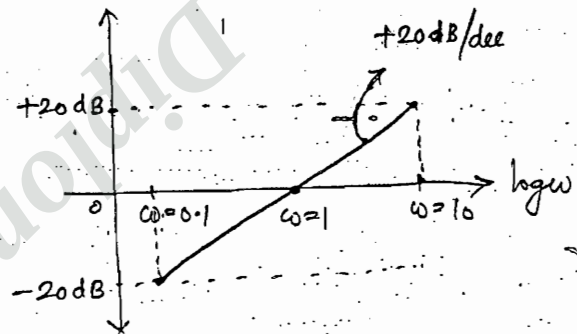
(1). $G(s) = K$ (constant)

$$|G(j\omega)|_{dB} = 20 \log K$$



(2). $G(s) = s$ (zero)

$$|G(j\omega)|_{dB} = 20 \log \omega$$



(3). $G(s) = \frac{1}{(1+sT)}$ (pole)

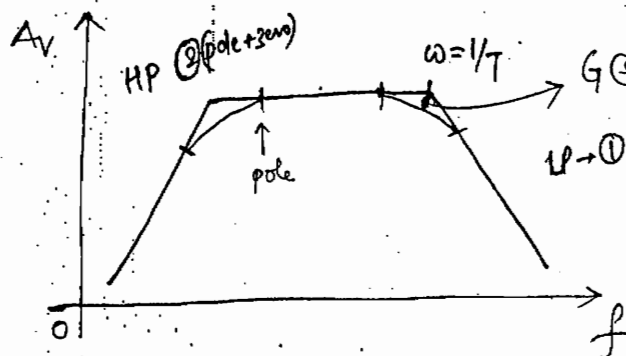
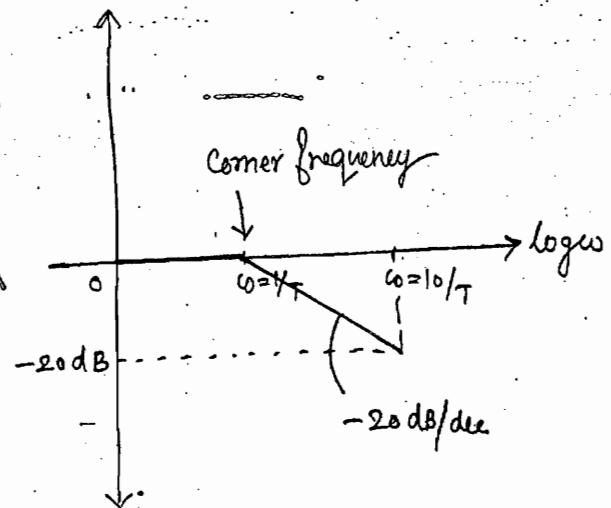
$$|G(j\omega)|_{dB} = -20 \log \sqrt{1+\omega^2 T^2}$$

$$\omega T \ll 1 \text{ (low freq.)}$$

$$|G(j\omega)|_{dB} = 0 \text{ dB}$$

$$\omega T \gg 1 \text{ (high freq.)}$$

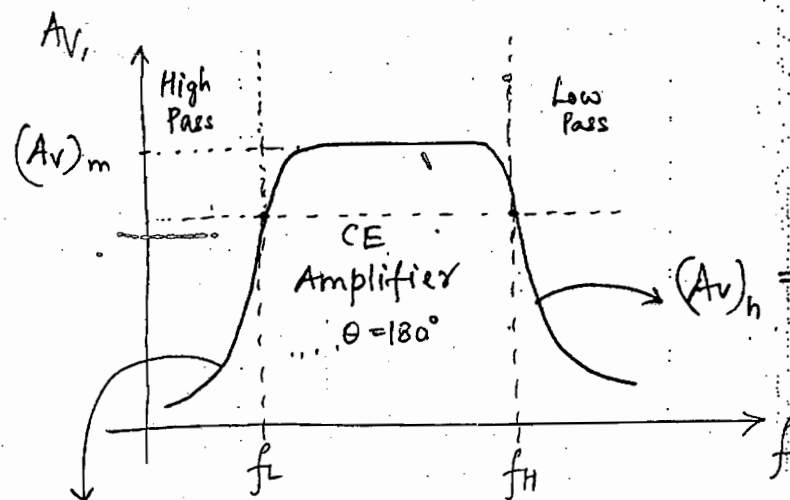
$$|G(j\omega)|_{dB} = -20 \log \omega T$$



$$G(s) = \frac{1}{1+sT}$$

$$|G(j\omega)|_{dB} = -20 \log \sqrt{1+\omega^2 T^2}$$

$$\text{At } \omega = 1/T, |G(j\omega)|_{dB} = -20 \log \sqrt{2} = -3 \text{ dB (error)}$$



$$(A_v)_L = \frac{(A_v)_m}{1 - j(f_L/f)}$$

where $f_L = \frac{1}{2\pi R_{\text{external}} C_{\text{external}}}$

$$(A_v)_H = \frac{(A_v)_m}{1 + j(f_H/f)}$$

where

$$f_H = \frac{1}{2\pi R_{\text{internal}} C_{\text{internal}}}$$

$$\theta_L = +\tan^{-1}(f_L/f)$$

when $f \rightarrow f_L$

$$\theta_L = 45^\circ$$

$$\theta_{LCE} = 180^\circ + \theta_L = 225^\circ$$

$$\theta_{LCB} = 45^\circ$$

$$\theta_H = -\tan^{-1}(f_H/f)$$

when $f \rightarrow f_H$

$$\theta_H = -45^\circ$$

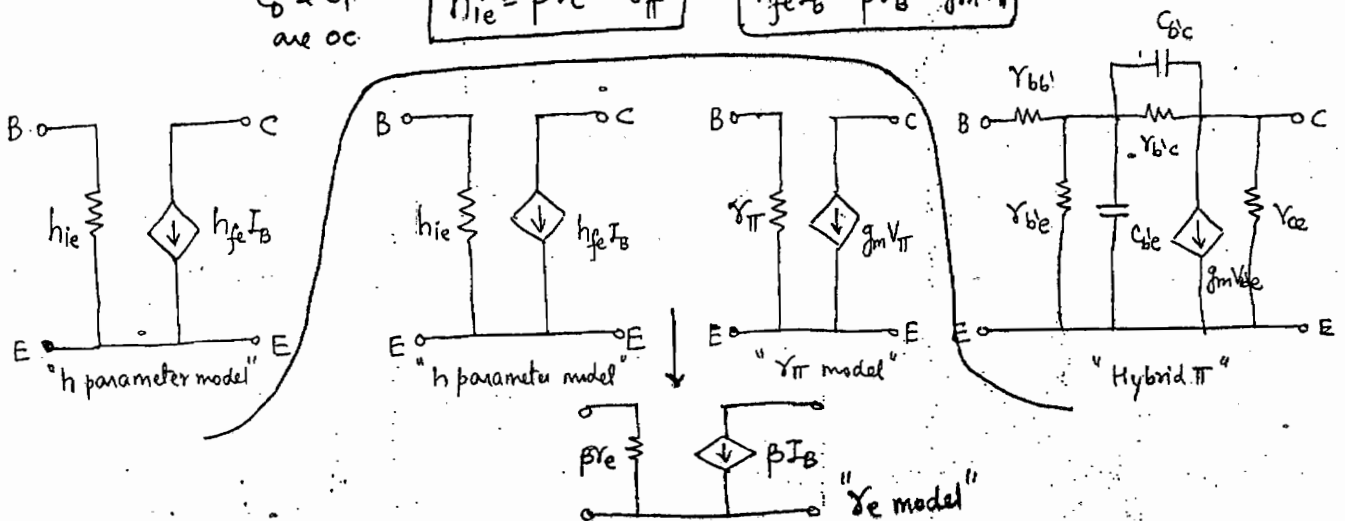
$$\theta_{HCE} = 180^\circ + \theta_H = 135^\circ$$

$$\theta_{HCB} = -45^\circ$$

C_b & C_T are ∞

$$h_{ie} = \beta r_e = r_{\pi}$$

$$h_{fe} I_B = \beta I_B = g_m V_{\pi}$$



$$A_v = -g_m R_c$$

$$A_v = -\frac{R_c}{r_e} \rightarrow "r_e"$$

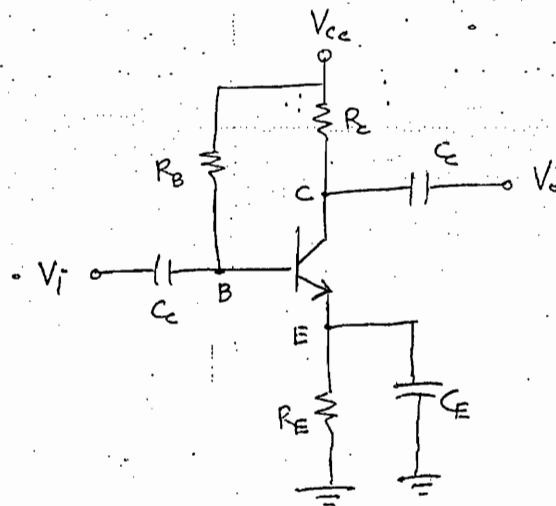
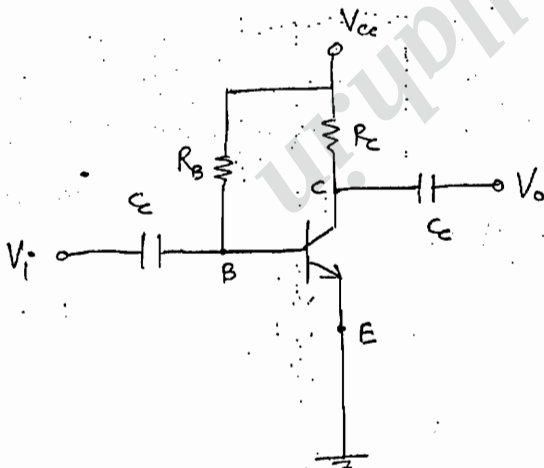
$$A_v = -\frac{h_{fe} R_c}{h_{ie}} \rightarrow "h \text{ parameter}"$$

$$A_v = -\frac{\beta R_c}{r_{\pi}} \rightarrow "r_{\pi}"$$

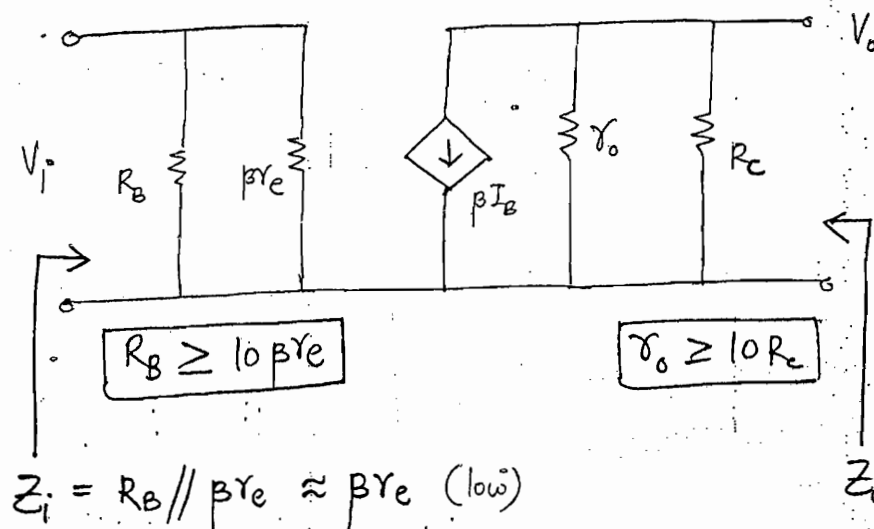
Practical Amplifier designs:

- (1). CE bypass amplifier
- (2). CE unbypass amplifier
- (3). CC amplifier
- (4). CB amplifier

(1). CE bypass amplifier:



If R_E is not equal to zero, then there must be a bypass capacitor C_E in // to it.



$$V_o = -\beta I_B R_C$$

$$I_B = \frac{V_i}{\beta R_E}$$

$$A_v = \frac{V_o}{V_i} = -\frac{R_C}{R_E} = -g_m R_C \quad \left(\propto \frac{1}{R_E} \right)$$

Heavy values of gain are unstable

i.e. if $A_v = 1000 \rightarrow$ varies (999.8 or 1000.5)

$\therefore A_v \uparrow$, stability $\downarrow$

Conclusions:

(i). Drawbacks of CE bypass amplifier:

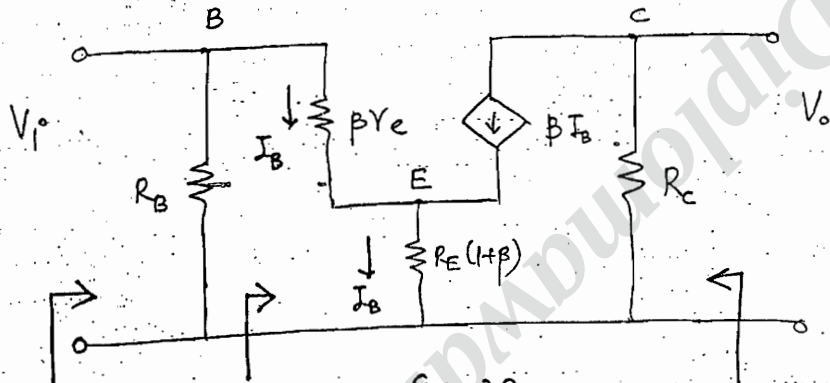
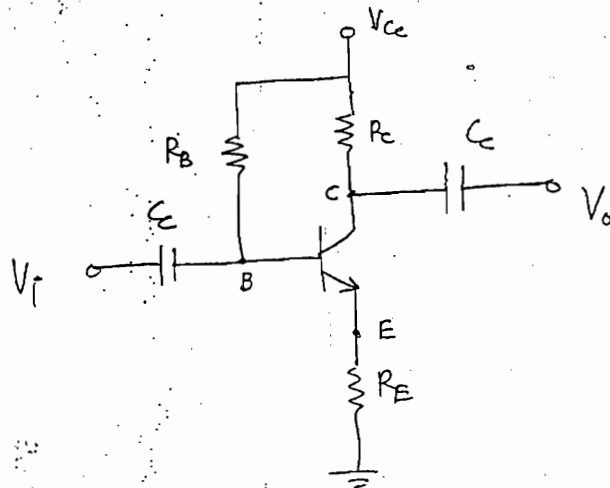
(i) the i/p impedance is low. (for an ideal amplifier i/p impedance should be ∞).

(ii) the gain is not stable becoz of dynamic resistor R_E .

* $A_v = \infty$, stability = 0

* $A_v = 0$, stability = ∞

(2) CE unbypassed amplifier:



$$Z_i' = R_B \parallel Z_i \quad Z_i = \beta r_e + (1 + \beta) R_E \approx \beta R_E \text{ (High)} \quad Z_o \approx R_C$$

Assume: $\beta = 100$, $r_e = 1 \Omega$, $R_E = 10 \text{ k}\Omega$

$$\begin{aligned} Z_i &= 100 \Omega + (101) 10 \text{ k}\Omega \\ &= 100 \Omega + 1 \text{ M}\Omega \\ &\approx 1 \text{ M}\Omega \end{aligned}$$

$$V_o = (-\beta I_B) R_C$$

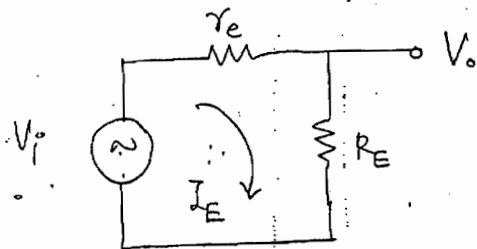
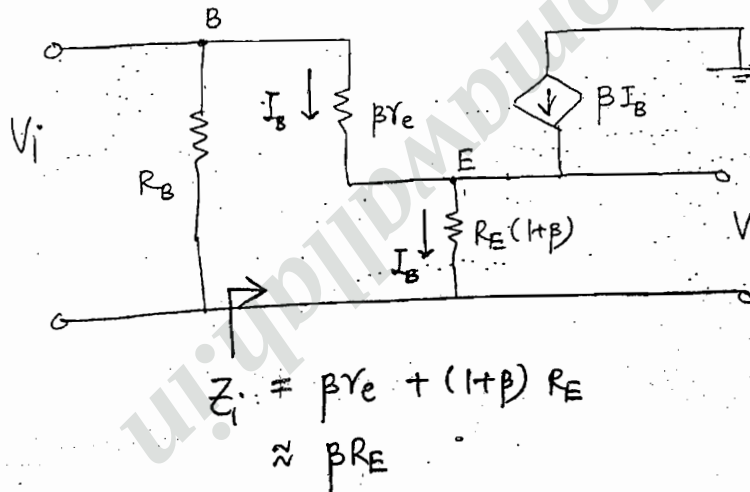
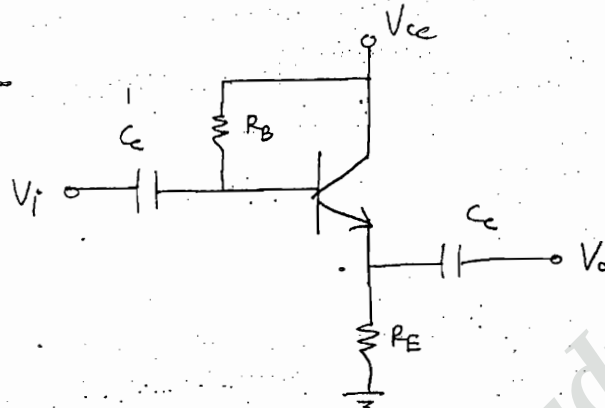
$$I_B = \frac{V_i}{\beta R_E}$$

$$A_v = \frac{V_o}{V_i} = -\frac{R_C}{R_E} \quad (\propto r_e^0)$$

As $A_v \downarrow$, stability $\uparrow$.

Conclusions:

- (1) The i/p impedance is high.
- (2) Gain is stable because of absence of dynamic resistance r_e .
- (3) Emitter follower amplifier:



$$V_o = \frac{V_i \times R_E}{(r_e + R_E)}$$

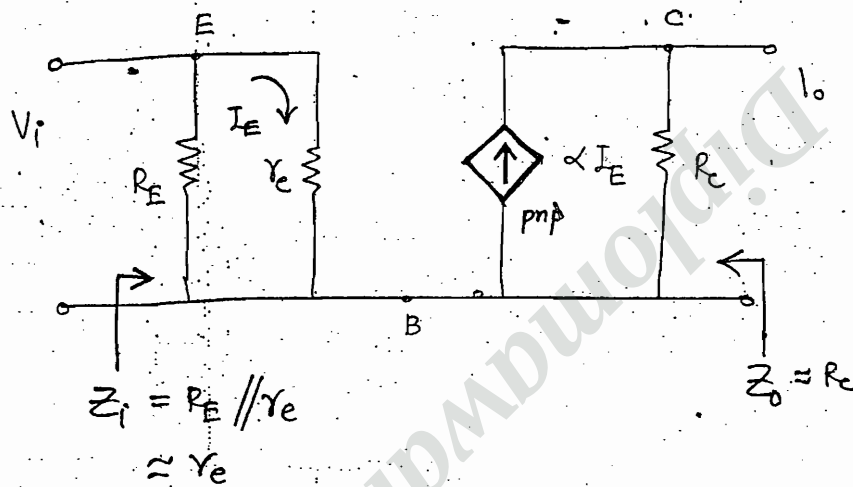
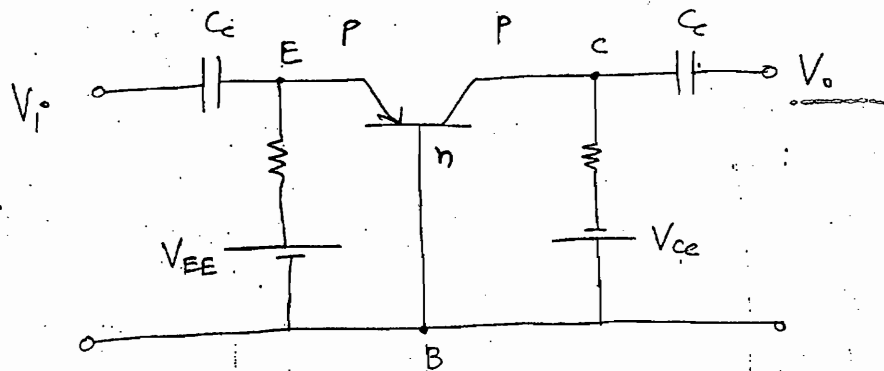
$$V_o = V_i \quad (\because R_E \gg r_e)$$

Voltage Buffer

$$Z_o = r_e \parallel R_E \approx r_e \text{ (impedance matching)}$$

Short: $A_V = \frac{V_o}{V_i} = \frac{(I_o) R_L}{(I_i) Z_i} = \frac{A_I \cdot R_L}{Z_i}$

$$\text{Here, } A_V = 1 \Rightarrow Z_i = A_I R_L = \beta \cdot R_E$$

(4). CB amplifier -

$$V_o = (\alpha I_E) R_C$$

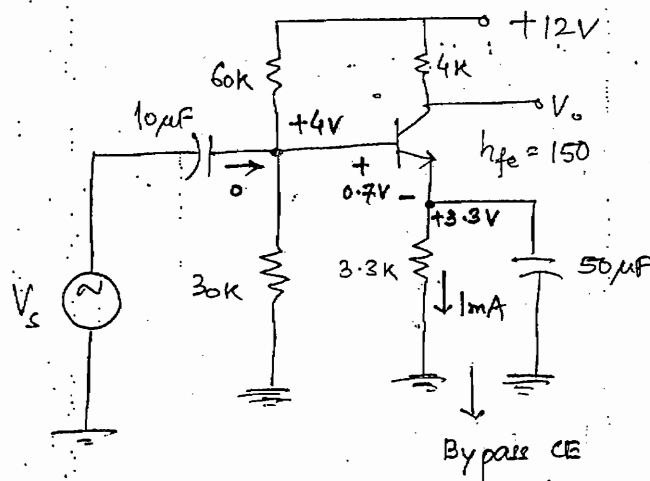
$$I_E = \frac{V_i}{r_e}$$

$$A_V = \frac{V_o}{V_i} = \frac{R_C}{r_e} \cdot \alpha \approx \frac{R_C}{r_e} \quad (\because \alpha \approx 1)$$

Problems :

CH-6 conv.

Q3.



$$\left| \frac{V_o}{V_s} \right| \approx ?$$

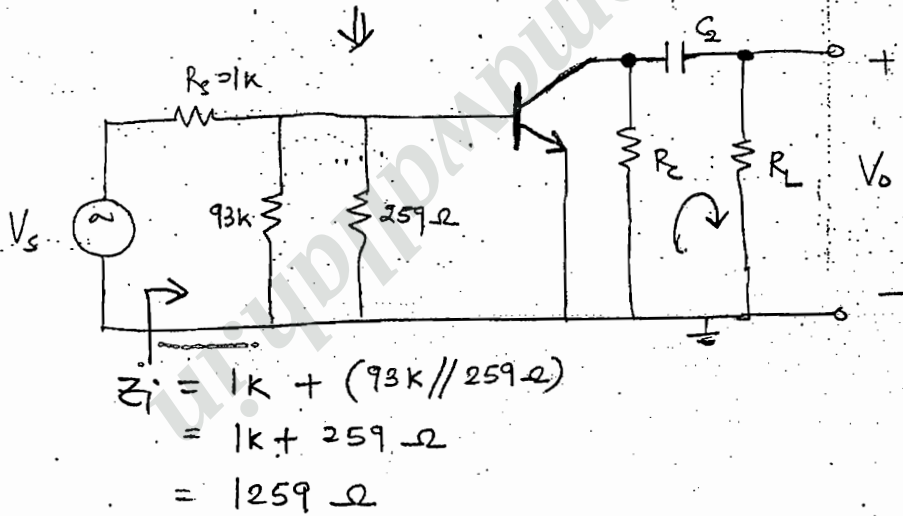
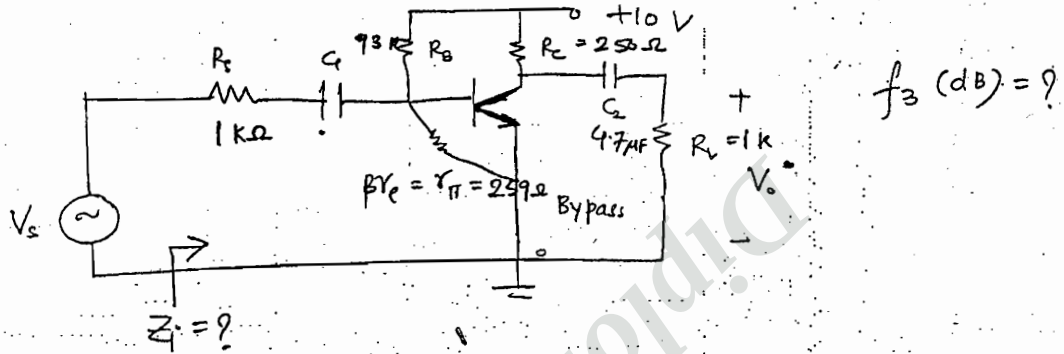
$$\text{Sol. } A_v = -\frac{R_e}{r_e} = -\frac{4K}{r_e}$$

$$r_e = \frac{V_T}{(I_c)_Q} = \frac{26 \text{ mV}}{1 \text{ mA}} = 26 \Omega$$

$$\therefore |A_v| = \left| -\frac{4K\Omega}{26\Omega} \right| = 153.8$$

$$\text{If } V_T = 25 \text{ mV}, |A_v| = \left| -\frac{4K\Omega}{25\Omega} \right| = 160$$

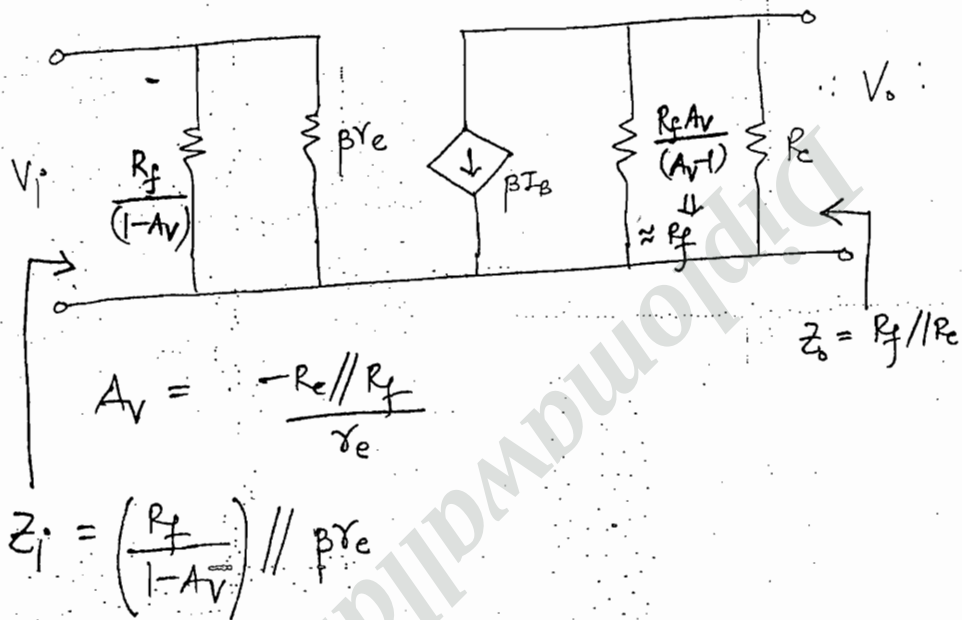
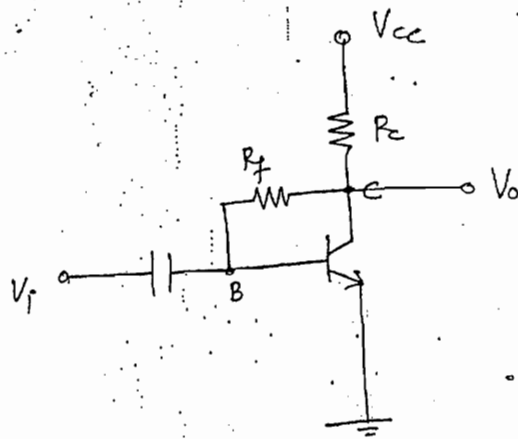
Q4.



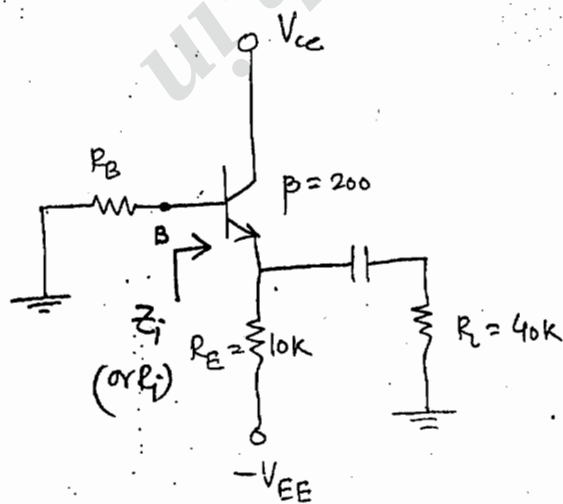
$$f_3 \text{ (dB)} = \frac{1}{2\pi (R_E + R_L) C}$$

$$= \frac{1}{2\pi (250 + 1000) \times 4.7 \times 10^{-6}}$$

$$= 27.09 \text{ Hz}$$



Q1.



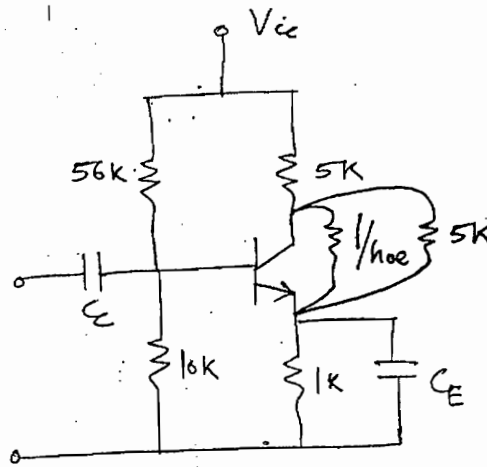
$$R_i = \beta \cdot (R_C // R_E)$$

$$= 200 \left(\frac{10k \cdot 40k}{50k} \right)$$

$$= 1600k$$

$$= 1.6 M\Omega$$

Q2.



$$h_{fe} = 100$$

$$h_{ie} = 2\text{ k}\Omega$$

$$h_{re} = 0$$

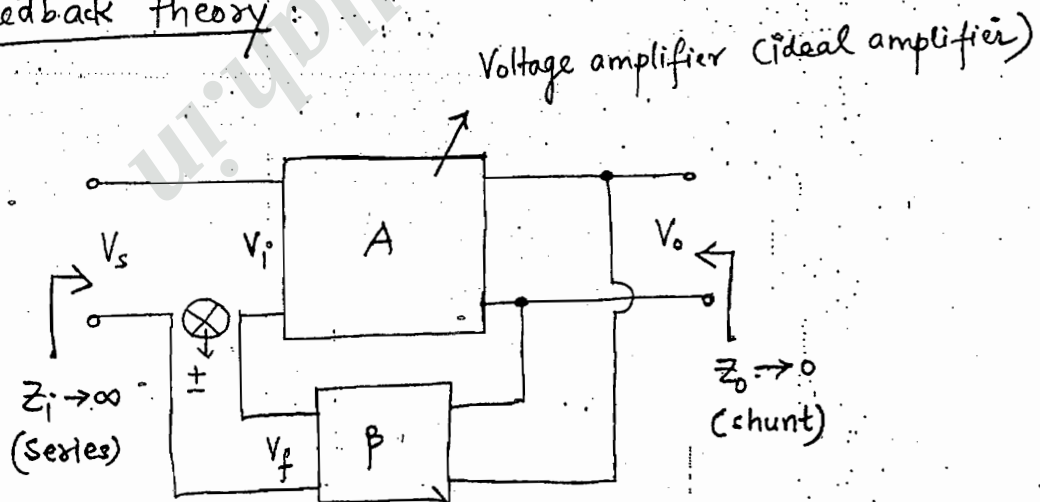
$$h_{oe} = 0.05 \text{ mho} \\ = 0.05 \times 10^{-3} \text{ S}$$

$$\begin{aligned} Z_o &= \frac{1}{h_{oe}} \parallel 5\text{ k} = \frac{1}{0.05 \times 10^{-3}} \parallel 5000 \\ &= 20\text{ k} \parallel 5\text{ k} \\ &= 4\text{ k}\Omega \end{aligned}$$

$$* f_T = \frac{1}{2\pi RC} = \frac{g_m}{2\pi(C_b + C_c)}$$

feedback amplifiers:

Feedback theory:



feedback fraction (or) feedback ratio
(or) reverse transmission factor

$$V_i = V_s + V_f \rightarrow +ve \text{ fb}$$

$$V_i = V_s - V_f \rightarrow -ve \text{ fb}$$

Positive fb

$$V_o = A V_i$$

$$V_i = V_s + V_f$$

$$V_o = A(V_s + V_f)$$

$$V_o = A(V_s + \beta V_o)$$

$$V_o(1 - \beta A) = A V_s$$

$$\boxed{\frac{V_o}{V_s} = \frac{A}{1 - \beta A}}$$

Negative fb

$$V_o = A V_i$$

$$V_i = V_s - V_f$$

$$V_o = A(V_s - V_f)$$

$$V_o = A(V_s - \beta V_o)$$

$$V_o(1 + \beta A) = A V_s$$

$$\boxed{\frac{V_o}{V_s} = \frac{A}{1 + \beta A}}$$

Conclusions:

$$(1) \quad A_{pf} > A > A_{nf}$$

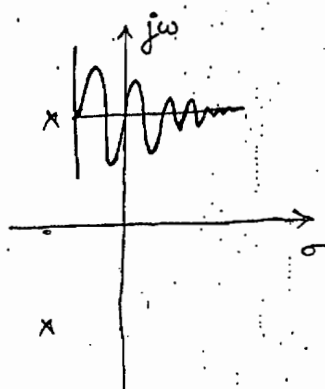
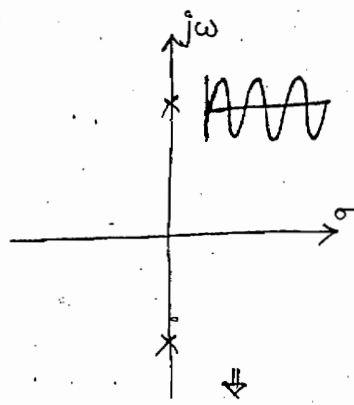
$$(2) \quad A_{nf} = \frac{A}{1 + \beta A}$$

$$\beta A \gg 1$$

$$A_{nf} \approx \frac{1}{\beta} \quad (\text{stability})$$

* -ve fb theory is applied for stable s/s like amplifiers.

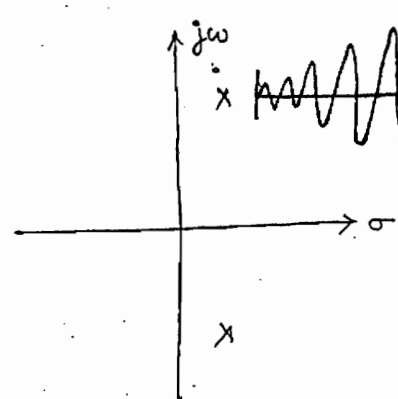
$$(3) \quad A_{pf} = \frac{A}{1 - \beta A}$$

 $\beta A < 1$  $\beta A = 1$ 

↓

Oscillators

$$A_{pf} = \frac{A}{0} \quad (\text{No } V_p)$$

 $\beta A > 1$ 

* +ve fb theory is applied for unstable s/s like oscillators.¹⁶³

Advantages of -ve fb amplifiers :

(1). Stability of ac gain :

Suppose there is a small change in the internal resistance 're' of an amplifier, then the fractional change of gain with feedback is

$$A_f = \frac{A}{1 + \beta A}$$

Differentiate w.r. to A

$$\frac{\partial A_f}{\partial A} = \frac{(1 + \beta A)(1) - A \cdot \beta}{(1 + \beta A)^2}$$

$$\frac{dA_f}{A_f} = \frac{1}{(1 + \beta A)^2} \cdot \frac{dA}{A_f}$$

$$\frac{dA_f}{A_f} = \frac{1}{(1 + \beta A)} \cdot \frac{dA}{A}$$

$$\text{fractional change of gain with fb} = \boxed{\frac{1}{(1 + \beta A)}} \cdot \text{fractional change of gain w/o fb}$$

↓
Sensitivity

$(1 + \beta A) \rightarrow$ Desensitivity

* for a good s/s, sensitivity $\downarrow \Rightarrow$ Desensitivity $\uparrow$
(ie. less sensitive to noise, temp. etc)

(2). Increase in i/p impedance :

$$V_i = V_s - V_f$$

$$V_i = V_s - \beta V_o$$

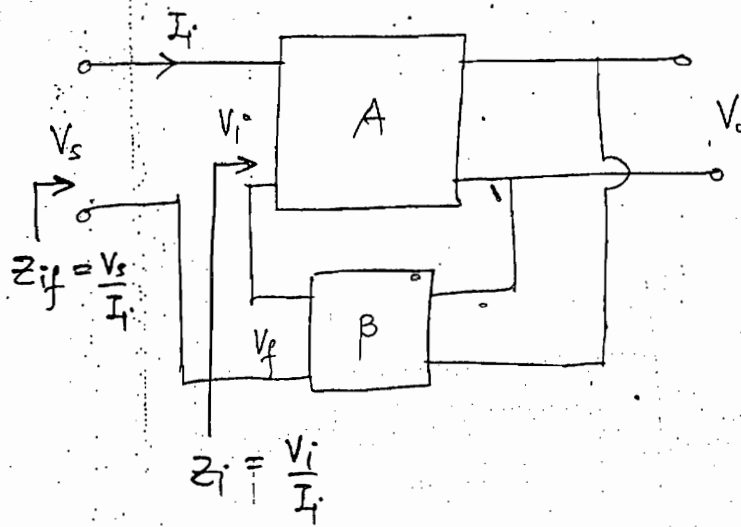
$$V_i = V_s - \beta A V_i$$

$$V_i(1 + \beta A) = V_s$$

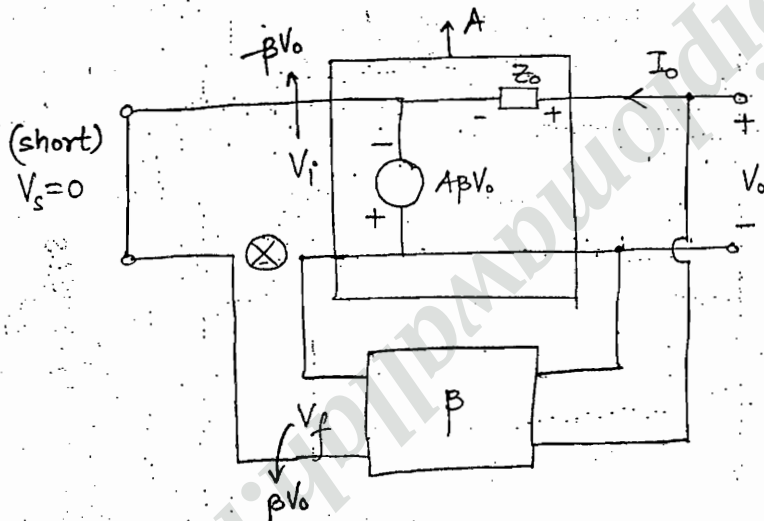
$$\frac{V_i}{I_i}(1 + \beta A) = \frac{V_s}{I_i}$$

$$Z_i(1 + \beta A) = Z_{if}$$

$$\therefore Z_{if} = (1 + \beta A) \cdot Z_i$$



(3). Decrease in o/p impedance:

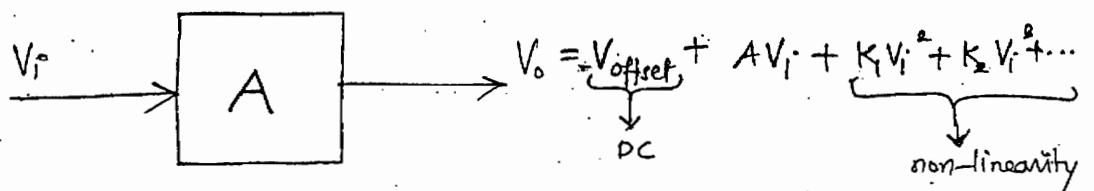


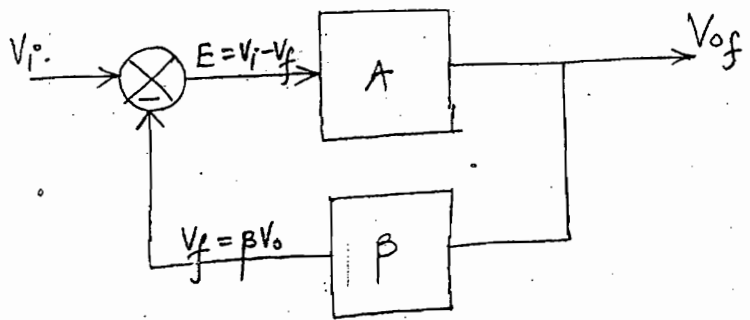
$$V_o + \beta A V_o = I_o Z_o$$

$$-V_o (1 + \beta A) = I_o Z_o$$

$$Z_{of} = \frac{V_o}{I_o} = \frac{Z_o}{(1 + \beta A)}$$

(4). Linearity:





$$V_{of} = V_{offset} + AE + K_1 E^2 + K_2 E^3 + \dots$$

$$V_{of} = V_{offset} + A(V_i - V_f) + K_1 E^2 + K_2 E^3 + \dots$$

$$V_o(1 + \beta A) = V_{offset} + AV_i + K_1 E^2 + K_2 E^3 + \dots$$

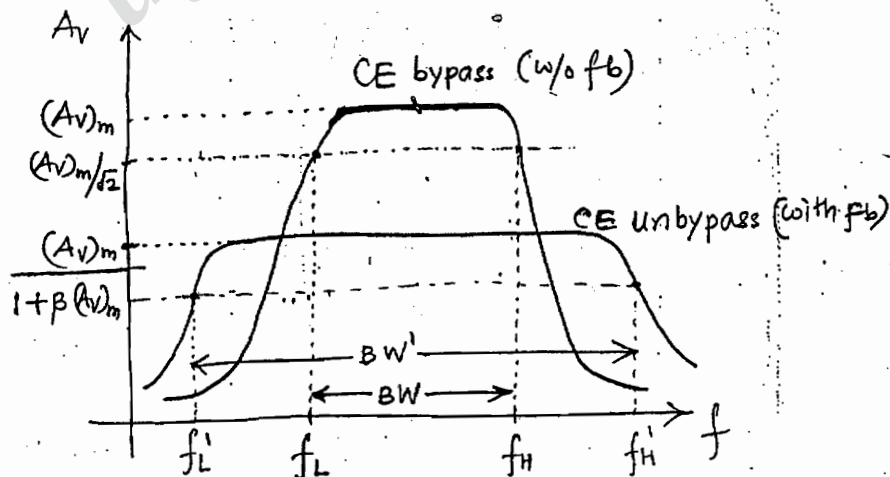
$$V_o = \frac{V_{offset}}{(1 + \beta A)} + \frac{A}{(1 + \beta A)} V_i + \frac{K_1 E^2 + K_2 E^3 + \dots}{(1 + \beta A)}$$

↓ tends to zero

$$V_o \approx \frac{A}{(1 + \beta A)} V_i \quad \text{and} \quad \beta A \gg 1$$

$$\therefore V_o \approx \frac{1}{\beta} V_i \rightarrow \text{linear relation}$$

(5) Increase in BW -



Lower cut off frequency

$$(A_v)_L = \frac{(A_v)_m}{1 - j(f_L/f)}$$

$$\therefore (A_v)_{ef} = \frac{(A_v)_L}{1 + \beta(A_v)_L}$$

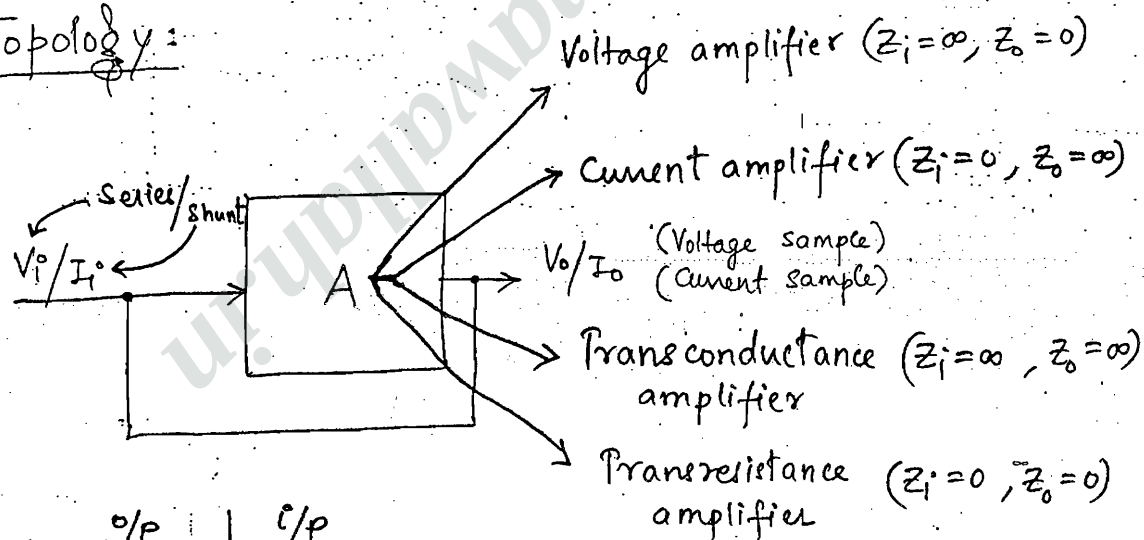
$$(A_v)_f = \frac{(A_v)_m}{1 - j(f_L/f)} = \frac{(A_v)_m}{1 + \beta \cdot \frac{(A_v)_m}{1 - j(f_L/f)}} = \frac{(A_v)_m}{1 + \beta (A_v)_m - j(f_L/f)}$$

$$(A_v)_{lf} = \frac{(A_v)_m}{1 + \beta (A_v)_m} \cdot \frac{1}{1 - j \left[\frac{f_L}{1 + \beta (A_v)_m} \right] \cdot \frac{1}{f}}$$

$$\therefore f_L' = \frac{f_L}{1 + \beta (A_v)_m}$$

Similarly, $f_H' = f_H (1 + \beta (A_v)_m)$

Topology:



- | | O/P | I/P |
|------|---------|--------|
| (1). | Voltage | Series |
| (2). | Voltage | Shunt |
| (3). | Current | Series |
| (4). | Current | Shunt |

Topology

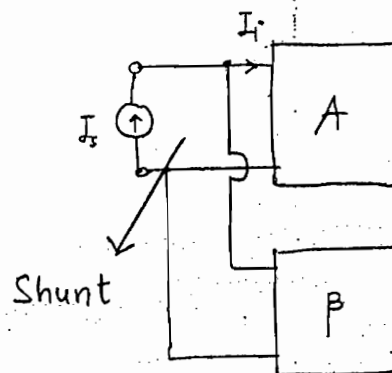
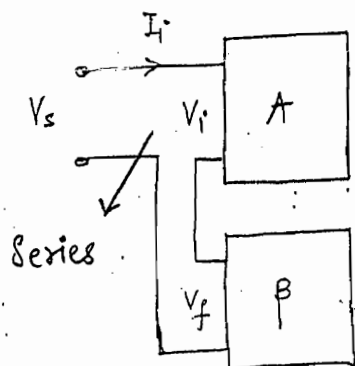
Block diagram analysis

Practical ckt analysis

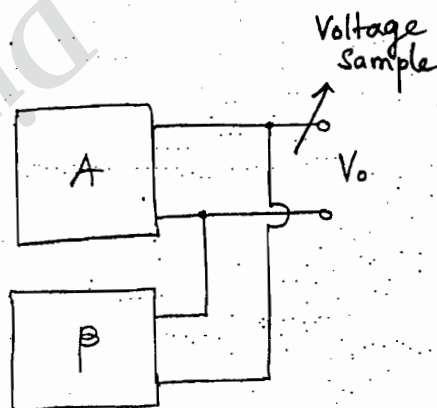
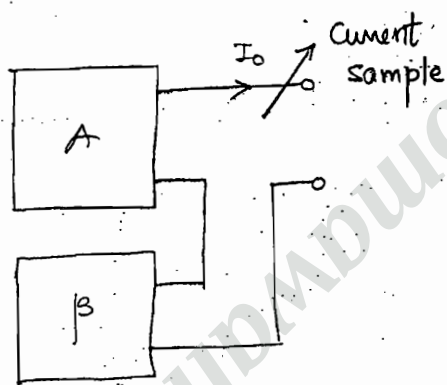
Block Diagram Analysis :

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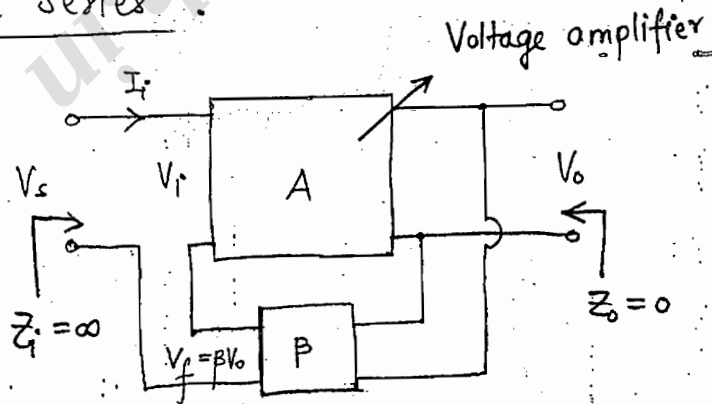
i/p side



o/p side



(1) Voltage Series :

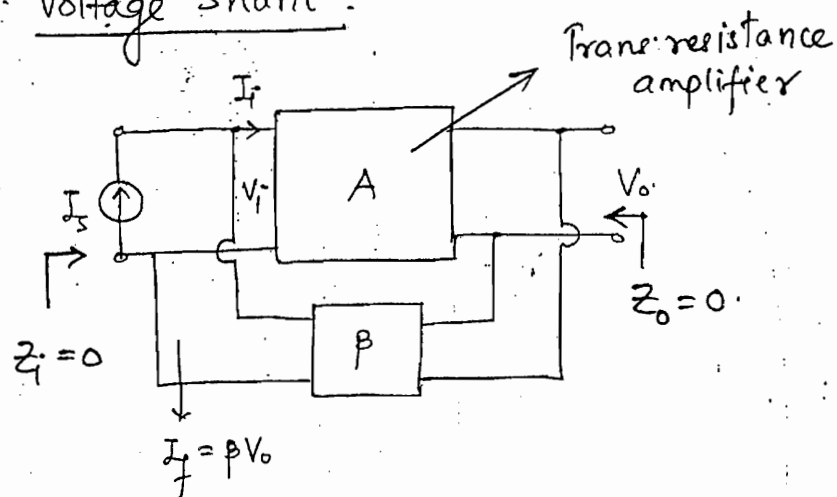


Voltage Series
(o/p) (or)

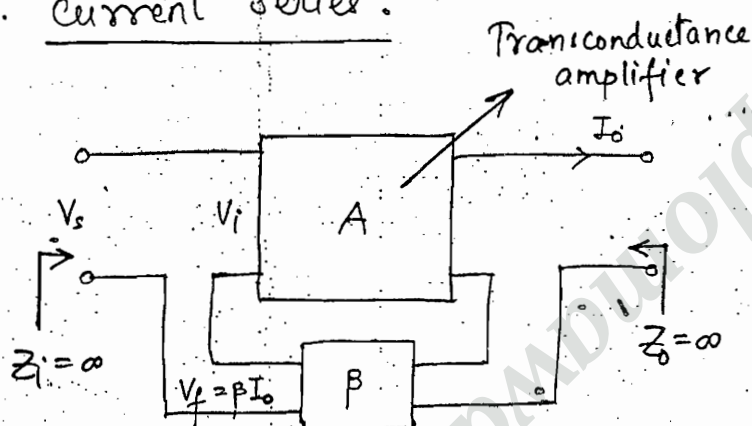
Series Voltage
(i/p) (or)

Series shunt
(i/p) (or)

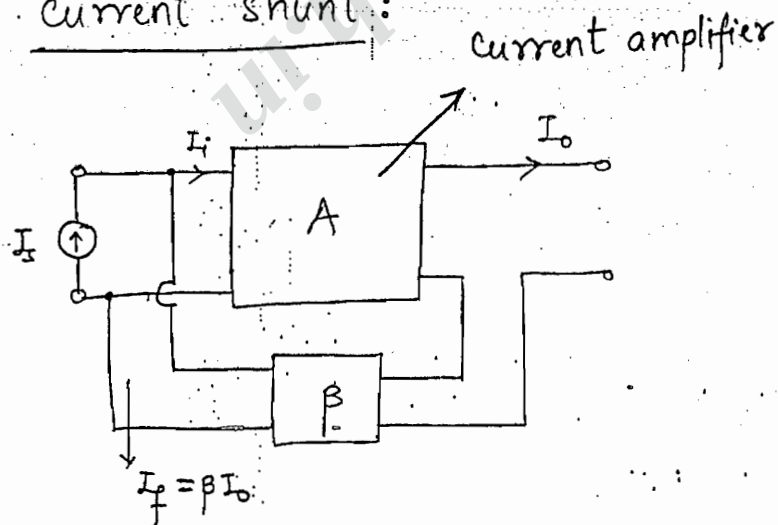
Voltage voltage
(o/p)

(2) Voltage shunt:

Voltage shunt
(or)
Shunt Voltage
(or)
Shunt shunt
(or)
Voltage current

(3) Current series:

Current series
(or)
Series current
(or)
Series series
(or)
Current voltage

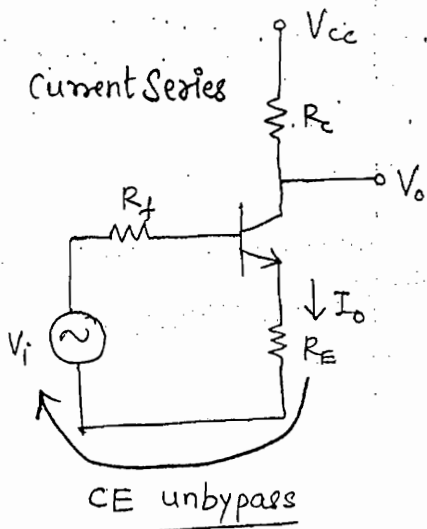
(4) Current shunt:

Current shunt
(or)
shunt current
(or)
shunt series
(or)
current current

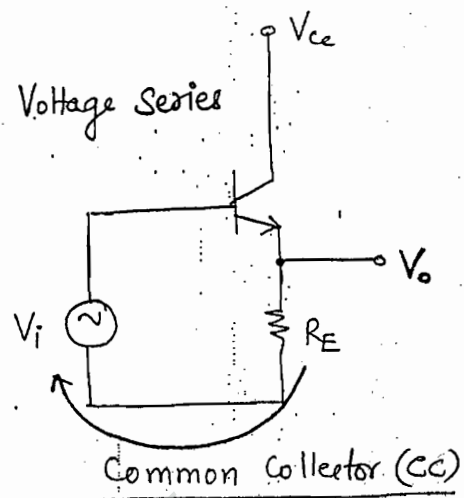
Practical ckt analysis :

169

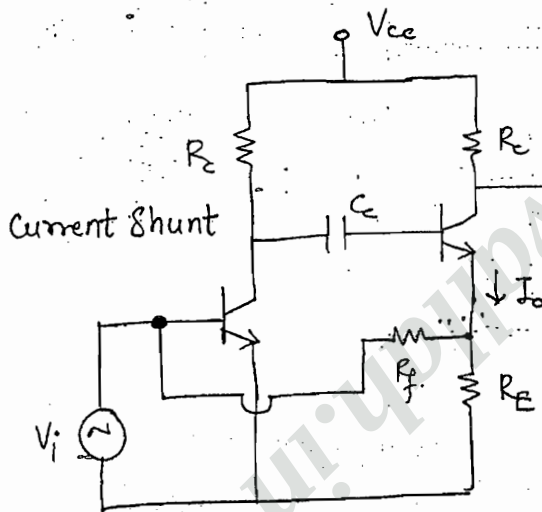
ckt-1



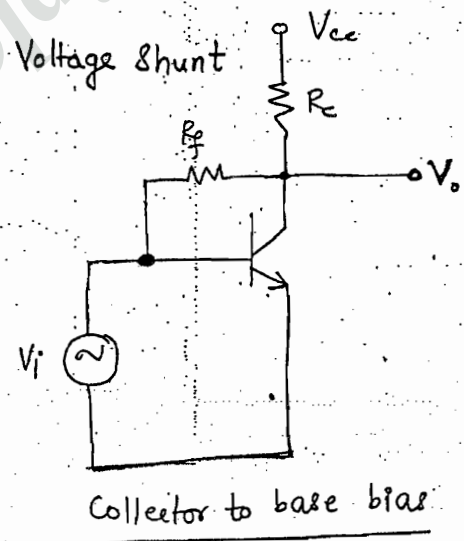
ckt-2



ckt-3

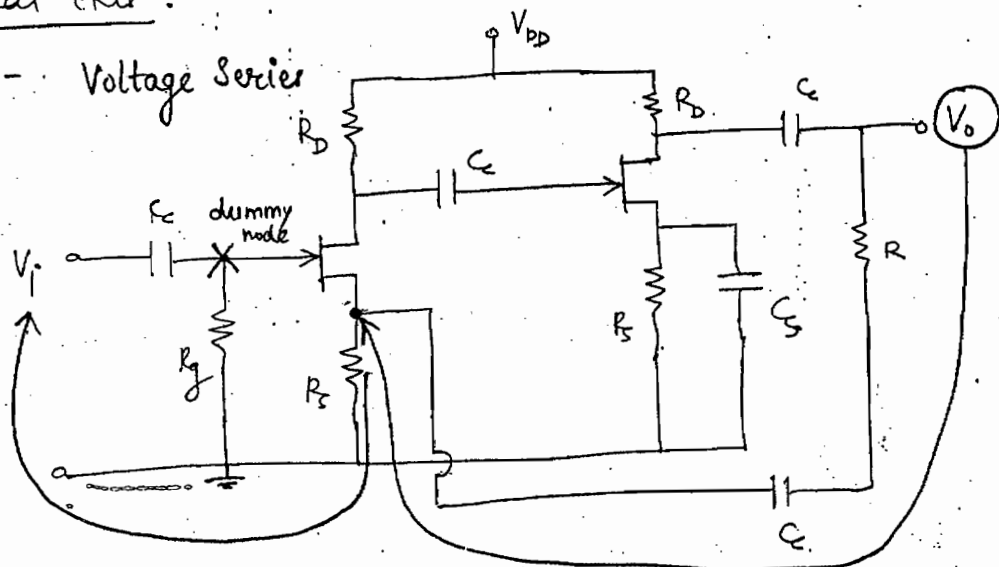


ckt-4



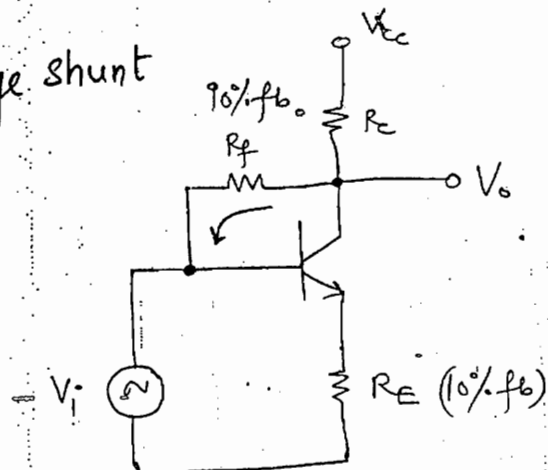
Typical ckt :

FET - Voltage Series



BJT-

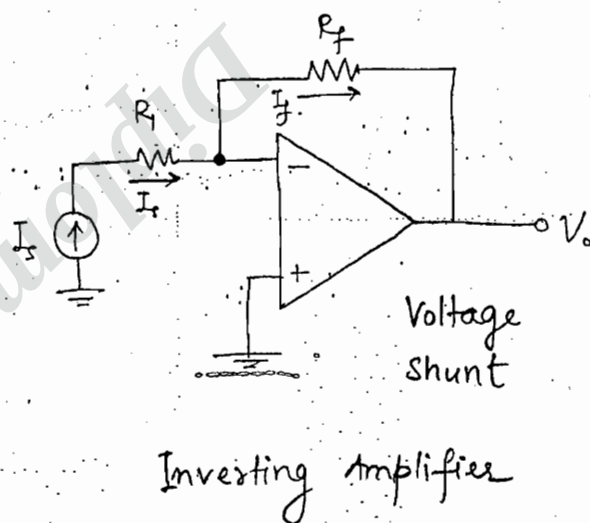
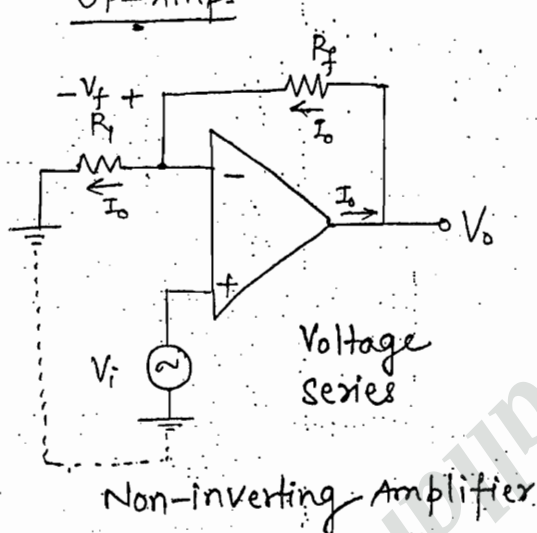
Voltage shunt



* If shunt not works completely, then series fb are required (2 things at a time).

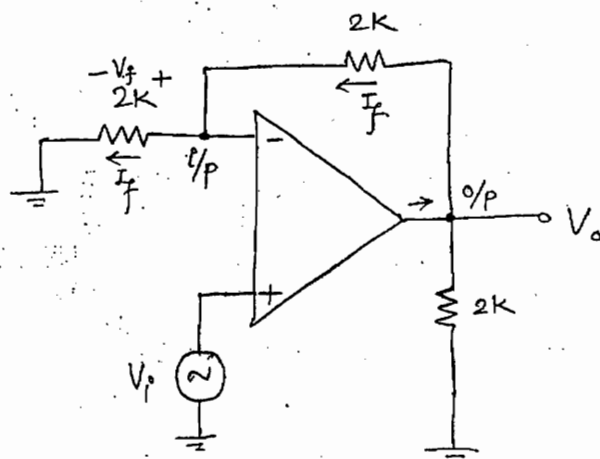
Note: If 2 fb i.e. series & shunt are connected at a time, shunt dominates series fb.

OP-Amps-



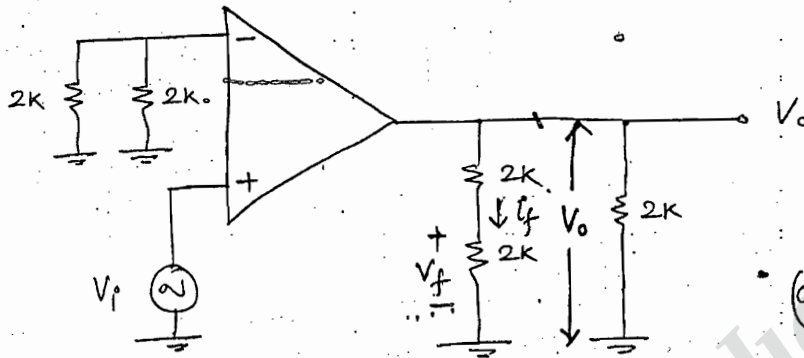
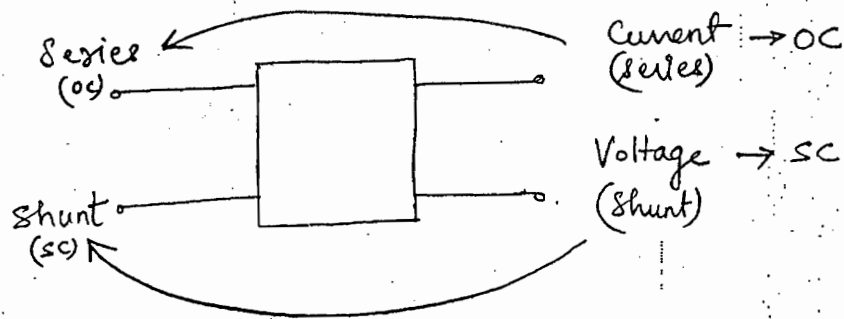
Problems in (ve) fb theory:

Model (1):



Voltage Series

Calculate A_f and β ?



$$\beta = \frac{V_f}{V_o} = \frac{2k}{2k+2k} = \frac{1}{2}$$

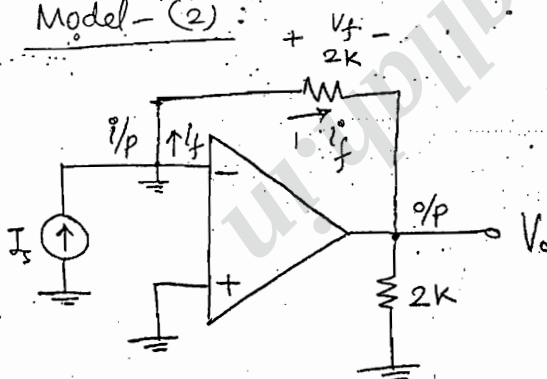
$$A_f = \frac{1}{\beta} = 2$$

$$(or) \beta = \frac{I_f}{V_o} = \frac{1}{4k\Omega}$$

or By virtual ground theory,

$$A_f = \frac{R_f}{R_i} + 1 = \frac{2k}{2k} + 1 = 2$$

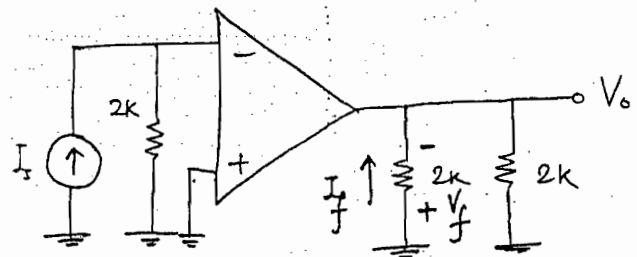
Model - (2) :



$$\beta = \frac{V_f}{V_o} = -1$$

$$(or) \beta = \frac{I_f}{V_o} = -\frac{1}{2k}$$

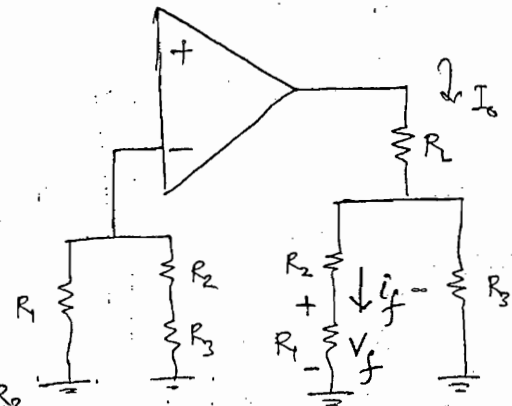
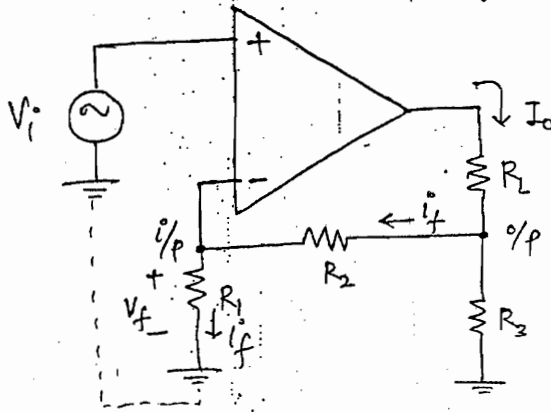
Voltage shunt



Model-(3):

$\beta = ?$

Current series



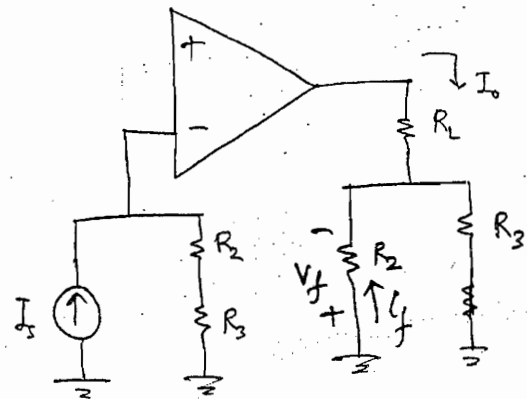
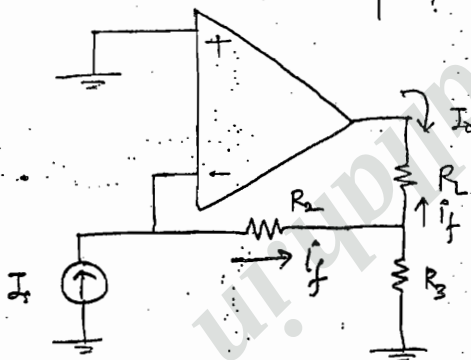
$$i_f = \frac{I_o \times R_3}{(R_1 + R_2 + R_3)} \Rightarrow \beta = \frac{i_f}{I_o} = \frac{R_3}{R_1 + R_2 + R_3}$$

$$(or) \beta = \frac{V_f}{I_o} = \frac{i_f \cdot R_1}{I_o} = \frac{R_1 R_3}{(R_1 + R_2 + R_3)}$$

Model-(4):

$\beta = ?$

Current shunt

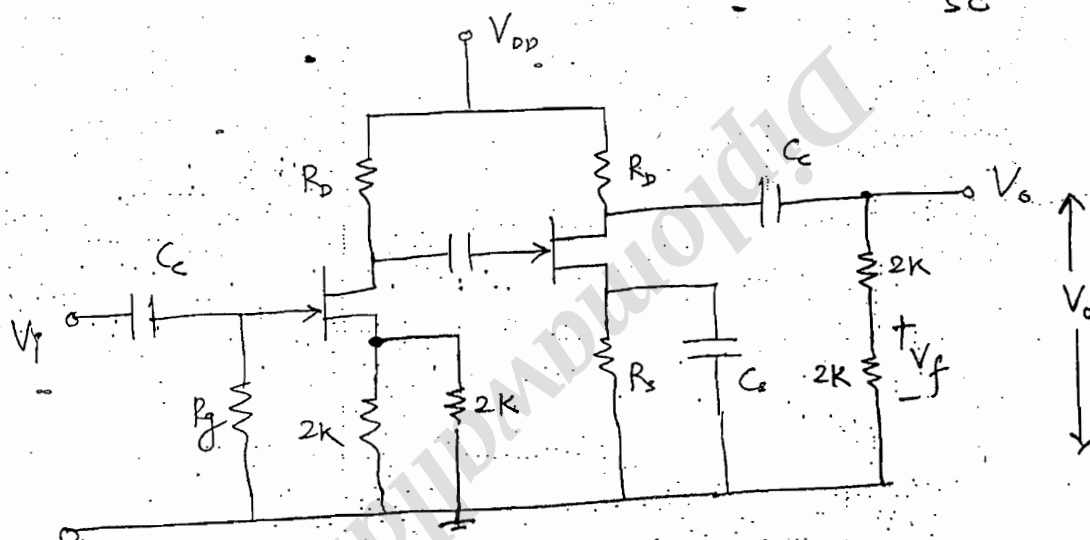
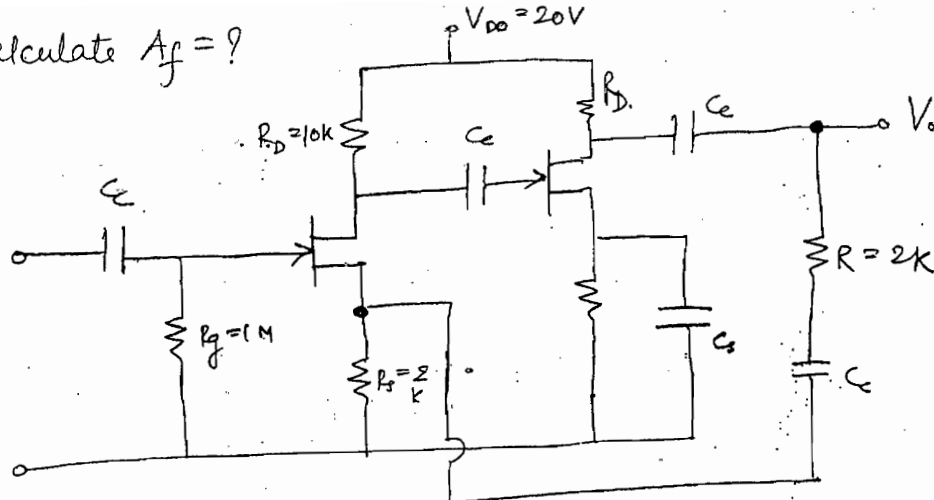


$$i_f = \frac{-I_o \times R_3}{(R_2 + R_3)} \Rightarrow \beta = \frac{i_f}{I_o} = \frac{-R_3}{R_2 + R_3}$$

$$(or) \beta = \frac{V_f}{I_o} = \frac{i_f \cdot R_2}{I_o} = \frac{-R_2 R_3}{R_2 + R_3}$$

Q. Calculate $A_f = ?$

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$$\beta = \frac{V_f}{V_o} = \frac{2K}{2K + 2K} = \frac{1}{2}$$

$$A_f = \frac{1}{\beta} = 2$$

Procedure for feedback :

- To get the i/p side, o/p should be neglected.
- for voltage series or voltage shunt, i/p node should be grounded.
- for current series or current shunt, i/p loop should be open.

to get the i/p side, i/p should be neglected.

→ for voltage series or current series, i/p loop should be open.

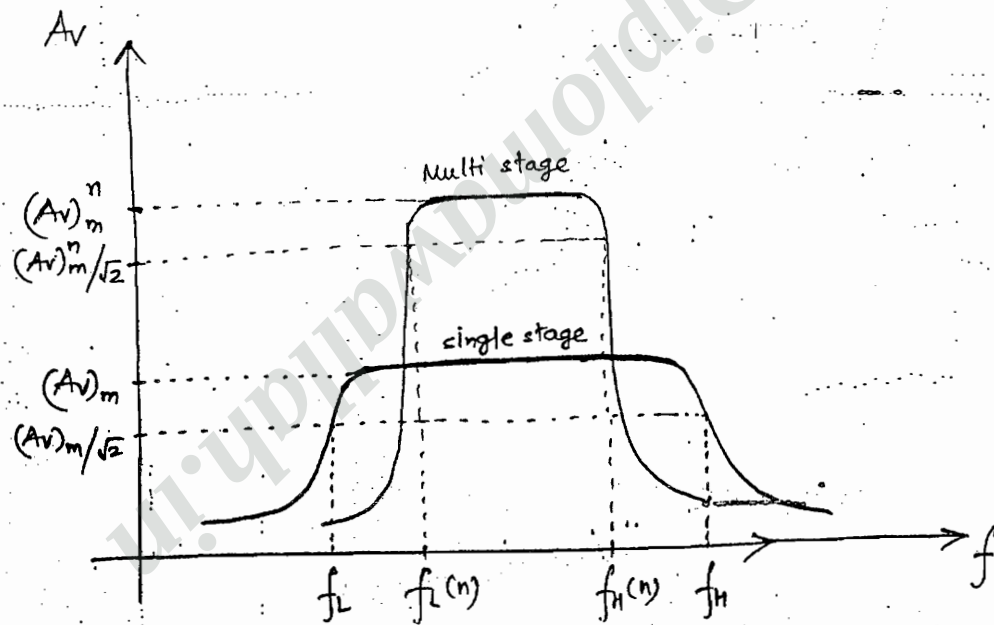
→ for voltage shunt or current shunt, i/p node should be grounded.

Integrated theory (OP-Amps):

Multistage Amplifiers:

(Integration → multistage)

Effect of cascading on BW: (identical stages)



High cut-off frequency:

Single stage → $(A_v)_h = \frac{(A_v)_m}{1 + j(f/f_H)}$

$$\left| \frac{(A_v)_h}{(A_v)_m} \right| = \frac{1}{\sqrt{1 + (f/f_H)^2}}$$

n identical stages → $\left| \frac{(A_v)_h}{(A_v)_m} \right|^n = \left| \frac{1}{\sqrt{1 + (f/f_H)^2}} \right|^n$

When $f \rightarrow f_H^{(n)}$

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$$\left| \frac{1}{\sqrt{1 + \left(\frac{f_H^{(n)}}{f_H} \right)^2}} \right|^n = \frac{1}{\sqrt{2}}$$

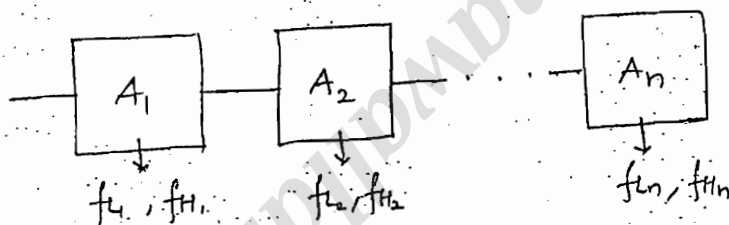
$$1 + \left\{ \frac{f_H^{(n)}}{f_H} \right\}^2 = 2^{1/n}$$

$$f_H^{(n)} = \sqrt{2^{1/n} - 1} \cdot f_H$$

Similarly,

$$f_L^{(n)} = \frac{f_L}{\sqrt{2^{1/n} - 1}}$$

Effect of cascading on BW : (Non identical stages)



$$A^n = A_1 \cdot A_2 \cdot A_3 \cdots A_n$$

$$A_L^n = A_{L1} \cdot A_{L2} \cdot A_{L3} \cdots A_{Ln}$$

$$A_H^n = A_{H1} \cdot A_{H2} \cdot A_{H3} \cdots A_{Hn}$$

$$\frac{A^n}{\sqrt{1 + \left(\frac{f_L^n}{f} \right)^2}} = \frac{A_1}{\sqrt{1 + \left(\frac{f_{L1}}{f} \right)^2}} \cdot \frac{A_2}{\sqrt{1 + \left(\frac{f_{L2}}{f} \right)^2}} \cdots \frac{A_n}{\sqrt{1 + \left(\frac{f_{Ln}}{f} \right)^2}}$$

$$1 + \left(\frac{f_L^n}{f} \right)^2 = 1 + \left(\frac{f_{L1}}{f} \right)^2 + \left(\frac{f_{L2}}{f} \right)^2 + \cdots + \left(\frac{f_{Ln}}{f} \right)^2 + \underbrace{\left(\frac{f_1 f_2}{f^2} \right)^2 + \cdots}_{\text{neglected}}$$

$$f_L^n = \sqrt{f_{L1}^2 + f_{L2}^2 + \cdots + f_{Ln}^2}$$

Similarly, $f_H^n = \frac{1}{\sqrt{\left(\frac{1}{f_{H1}} \right)^2 + \left(\frac{1}{f_{H2}} \right)^2 + \cdots + \left(\frac{1}{f_{Hn}} \right)^2}}$

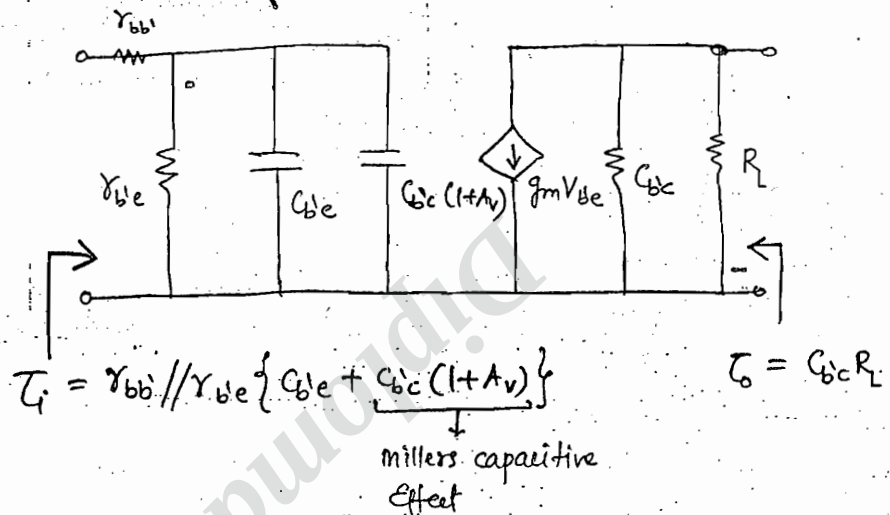
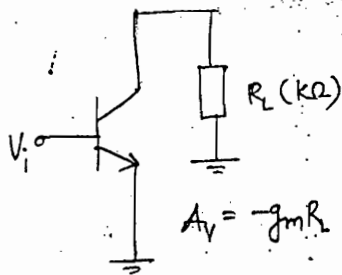
Important cascading designs:

(1). Cascode amplifiers : (CE - CB) (Wide Band Amplifiers) ¹⁷⁶

(2). Darlington pair : (cc - ce) (High γ_p impedance ckt)

(1). Cascode amplifiers (CE - CB)

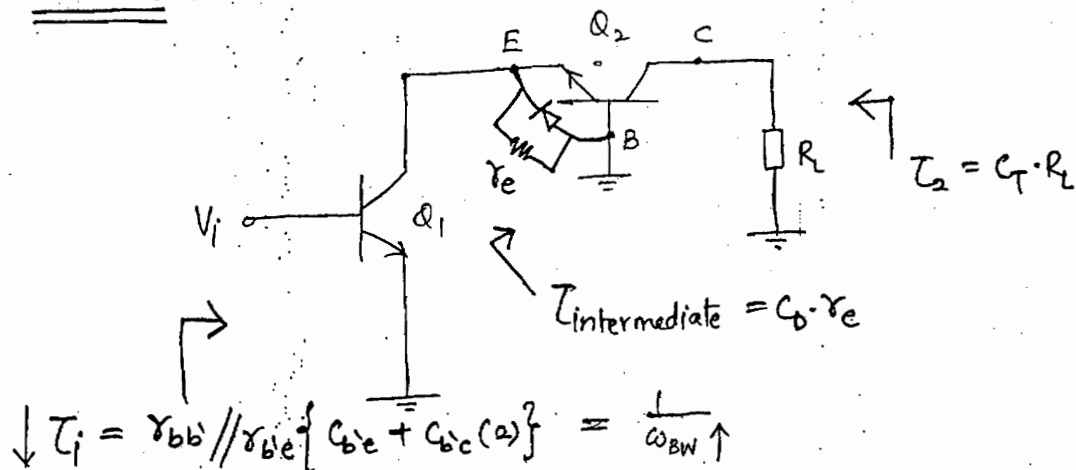
CE :



As $A_v = 1000 \therefore T_i \uparrow = \frac{1}{\omega_{BW} \downarrow} \quad (T_i \gg T_o)$

To reduce, T_i there must be reduction in A_v to improve ω_{BW} .

CE - CB :



$$\downarrow T_i = r_{bb'} // r_{be} \{ C_{be} + C_{bc}(a) \} = \frac{1}{\omega_{BW} \uparrow}$$

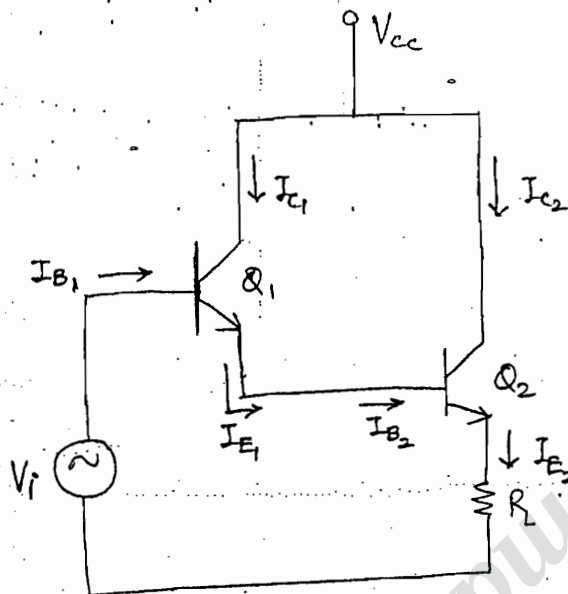
$$\begin{aligned} A_{v1} &= -g_m R_L \\ &= -g_m r_e \\ &= -\frac{r_e}{r_o} = 1 \end{aligned}$$

$$A_{v2} = g_m R_L$$

$$\begin{aligned} A_{cascode} &= A_{v1} \cdot A_{v2} \\ &= -1 \cdot g_m R_L \\ &= -g_m R_L \quad (= A_{v_{CE}}) \end{aligned}$$

Hence, with CE-CB, ω_{BW} increases with keeping the gain ($A_{\text{cascade}} = A_V = -g_m R_L$) same as with single CE.

(2). Darlington pair: (CC-CC)



$$(4). Z_o = R_E \parallel Y_e \approx Y_e$$

$$\therefore Z_o = Y_e \rightarrow \text{Impedance matching}$$

$$(1). A_I = A_{I1} \cdot A_{I2}$$

$$\begin{aligned} A_{I1} &= \frac{I_{E1}}{I_{B1}} \\ &= \frac{I_{B1} + I_{C1}}{I_{B1}} \\ &= 1 + \frac{I_{C1}}{I_{B1}} \\ &= 1 + \beta \end{aligned}$$

$$\begin{aligned} A_{I2} &= \frac{I_{E2}}{I_{B2}} \\ &= \frac{I_{B2} + I_{C2}}{I_{B2}} \\ &= 1 + \frac{I_{C2}}{I_{B2}} \\ &= 1 + \beta \end{aligned}$$

$$\therefore A_I = (1 + \beta)^2 \approx \beta^2 \Rightarrow \boxed{A_I = \beta^2}$$

$$(2). A_V = A_{V1} \cdot A_{V2}$$

$$= 1 \cdot 1$$

$$\therefore \boxed{A_V = 1} \rightarrow \text{Buffer action}$$

$$(3). Z_i = A_I \cdot R_L$$

$$\therefore \boxed{Z_i = \beta^2 \cdot R_L}$$

$$\text{Assume: } \beta = 100, R_L = 1 \text{ k}\Omega$$

$$Z_i = (100)^2 \cdot 1 \text{ k}\Omega = 10 \text{ M}\Omega \text{ (High)}$$

Disadvantages:

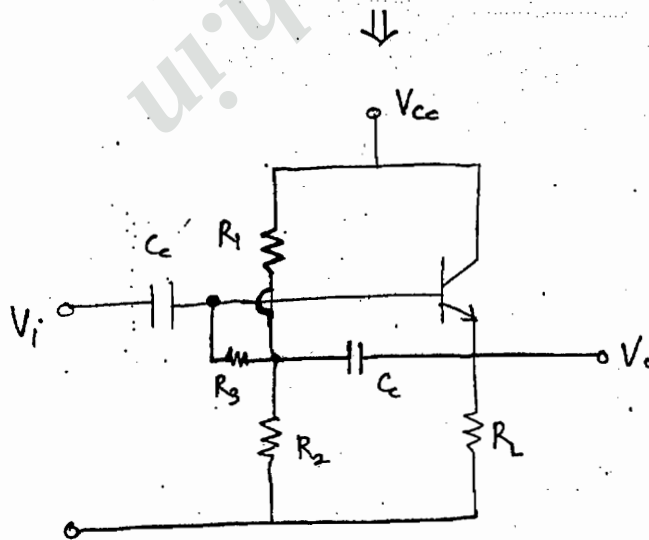
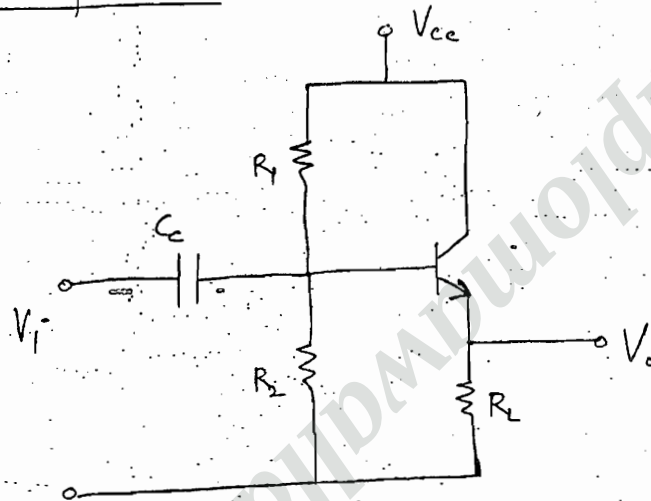
- (1). In practical design, the i/p impedance will be low for a darlington pair becoz of biasing resistors.

eg. $Z_i \parallel R_1 \parallel R_2$ if $R_1 = 100k \Omega$
 $R_2 = 10k \Omega$

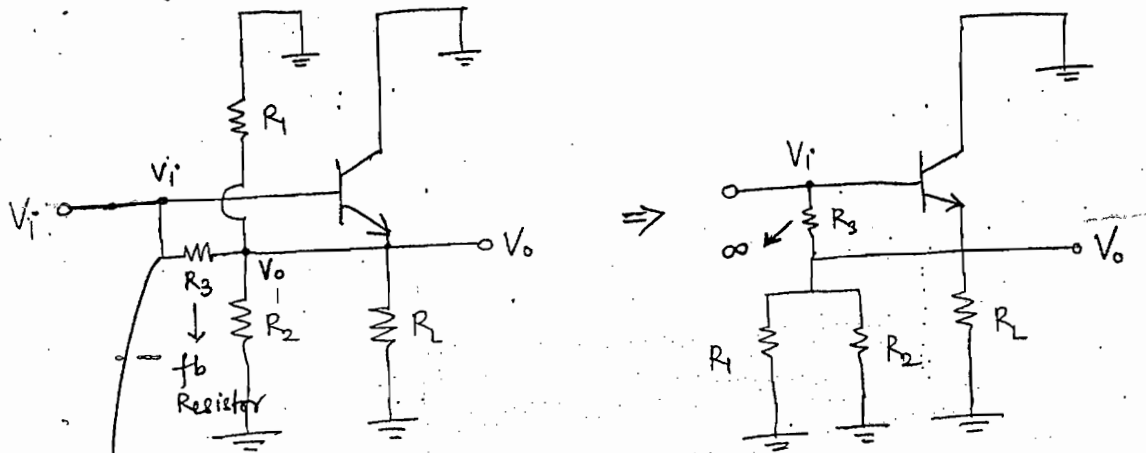
$$Z_i = Z_i \parallel 100k \parallel 10k$$

$$\approx 10k$$

Emitter follower : Boot strap

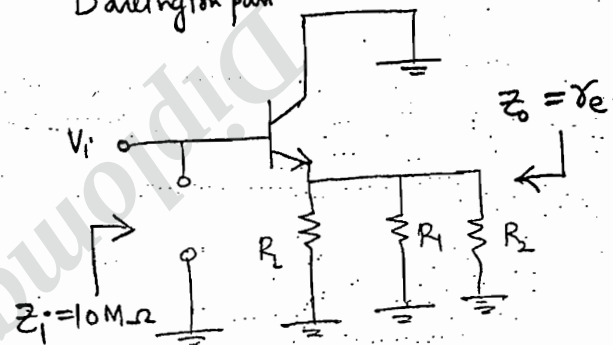


Voltage Shunt



$$R_{3i} = \frac{R_3}{1-A_v} \approx \frac{R_3}{1-1} \approx \infty$$

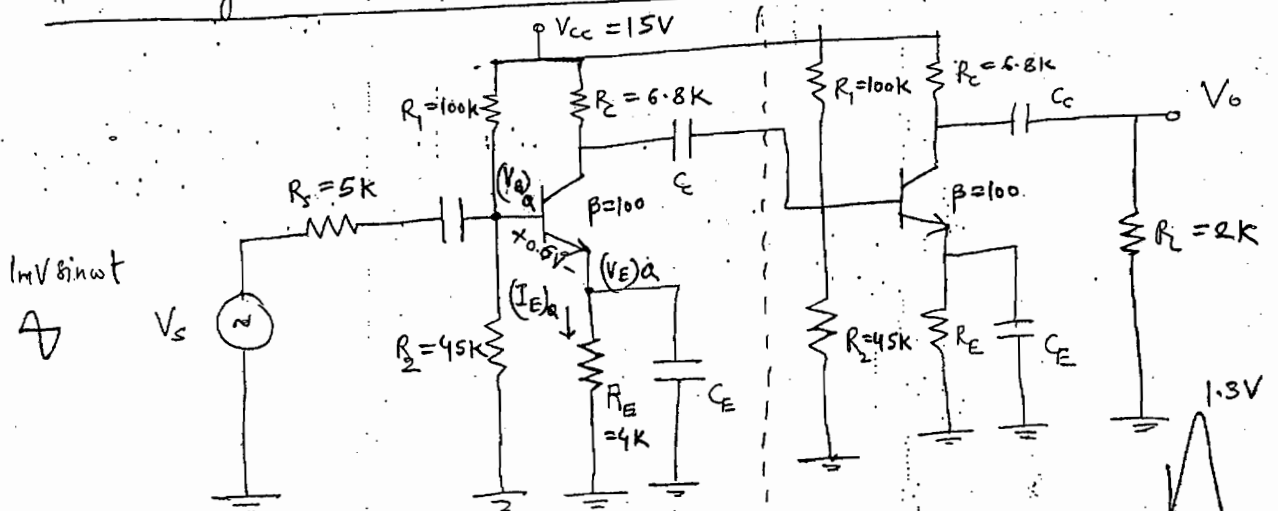
Darlington pair



Coupling designs:

- (1). RC coupling
- (2). Direct coupling

Two stage RC coupled CE amplifier:



Calculate : $A_{Vs} = \frac{V_o}{V_s}$

DC analysis: Q_1 & Q_2

$$(V_B)_Q = \frac{15 \times 45K}{(100K + 45K)} = 4.6V$$

$$(V_E)_Q = 4.6 - 0.6 = 4V$$

$$(I_E)_Q = \frac{(V_E)_Q}{R_E} = \frac{4}{4K} = 1mA$$

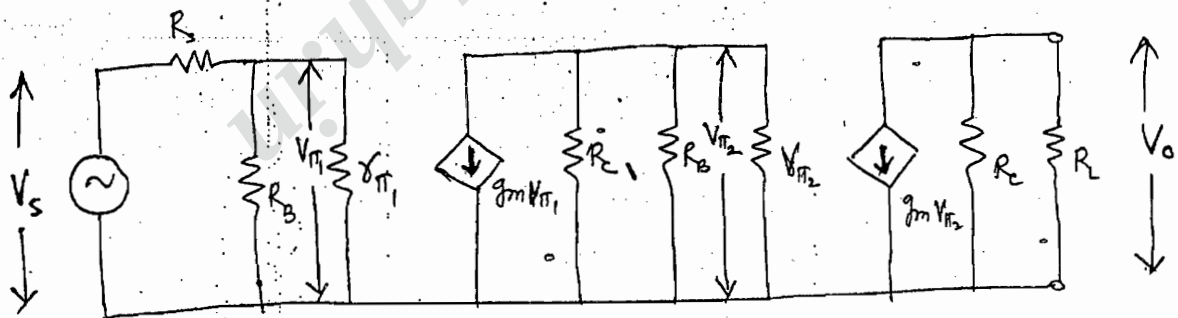
$$g_m = \frac{(I_C)_Q}{V_T} \approx \frac{1mA}{25mV} = 40mS$$

$$r_e = \frac{1}{g_m} = 25\Omega$$

$$\beta r_e = r_{\pi} = h_{ie} = 100 \times 25 = 2.5K\Omega$$

AC analysis:

$$R_B = R_1 // R_2 = 100K // 45K = 32K$$



$$A_{VS} = \frac{V_o}{V_s} = \frac{V_o}{V_{\pi 2}} \cdot \frac{V_{\pi 2}}{V_{\pi 1}} \cdot \frac{V_{\pi 1}}{V_s}$$

$$\frac{V_{\pi 1}}{V_s} = \frac{R_B // r_{\pi 1}}{R_B // r_{\pi 1} + R_s} = \frac{32K // 2.5K}{32K // 2.5K + 5K} = 0.316$$

$$V_{\pi 2} = -g_m V_{\pi 1} (R_C // R_B // r_{\pi 2})$$

$$\frac{V_{\pi 2}}{V_{\pi 1}} = -g_m (R_C // R_B // r_{\pi 2}) = -40mS (6.8K // 32K // 2.5K) = -69$$

$$V_o = -g_m V_{\pi_2} (R_c \parallel R_L)$$

$$\frac{V_o}{V_{\pi_2}} = -g_m (R_c \parallel R_L) = -40 \text{ mS} (6.8 \text{ k} \parallel 2 \text{ k}) = -61$$

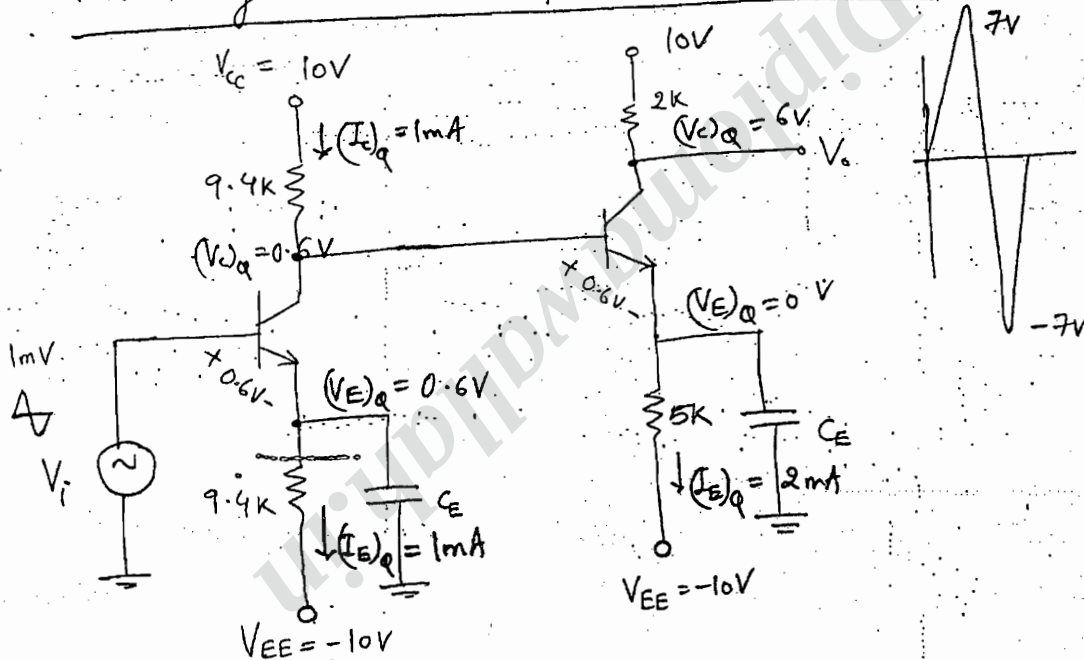
$$A_{V_s} = 0.316 \times -69 \times -61$$

$$= 1330$$

$$\therefore \frac{V_o}{V_s} = 1330$$

$$\text{Eg. } V_o = 1330 \times 1 \text{ mV} = 1.3 \text{ V}$$

Two stage direct coupled BJT amplifier -



Q₁:

$$g_{m1} = \frac{(I_c)_Q}{-V_T} = \frac{1 \text{ mA}}{25 \text{ mV}} = 40 \text{ mS}$$

$$r_{e1} = \frac{1}{g_{m1}} = 25 \Omega$$

$$\beta r_{e1} = r_{\pi_1} = 2.5 \text{ k}\Omega$$

Q₂:

$$g_{m2} = \frac{(I_c)_Q}{V_T} = \frac{2 \text{ mA}}{25 \text{ mV}}$$

$$= 80 \text{ mS}$$

$$r_{e2} = \frac{1}{g_{m2}} = 12.5 \Omega$$

$$\beta r_{e2} = r_{\pi_2} = 1.25 \text{ k}\Omega$$

$$\begin{aligned}
 A_{V_1} &= -g_m (R_c \parallel r_{\pi_2}) \\
 &= -40 \cdot (9.4 \text{ K} \parallel 1.25 \text{ K}) \\
 &= -44
 \end{aligned}$$

$$\begin{aligned}
 A_{V_2} &= -g_{m_2} \cdot R_c \\
 &= -80 \cdot 2 \text{ K} \\
 &= -160
 \end{aligned}$$

$$\begin{aligned}
 \therefore A_V &= A_{V_1} \cdot A_{V_2} \\
 &= -44 \times -160 \\
 &= 7040
 \end{aligned}$$

* This is the choice of op-amp. This type of amplifier are used in instrumentation and biomedical measurements. Direct coupled technique is better than RC coupled technique.

Conclusion:

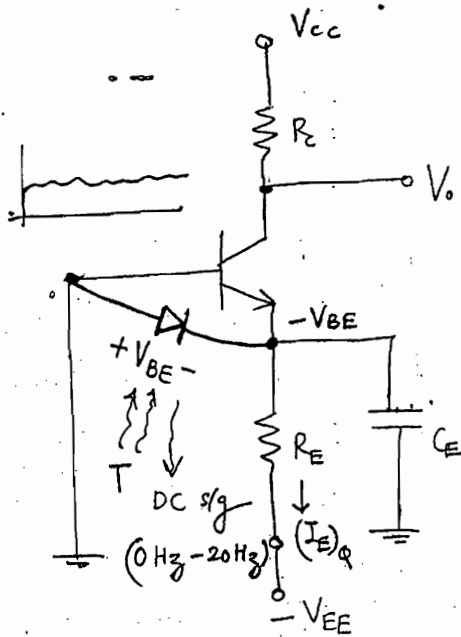
In integrated ckt designs, 2 important conditions to select an amplifier are:

- (1). It should give high gain.
- (2). The size should be less.

Note: So in integrated ckt, direct coupled amplifier became superior than RC coupled amplifier becoz of high gain and small in size.

Direct coupled amplifier:

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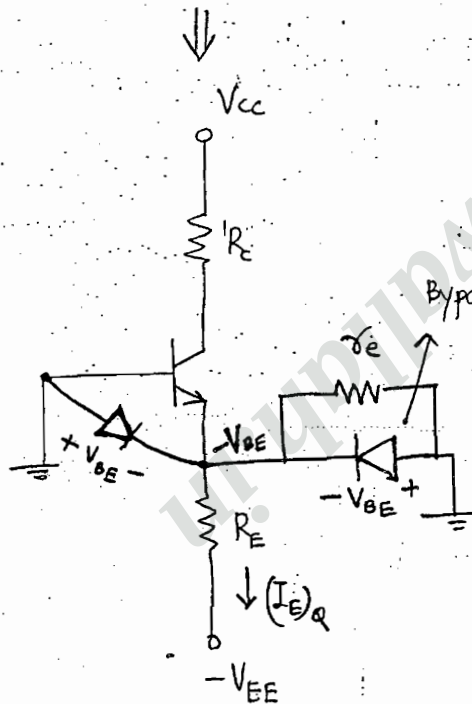
$$(1) \cdot \frac{dV_{BE}}{dT} = -2.5 \text{ mV}/^{\circ}\text{C}$$

↓
DC drift problem.

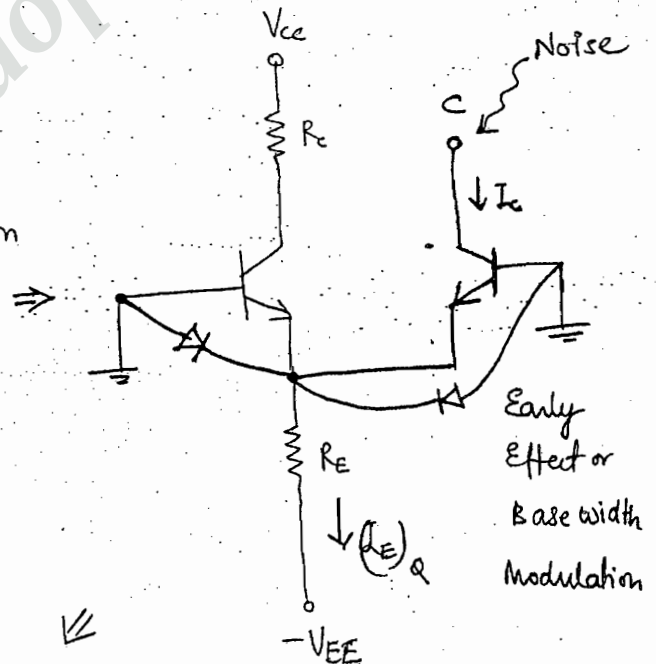
$$(2) \cdot f_{min} = \frac{1}{2\pi R_E C_E}$$

$$\text{for } f_{min} \rightarrow 0 \Rightarrow C_E \rightarrow \infty$$

↓
(Impossible to fabricate on IC practically).

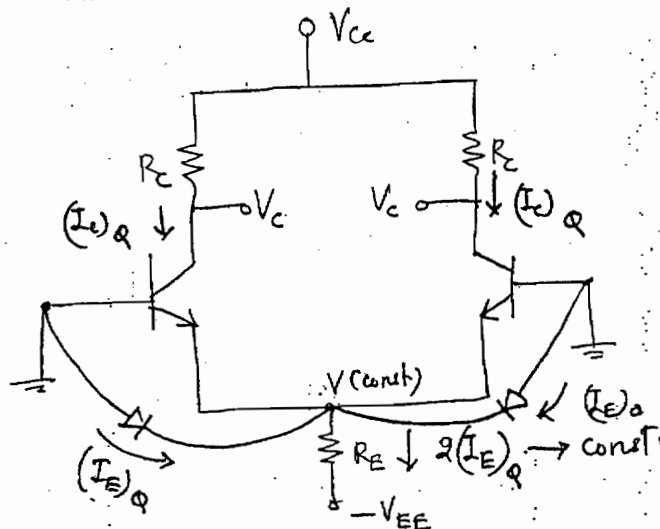


Bypass action



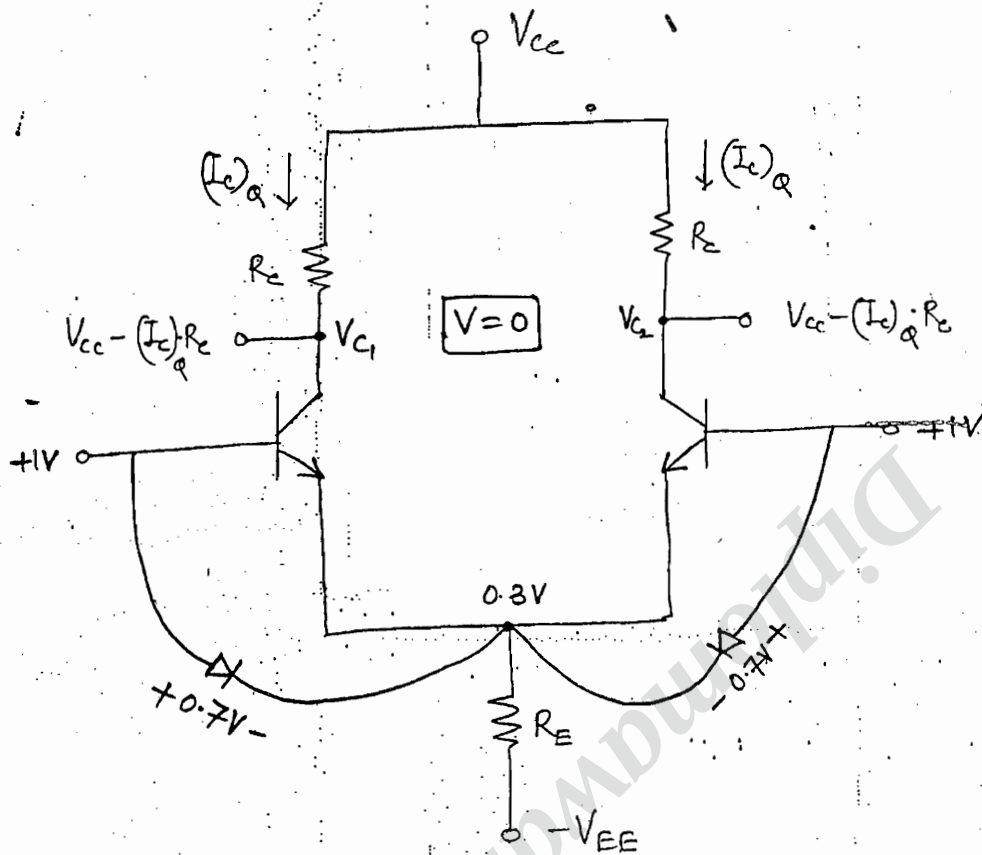
Early Effect or Base width Modulation

Differential amplifier



Testing of differential amplifiers for various i/p :

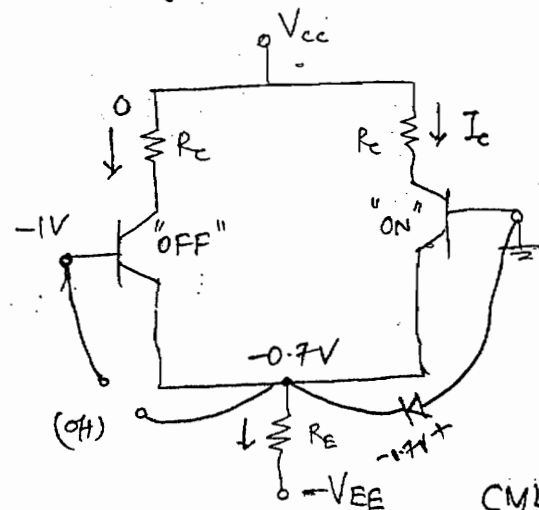
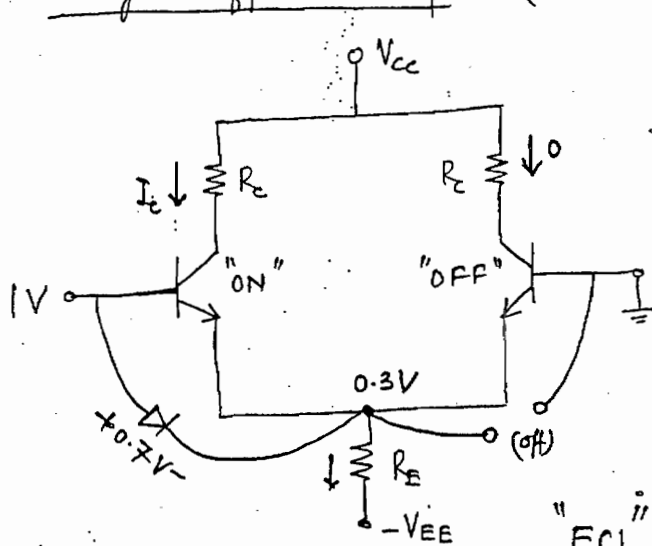
Common i/p :



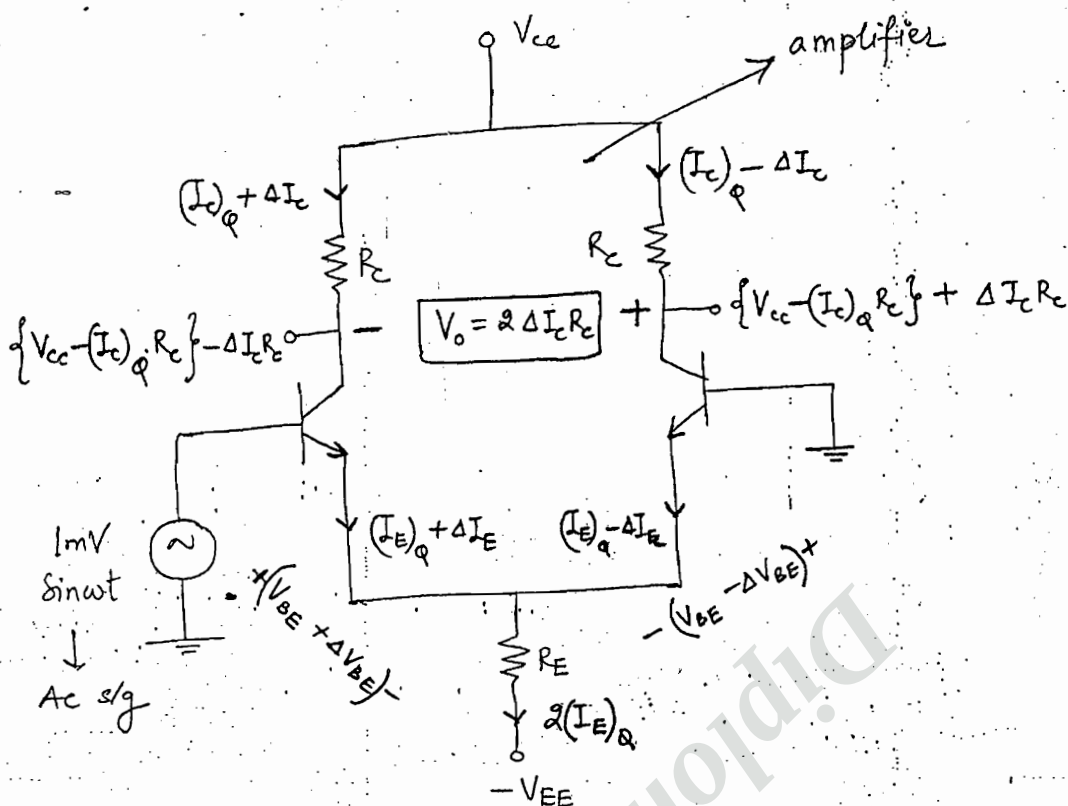
Common s/g are nothing but noise (or) interference s/g which is eliminated by differential amplifier.

$\therefore V = V_{C1} - V_{C2} = 0$ for common s/g.

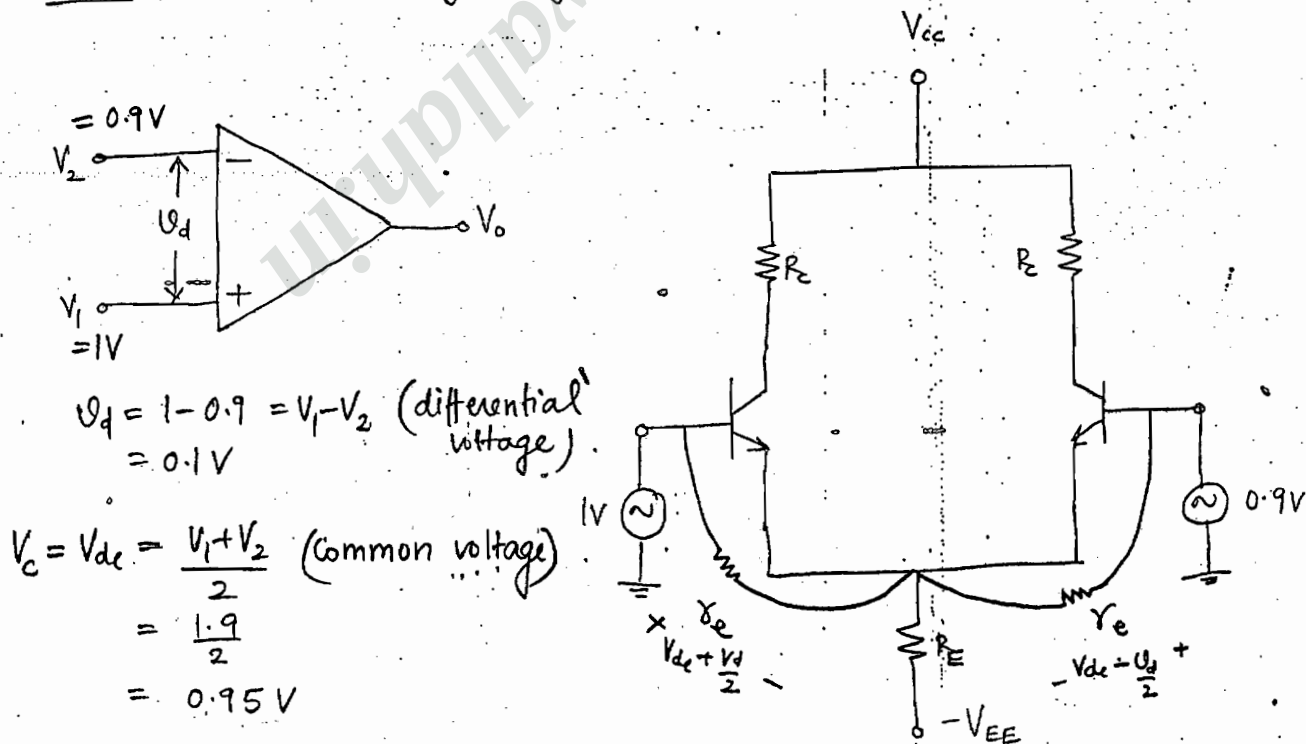
Large difference i/p : ($\geq IV$) $\rightarrow$ switching



"ECL" (Emitter coupled logic) (or) CML (Current mode logic)



Mathematical analysis of differential amplifiers :



$$V_1 = \frac{V_1 - V_2}{2} + \frac{V_1 + V_2}{2}$$

$$= \frac{V_d}{2} + V_{de}$$

$$V_2 = \frac{V_2 - V_1}{2} + \frac{V_1 + V_2}{2}$$

$$= -\frac{V_d}{2} + V_{de}$$

Mathematics involved inside:

$$V_1 = \frac{1 - 0.9}{2} + \frac{1 + 0.9}{2} = \frac{0.1}{2} + 0.95$$

$$-V_2 = \frac{0.9 - 1}{2} + \frac{1 + 0.9}{2} = -\frac{0.1}{2} + 0.95$$

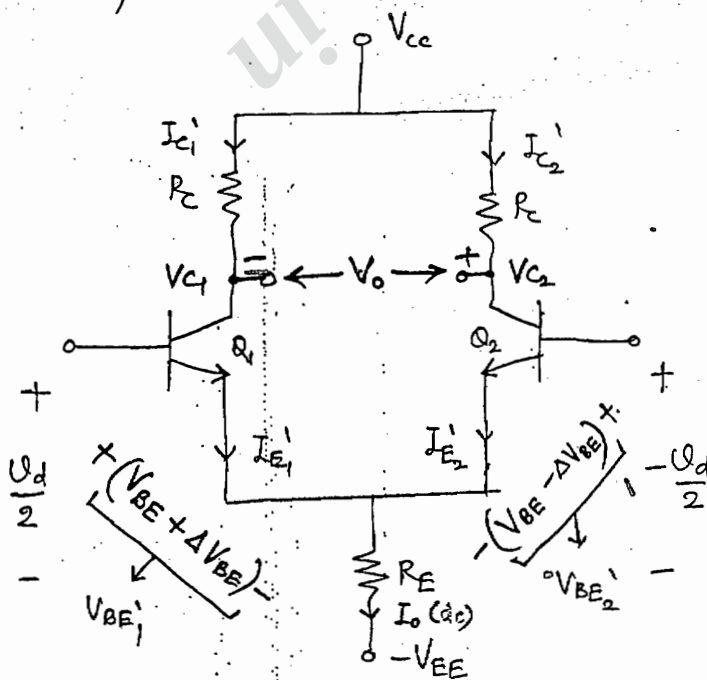
$$V_d = V_1 - V_2$$

$$= \frac{0.1}{2} + 0.95 - (-\frac{0.1}{2}) - 0.95$$

$$= 0.1 \text{ V}$$

Transfer characteristics of OP-Amp:

Analysis:



$$V_o = V_{C1} - V_{C2}$$

$$= (V_{CC} - I_{C1} R_C) - (V_{CC} - I_{C2} R_C)$$

$$= (I_{C2} - I_{C1}) R_C$$

$$I_{E1}' = (I_E)_Q + \Delta I_E$$

$$I_{E2}' = (I_E)_Q - \Delta I_E$$

$$(I_E)_Q = I_{E0} \cdot e^{V_{BE}/V_T}$$

$$I_{E1}' = I_{E0} \cdot e^{V_{BE1} + \Delta V_{BE}/V_T}$$

$$I_{E1}' = I_{E0} \cdot e^{V_{BE1}'/V_T}$$

$$I_{E2}' = I_{E0} \cdot e^{V_{BE2}'/V_T}$$

$$\frac{I_{E1}'}{I_{E2}'} = e^{\underbrace{(V_{BE1}' - V_{BE2}')}_{V_d}/V_T}$$

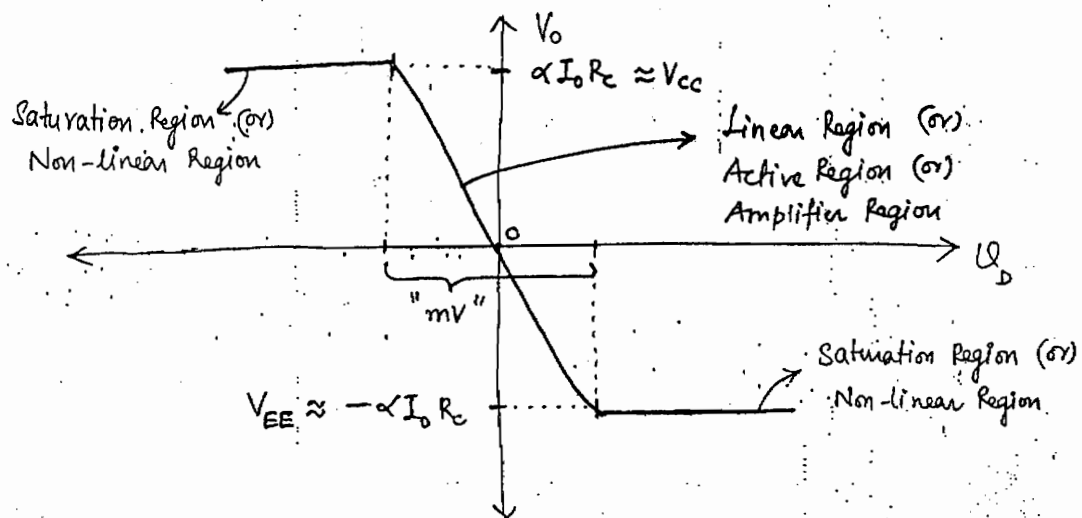
$$\frac{I_{E1}'}{I_{E2}'} = e^{V_d/V_T}$$

$$\frac{I_{E1}' - I_{E2}'}{I_{E1}' + I_{E2}'} = \frac{e^{V_d/V_T} - 1}{e^{V_d/V_T} + 1}$$

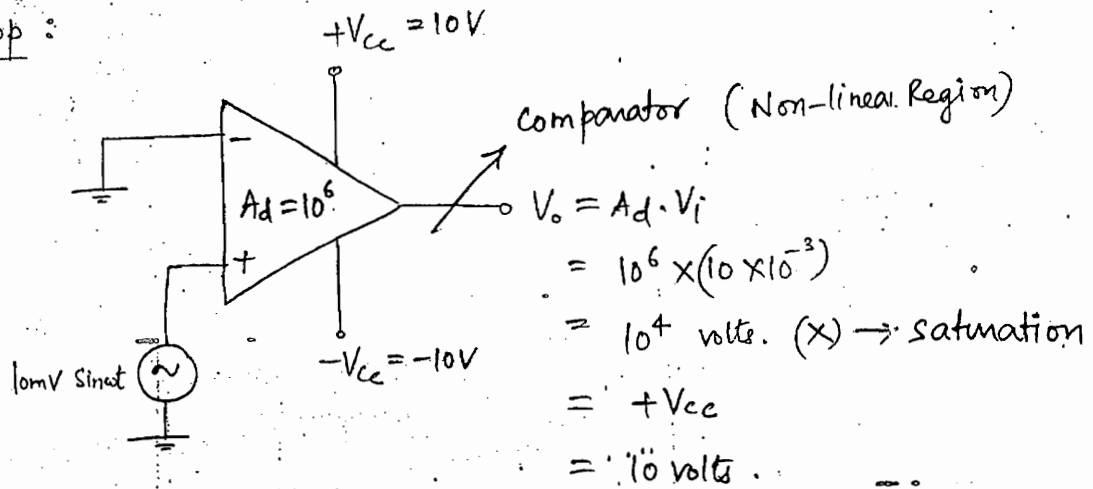
$$(1) \quad I_{E1}' - I_{E2}' = \frac{I_{C1}' - I_{C2}'}{\alpha} \quad (2) \quad I_{E1}' + I_{E2}' = I_0 \text{ (dc)}$$

$$\frac{I_{C1}' - I_{C2}'}{\alpha I_0} = \frac{e^{V_d/V_T} - 1}{e^{V_d/V_T} + 1}$$

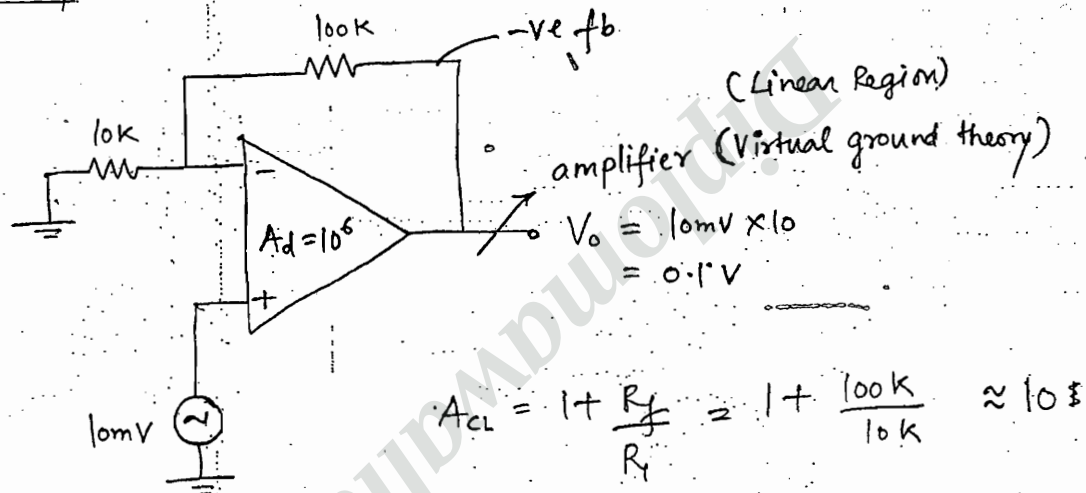
$$V_o = -\alpha I_0 R_c \cdot \frac{(e^{V_d/V_T} - 1)}{(e^{V_d/V_T} + 1)}$$



Open loop :



Closed loop :



* Under Linear region, $V_d \rightarrow 0$ (Virtual ground theory)

Advantages of differential amplifier compared to direct coupled amplifier :

- (1). It gives best noise rejection capability.
- (2). There is no need of coupling of bypass capacitors.
- (3). The i/p impedance is high. Typically $1\text{M}\Omega$ we can design.

Ckt configurations :

At the i/p side we have -

- (1) single i/p
- (2) Dual i/p

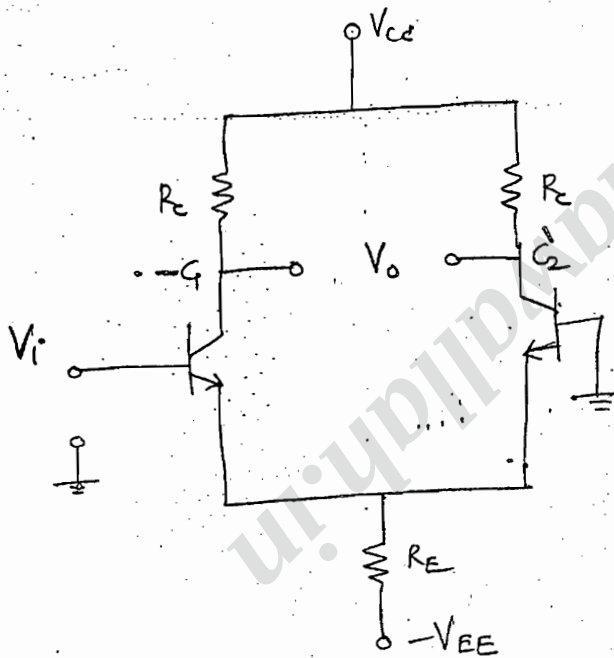
At the o/p side, we have

- (1) Balanced o/p (o/p is taken across C_1 & C_2)
- (2) Unbalanced o/p (o/p is taken across C_1 to gnd or C_2 to gnd)

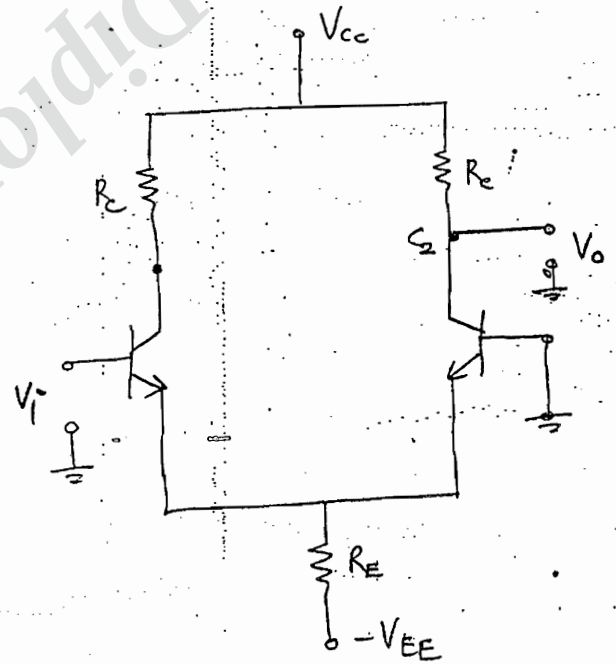
The configurations are:

- (1) single i/p balanced o/p
- (2) single i/p unbalanced o/p
- (3) dual i/p balanced o/p
- (4) dual i/p unbalanced o/p

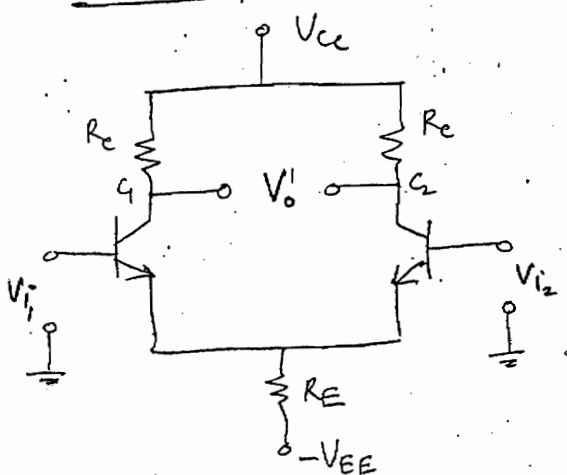
Single i/p balanced o/p:



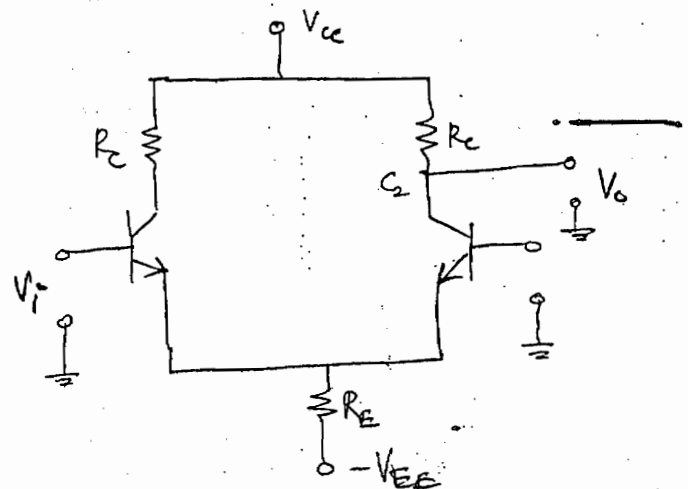
Single i/p unbalanced o/p:



Dual i/p balanced o/p:



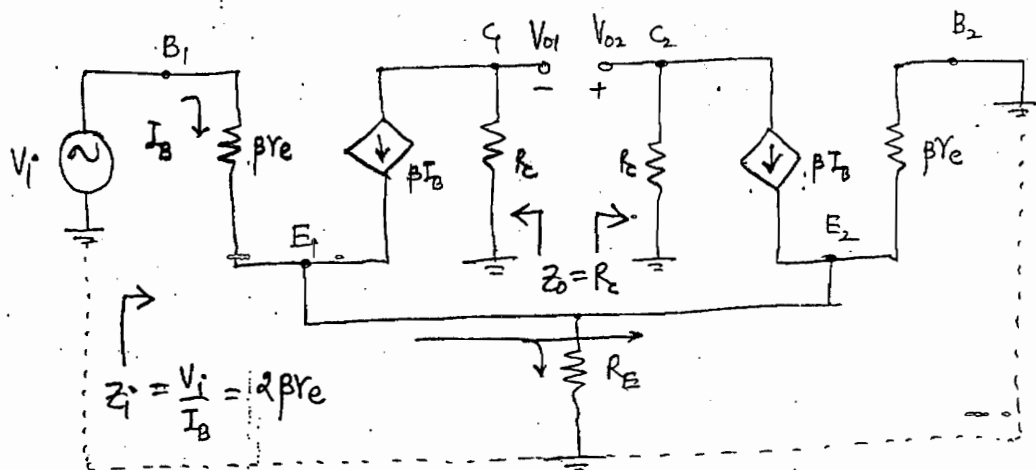
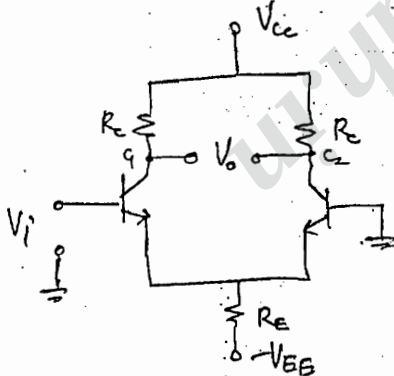
Dual i/p unbalanced o/p:

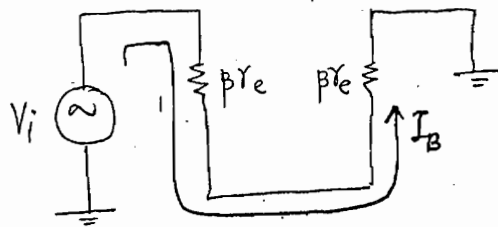


Design problems in differential amplifier:

- Q1. Calculate the voltage gain of a single i/p balanced o/p or dual i/p balanced o/p differential amplifier with $R_C = 1K$ and $(I_E)_Q = 26mA$.
- Q2. Calculate the voltage gain of single i/p unbalanced o/p or dual i/p unbalanced o/p differential amplifier with $R_C = 1K$ and $(I_E)_Q = 26mA$.
- Q3. Calculate the i/p impedance of a differential amplifier ckt with $\beta = 50$ and $(I_E)_Q = 26mA$.
- Q4. Calculate the o/p impedance of a diff. amplifier ckt with $R_C = 1K$.
- Q5. Design the operating point of a diff. amplifier ckt

Ac analysis:





$$Z_i = 2\beta r_e$$

$$\therefore R_E \gg \beta r_e$$

$$V_{o1} = -\beta I_B R_c$$

$$V_{o1} = -\beta \frac{V_i}{2\beta r_e} R_c$$

$$\frac{V_{o1}}{V_i} = -\frac{R_c}{2r_e} \rightarrow \text{unbalanced gain}$$

$$\text{Similarly, } \frac{V_{o2}}{V_i} = +\frac{R_c}{2r_e} \rightarrow \text{unbalanced gain}$$

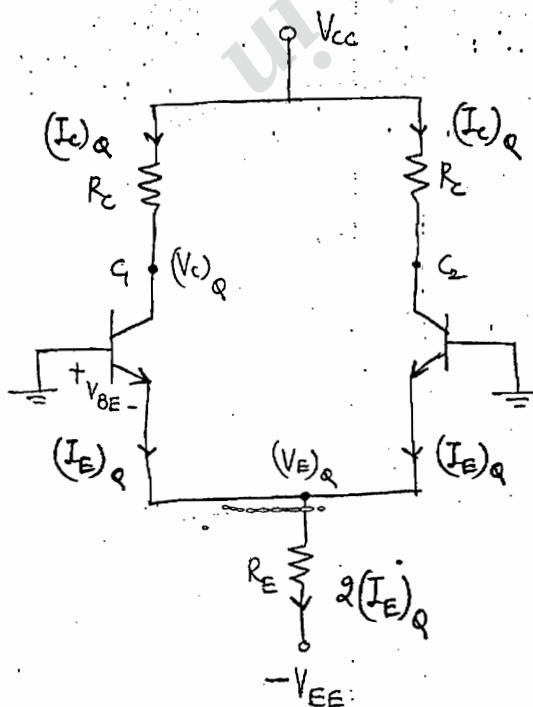
$$\text{And, } A_v = A_{v1} - A_{v2}$$

$$A_v = -\frac{R_c}{2r_e} - \frac{R_c}{2r_e}$$

$$A_v = -\frac{R_c}{r_e} \rightarrow \text{Balanced gain}$$

Sol(5).

DC Analysis: operating point



$$V_{EE} - V_{BE} - 2(I_E)_Q R_E = 0$$

$$(I_E)_Q = \frac{V_{EE} - V_{BE}}{2R_E}$$

$$\begin{aligned} (V_{CE})_Q &= (V_C)_Q - (V_E)_Q \\ &= V_{CC} - (I_C)_Q R_c + V_{BE} \end{aligned}$$

$$(V_{CE})_Q = V_{CC} + V_{BE} - (I_C)_Q R_c$$

Sol. (1). Balanced o/p:

$$|A_d| = \left| \frac{R_c}{r_e} \right| = \frac{1K}{1\Omega} = 1000$$

$$r_e = \frac{V_T}{(I_E)_Q} = \frac{26}{26} = 1\Omega$$

Sol (2). Unbalanced o/p:

$$|A_d| = \left| \frac{R_c}{2r_e} \right| = \frac{1K}{2} = 500$$

Sol (3). $Z_i = 2\beta r_e$
 $= 2 \times 50 \times 1 = 100\Omega$

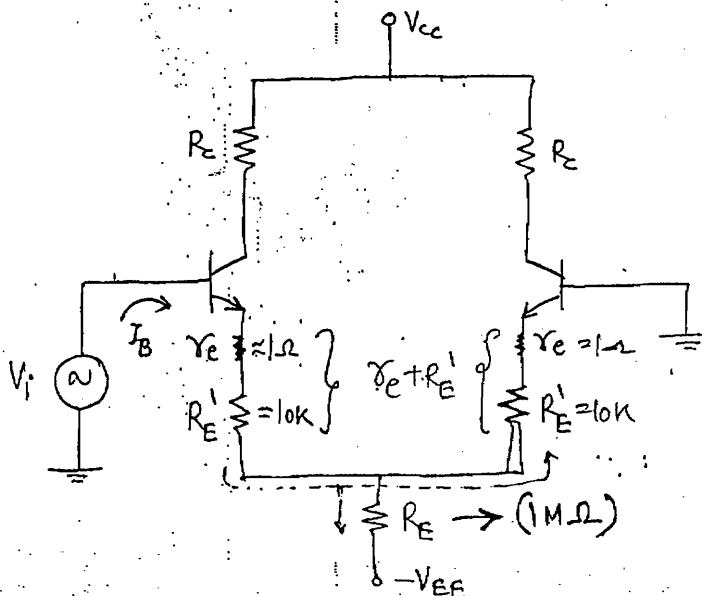
Sol (4). $Z_o = R_c = 1K\Omega$

Conclusion:

- (1). the gain of ^{diff.} amp is unstable.
- (2). the o/p impedance is low.

To solve this problem, we use feedback.

Swamping resistor technique:



$$A_d = \frac{R_c}{r_e}$$

↓
Swamping Resistor technique

$$A_d = \frac{R_c}{r_e + R_{E'}}$$

$$A_d \approx \frac{R_c}{R_{E'}} \rightarrow \text{stable}$$

$$Z_i = 2\beta r_e \rightarrow$$

Swamping

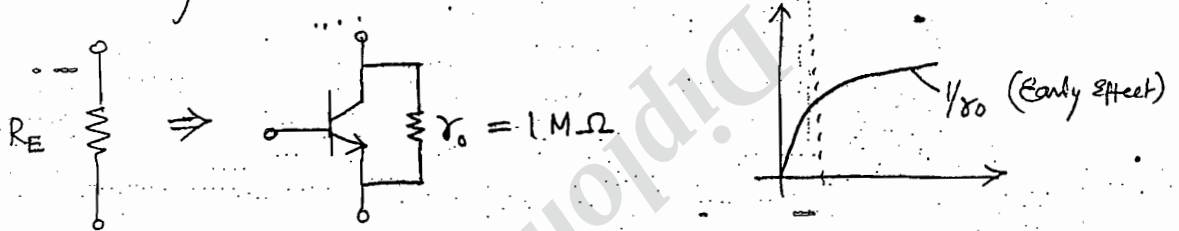
$$Z_i = 2\beta(r_e + R_{E'})$$

$$Z_i \approx 2\beta R_{E'} \text{ (High)}$$

$$\text{If } \beta = 50, R_{E'} = 10 \text{ k}\Omega \Rightarrow Z_i = 2 \times 50 \times 10 \text{ k}\Omega = 1 \text{ M}\Omega$$

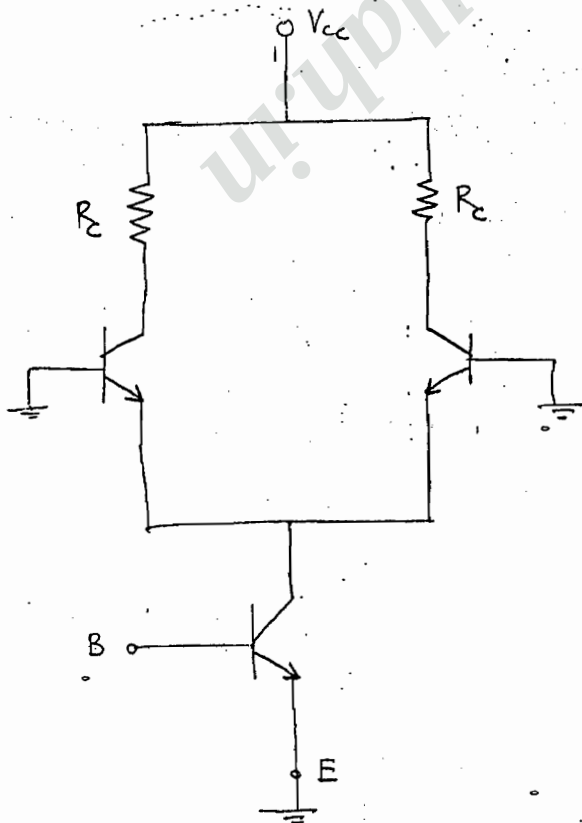
* The only problem is with this, we have to keep $R_E \rightarrow 1 \text{ M}\Omega$ {i.e. to block I_B , $R_E \gg (r_e + R_{E'})$ }

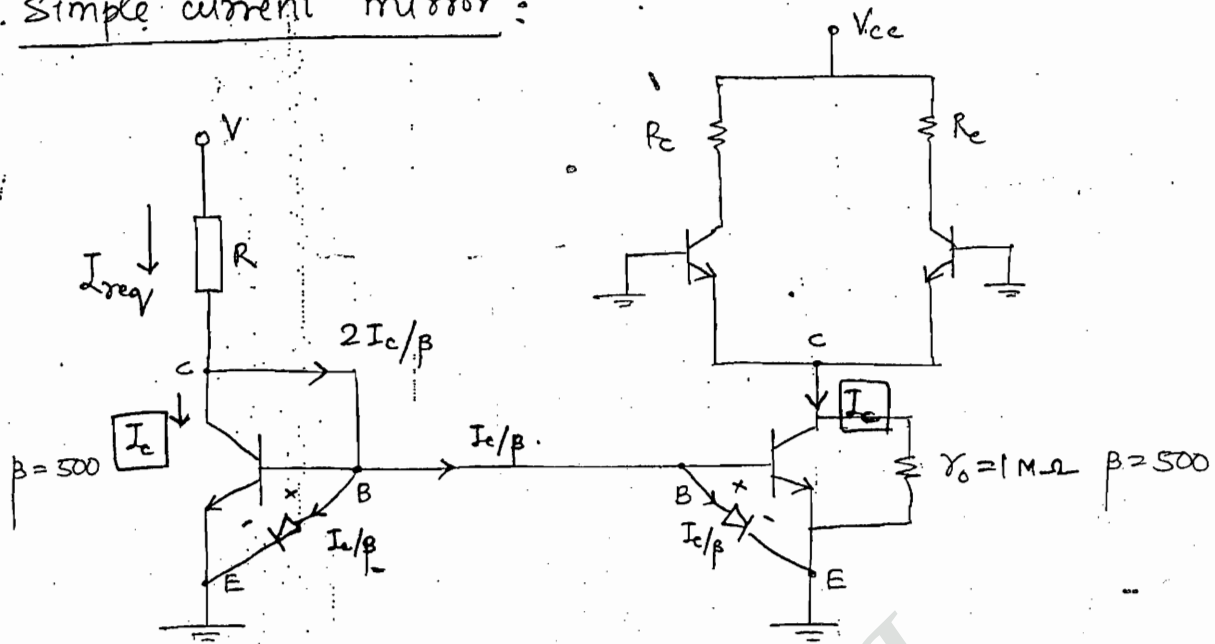
On place of R_E , put transistor to solve the above problem.



Integrated ckt Biasing:

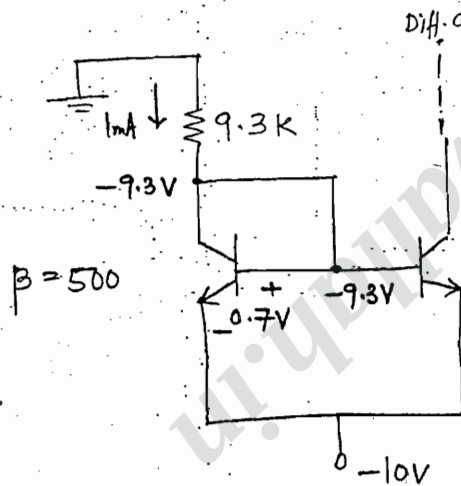
Current mirror Biasing:



(1). Simple current mirror:

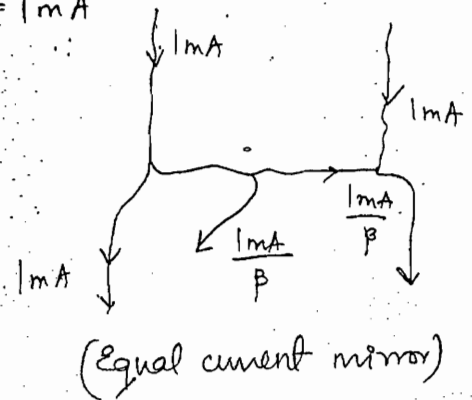
$I_b = I_c / \beta$ for reduce $I_b \Rightarrow \beta \uparrow$ (High value)

Q.



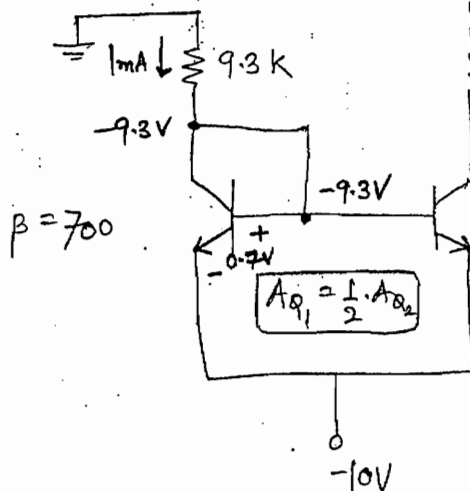
$I_o = 1\text{mA}$

$\beta = 500$



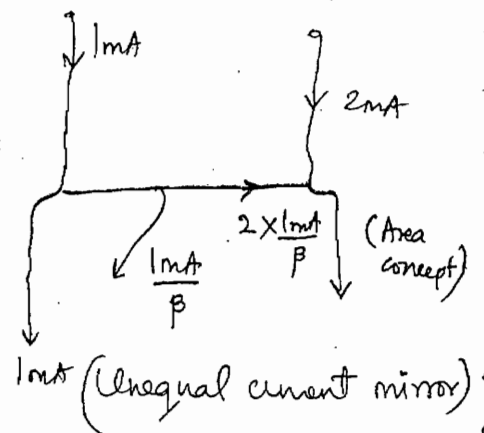
(Equal current mirror)

Q.



$I_o = ?$ $I_o \approx 2\text{mA}$

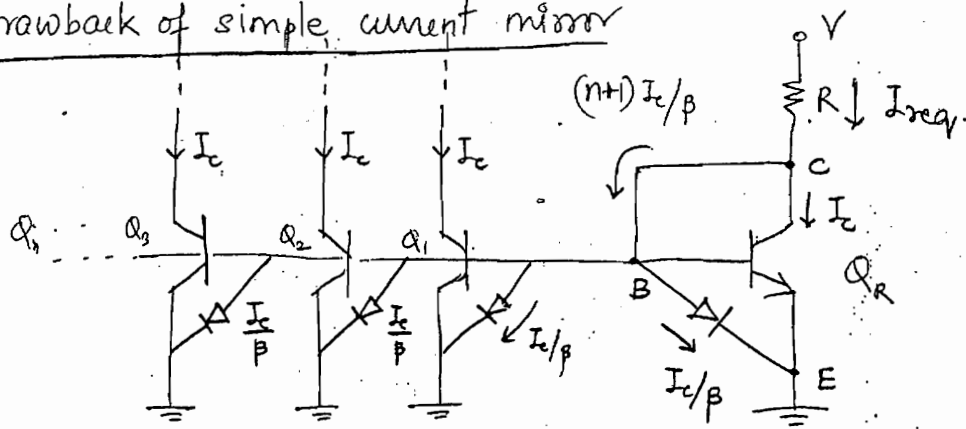
$\beta = 715$



(Area concept)
(Unequal current mirror)

* β are equal (theoretically)

* β are slightly different (practically) $\rightarrow$ don't bother (approx ans) ✓



$$I_{ref} = I_c + (n+1) \frac{I_c}{\beta}$$

$$I_{ref} = I_c \left[1 + \frac{(n+1)}{\beta} \right]$$

eg. If $n=99$, then $\beta=500$.

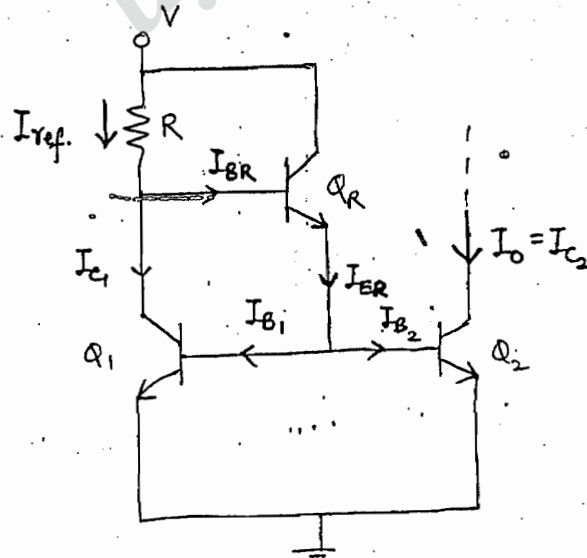
$$I_{ref} = I_c \left(1 + \frac{100}{500} \right) = \frac{6}{5} I_c$$

$I_{ref} \neq I_c$ (accuracy is lost)

Hence, $n \leq 10$ transistors in the biasing for better accuracy.

To improve accuracy, there is modification required.

(2). Modified current mirror:



$$\begin{aligned} I_{ref} &= I_{C1} + I_{B1} \\ &= I_{C1} + \frac{I_{B1} + I_{B2}}{(1+\beta)} \\ &= I_{C1} + \frac{I_{B1} + I_{B2}}{(1+\beta)} \end{aligned}$$

Acc. to current mirror,

$$I_{C1} = I_{C2} \quad \& \quad I_{B1} = I_{B2}$$

$$\begin{aligned} \text{Now, } I_{ref} &= I_{C2} + \frac{2 I_{B2}}{(1+\beta)} \\ &= I_{C2} + \frac{2 I_{C2}}{\beta(1+\beta)} \end{aligned}$$

$$I_{ref} = I_{C2} + \frac{2 I_{C2}}{\beta + \beta^2}$$

$$I_{ref} \approx I_{C2} \left(1 + \frac{2}{\beta^2} \right)$$

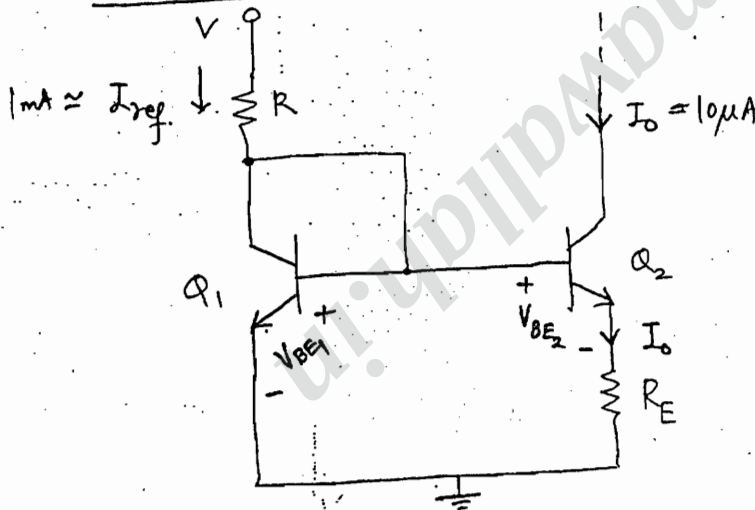
$$\therefore I_0 = I_{C2} = \frac{I_{ref}}{\left(1 + \frac{2}{\beta^2} \right)}$$

for n transistors,

$$I_{Ci} = \frac{I_{ref}}{1 + \frac{(n+1)}{\beta^2}}$$

Here, accuracy is much improved.

(3) micro level current mirror (mA to μ A mirror):



$$V_{BE1} = V_{BE2} + I_0 R_E$$

$$I_0 R_E = V_{BE1} - V_{BE2}$$

$$V_{BE1} = V_T \ln \left(\frac{I_{ref}}{I_S} \right)$$

$$V_{BE2} = V_T \ln \left(\frac{I_0}{I_S} \right)$$

$$\therefore I_0 R_E = V_T \ln \left(\frac{I_{ref}}{I_0} \right)$$

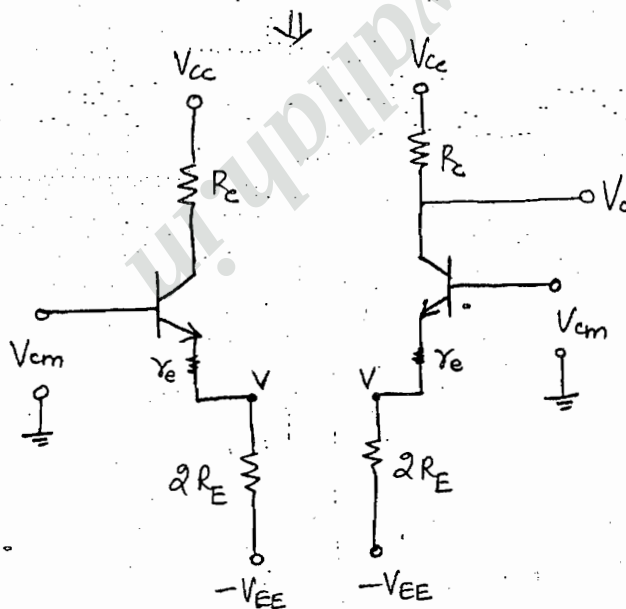
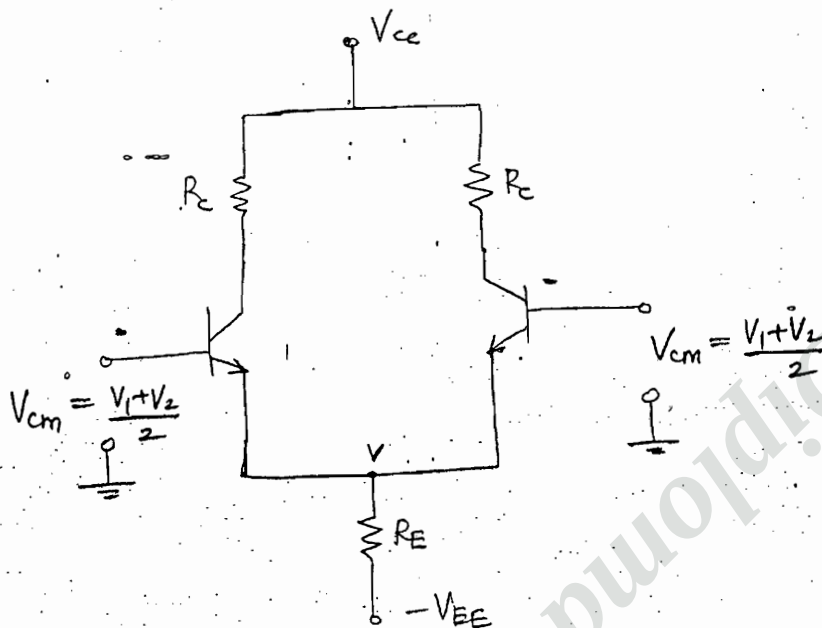
$$\text{eg. } 10 \times 10^{-6} R_E = 26 \text{ mV} \cdot \ln \left(\frac{1 \text{ mA}}{10 \times 10^{-6} \text{ A}} \right)$$

$$\therefore (\text{designing } R_E) \rightarrow R_E = \frac{26 \times 10^{-3}}{10^{-7}} \ln \left(\frac{10^3}{10^{-5}} \right) = 11 \text{ K}\Omega$$

↓
Widlar current source
↓
Scientist

CMRR :

$$CMRR = \frac{A_d}{A_c} \Rightarrow (CMRR)_{dB} = 20 \log \left(\frac{A_d}{A_c} \right)$$

Common mode gain (A_c):

$$A_{cm} = \frac{V_o}{V_{cm}} = \frac{-R_C}{r_e + 2R_E}$$

$$\Rightarrow 2R_E \gg r_e$$

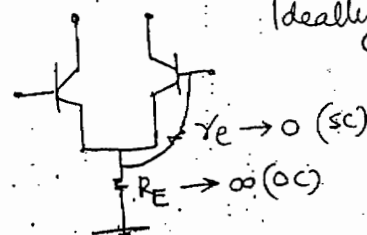
$$\therefore A_{cm} = \frac{-R_C}{2R_E} \rightarrow \text{unbalanced}$$

$$A_d = \frac{-R_C}{2r_e} \rightarrow \text{unbalanced}$$

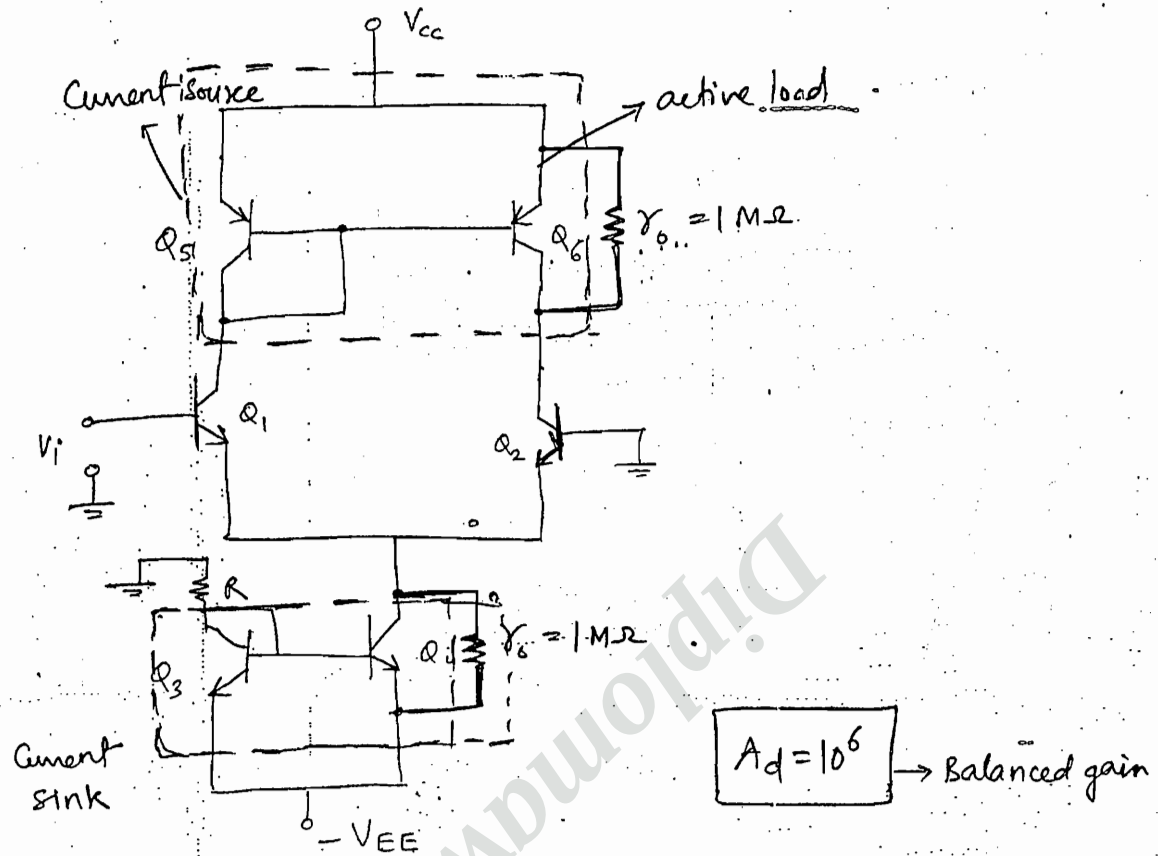
$$CMRR = A_d / A_c$$

$$CMRR = \frac{-R_C / 2r_e}{-R_C / 2R_E}$$

$$CMRR = R_E / r_e = g_m R_E$$

For AC -
Ideally ($CMRR = \infty$)

Integrated IC differential amplifier:



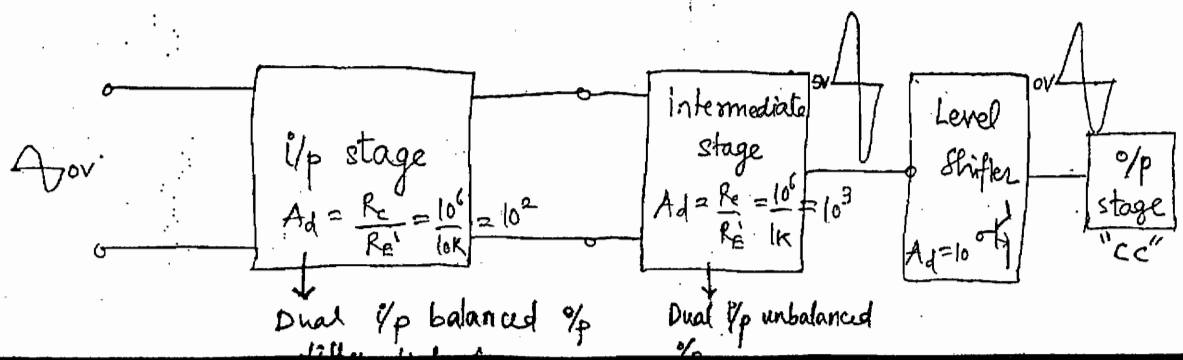
$$A_d = \frac{R_c}{2r_e} = \frac{r_o}{2r_e} = \frac{1 M\Omega}{2 \times 1 \Omega} = 0.5 \times 10^6$$

$$A_c = \frac{R_c}{2R_E} = \frac{r_o}{2r_o} = 0.5$$

$$CMRR = \frac{A_d}{A_c} = \frac{0.5 \times 10^6}{0.5} = 10^6$$

Operational amplifier:

Block diagram of Op-Amp:



Applications of Op-Amp : Linear applications :

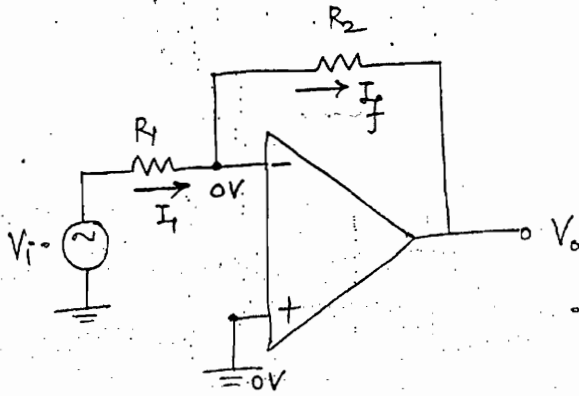
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- (1). Inverting amplifier
- (2). Non-inverting amplifier
- (3). Phase shifter
- (4). Voltage followers
- (5). Differential amplifiers
- (6). Subtractor
- (7). Adder (inverting)
- (8). Non-inverting adder
- (9). Current to voltage converter
- (10). Voltage to current converter

- * (11). Voltage limiters
- (12). Logarithmic amplifiers
- (13). Antilog amplifiers
- * (14). precision rectifiers
- (15). Instrumentational amplifiers
- (16). Modulators (Analog multipliers)
- (17). Demodulators (Analog dividers)
- * (18). Filter designs (active filters)
 - LPF
 - HPF
 - BPF
 - BR F
 - All pass filter
- (19). Integrators
- (20). Differentiators

Non-linear applications :

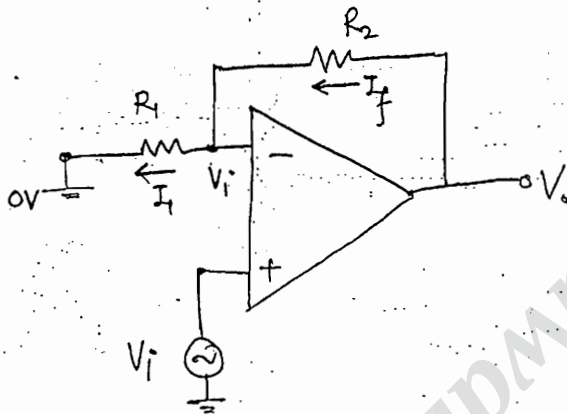
- (1). Generating a square wave
 - Comparators (ZCD)
 - Schmitt trigger
 - Astable multivibrator
- (2). Mono stable multivibrator
- (3). 555 timer
- (4). Oscillators
 - RC oscillators
 - RC phase shift oscillator
 - Wein bridge oscillator
 - LC oscillators
 - Hartley
 - Colpitts

(1) Inverting amplifier:

$$I_i = I_f$$

$$\frac{V_i - 0}{R_1} = \frac{0 - V_o}{R_2}$$

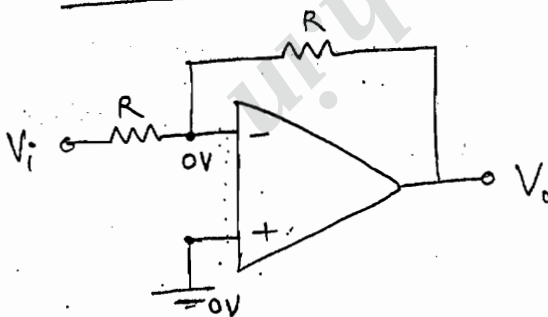
$$\frac{V_o}{V_i} = -\frac{R_2}{R_1}$$

(2) Non-inverting amplifier:

$$I_i = I_f$$

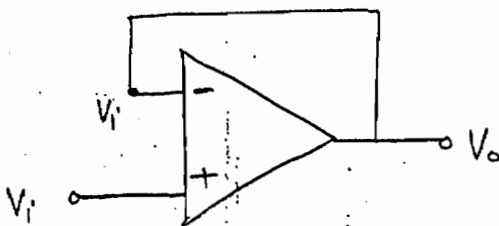
$$\frac{V_i - 0}{R_1} = \frac{V_o - V_i}{R_2}$$

$$\frac{V_o}{V_i} = 1 + \frac{R_2}{R_1}$$

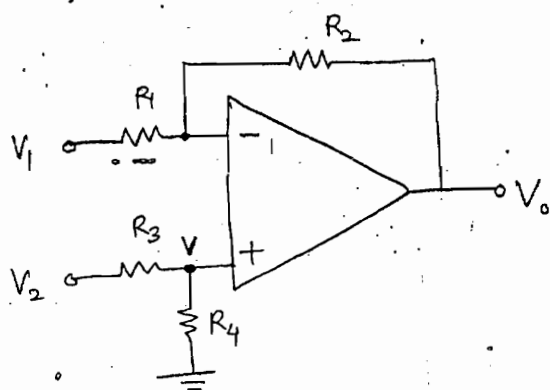
(3) Phase shifter:

$$\frac{V_o}{V_i} = -\frac{R}{R}$$

$$V_o = -V_i$$

(4) Voltage follower:

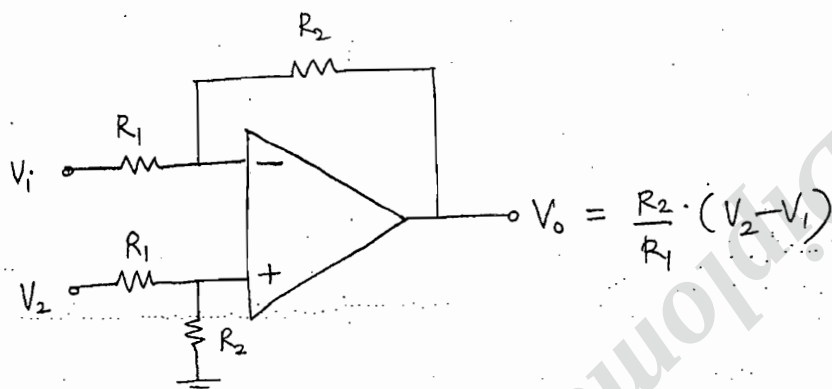
$$V_o = V_i$$

(5). Differential amplifier:

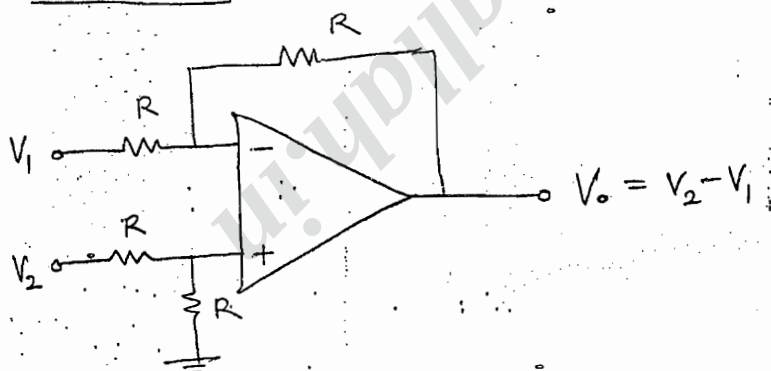
By superposition,

$$V_o = -\frac{R_2}{R_1} V_1 + \left(1 + \frac{R_2}{R_1}\right) V$$

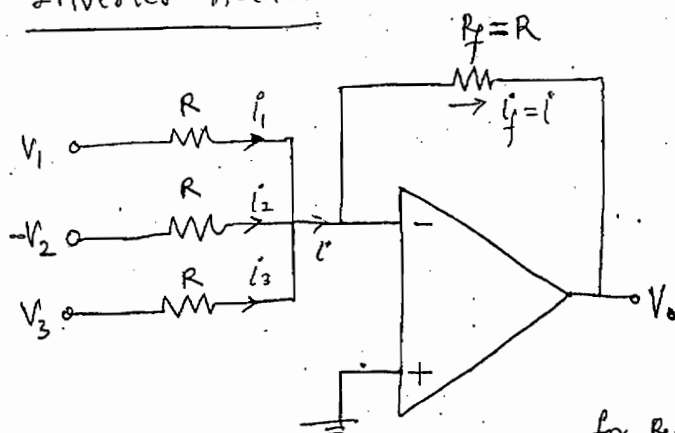
$$\text{where } V = \frac{V_2 \cdot R_4}{(R_3 + R_4)}$$



$$V_o = \frac{R_2}{R_1} (V_2 - V_1)$$

(6). Subtractor:

$$V_o = V_2 - V_1$$

(7). Inverter Adder:

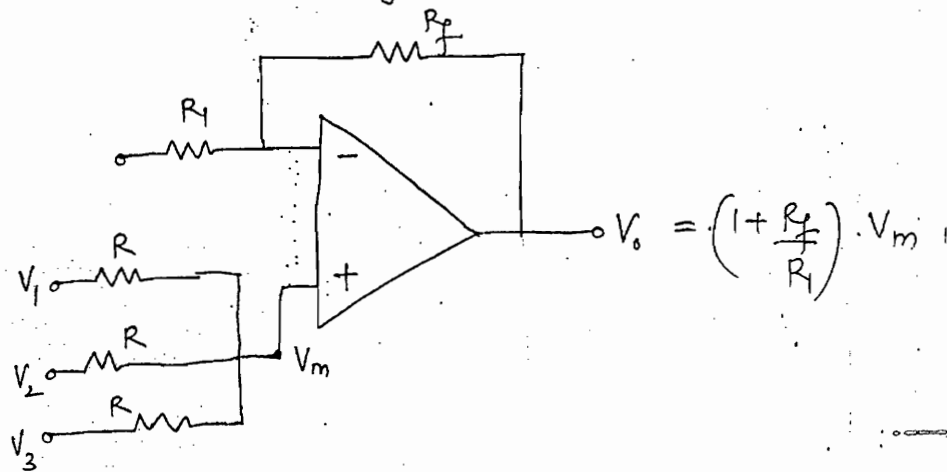
$$i_f = i$$

$$i_1 + i_2 + i_3 = \frac{0 - V_o}{R_f}$$

$$\frac{V_1 + V_2 + V_3}{R} = -\frac{V_o}{R_f}$$

$$V_o = -\frac{R_f}{R} (V_1 + V_2 + V_3)$$

$$\text{for } R_f = R; \quad V_o = -(V_1 + V_2 + V_3)$$

(8) Non-inverting Adder:

By using superposition,

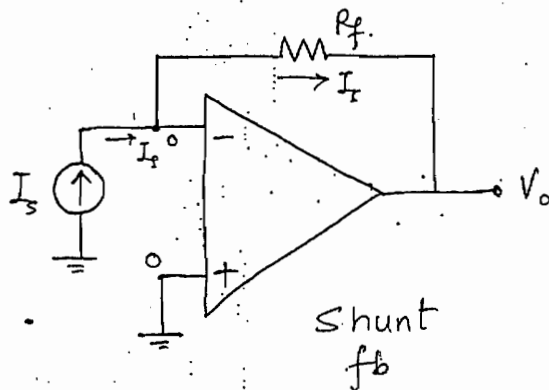
$$V_{m1} = \frac{R/2}{R + R/2} \cdot V_1$$

$$V_{m2} = \frac{R/2}{R + R/2} \cdot V_2$$

$$V_{m3} = \frac{R/2}{R + R/2} \cdot V_3$$

$$\begin{aligned} \therefore V_m &= V_{m1} + V_{m2} + V_{m3} \\ &= \frac{R/2}{R + R/2} \cdot (V_1 + V_2 + V_3) \\ &= \frac{R}{3R} (V_1 + V_2 + V_3) \\ &= \frac{(V_1 + V_2 + V_3)}{3} \end{aligned}$$

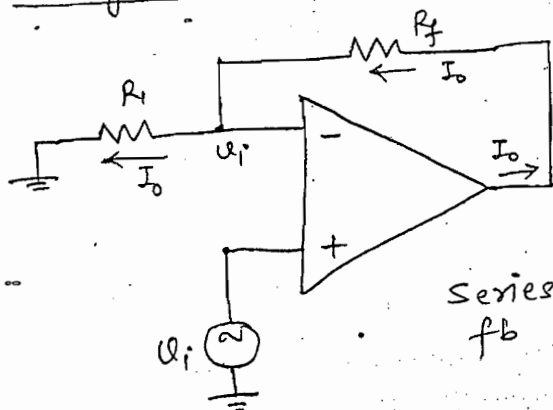
$$V_o = \left(1 + \frac{R_f}{R_1}\right) \frac{(V_1 + V_2 + V_3)}{3}$$

(9) Current to voltage converter: "CCVS"

$$0 - V_o = I_s \cdot R_f$$

$$V_o = -I_s R_f$$

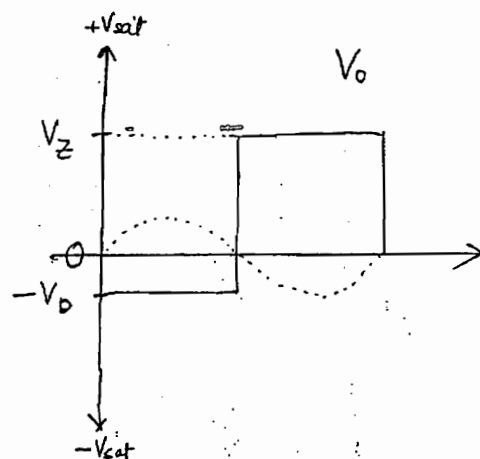
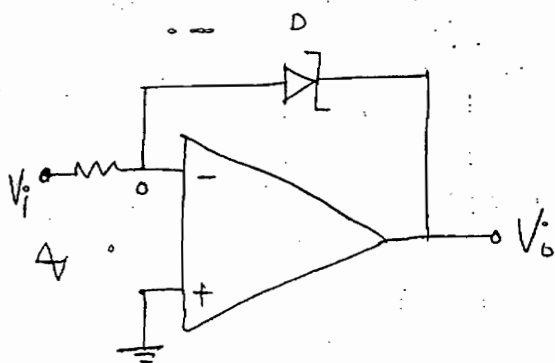
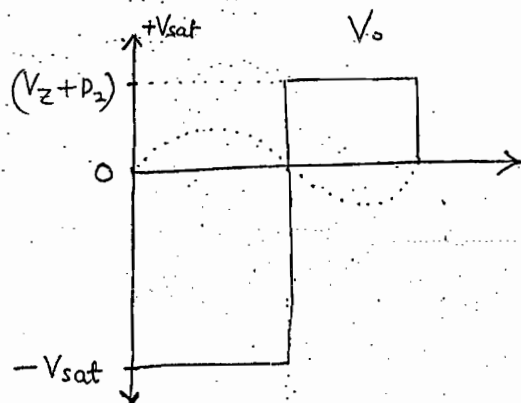
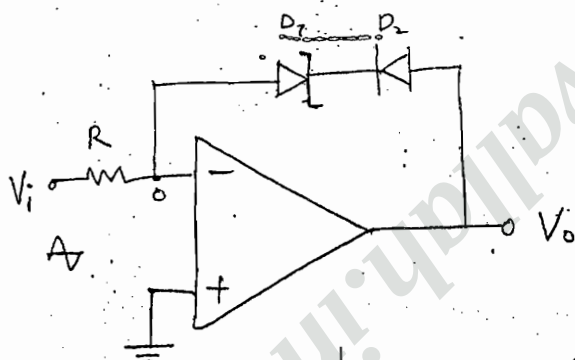
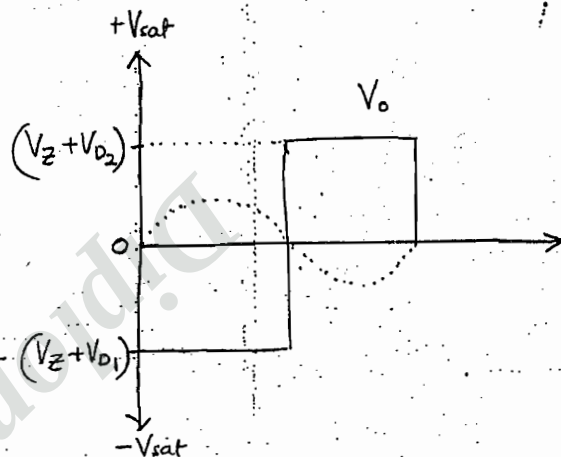
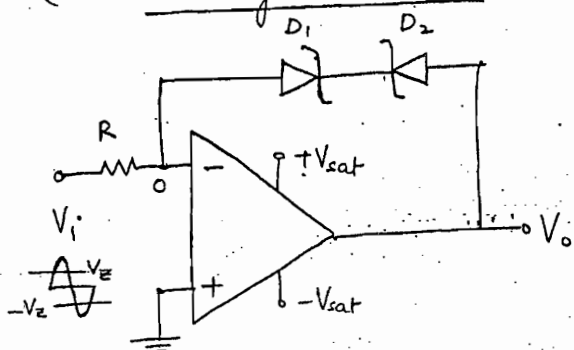
(10). voltage to current converter: "VCCS"



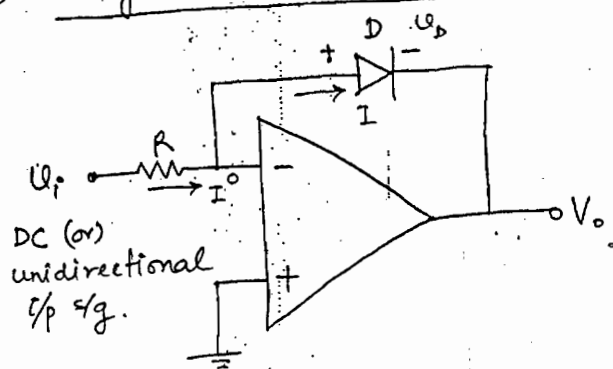
$$V_i = I_o R_1$$

$$I_o = \frac{V_i}{R_1}$$

* (11). Voltage limiter:



(12). Logarithmic amplifier: (data compression)



$$0 - V_o = U_D$$

$$V_o = -U_D$$

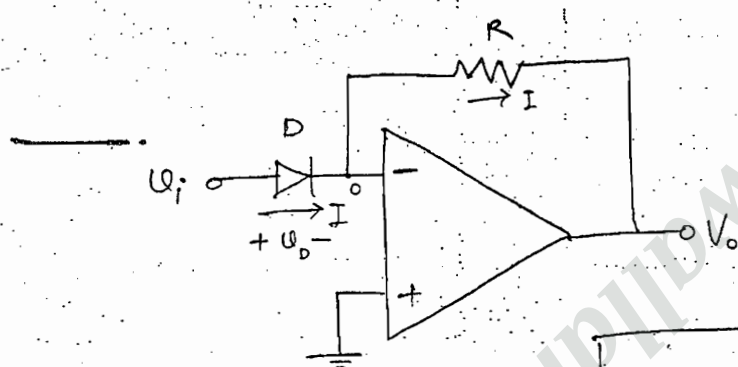
$$I = I_0 e^{U_D/V_T}$$

$$U_D = V_T \cdot \ln\left(\frac{I}{I_0}\right)$$

$$V_o = -V_T \ln\left(\frac{U_i}{I_0 R}\right)$$

$$\therefore V_o \propto \ln U_i$$

(13). Antilog amplifier: (data expansion)



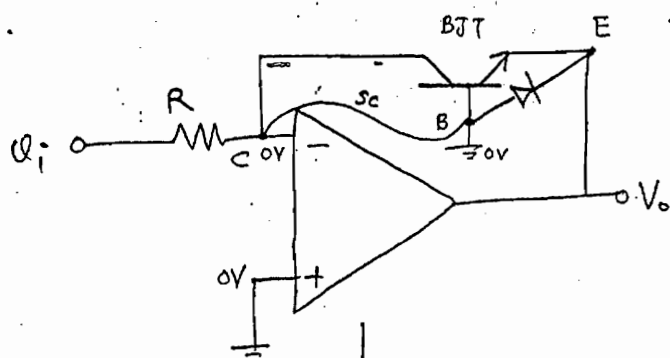
$$I = I_0 e^{U_D/V_T}$$

$$\frac{0 - V_o}{R} = I_0 e^{U_D/V_T}$$

$$V_o = -I_0 R \cdot e^{U_i/V_T}$$

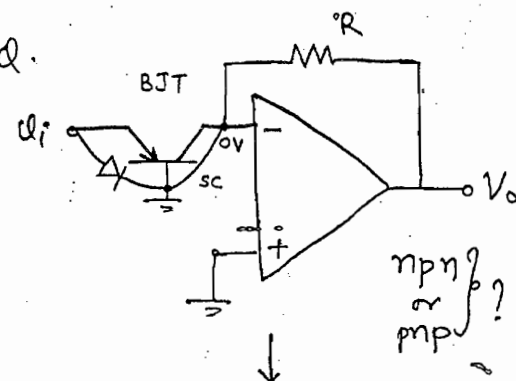
$$\therefore V_o \propto e^{U_i}$$

Q.



Logarithmic amplifier
(Replacement of diode with BJT)

Q.

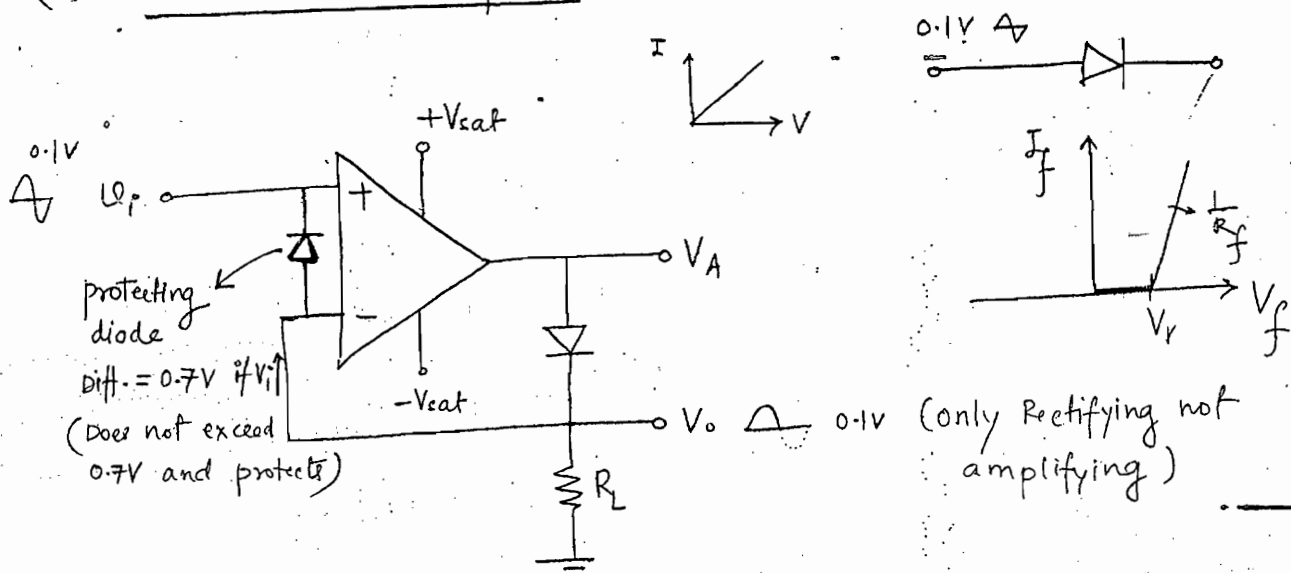


Antilog amplifier
(Replacement of diode with BJT)

BJT must be "pnp".

(14) Precision rectifiers:

205



+ve Half cycle (CL)

$$V_A = +V_{sat}$$

$D \rightarrow ON$

$$V_o = V_i \text{ (Linear)}$$

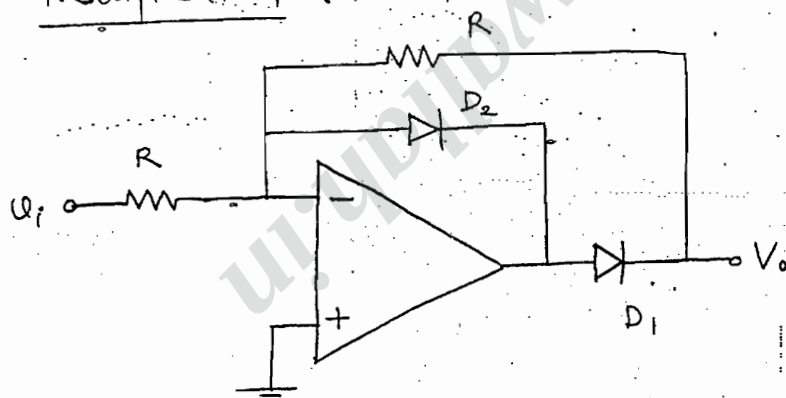
-ve Half cycle (OL)

$$V_A = -V_{sat}$$

$D \rightarrow OFF$

$$V_o = 0 \text{ (Non-linear)}$$

Modification :



+ve Half cycle

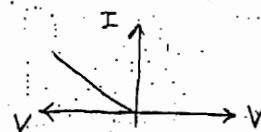
$D_2 \rightarrow ON, D_1 \rightarrow OFF$

$$V_o = 0$$

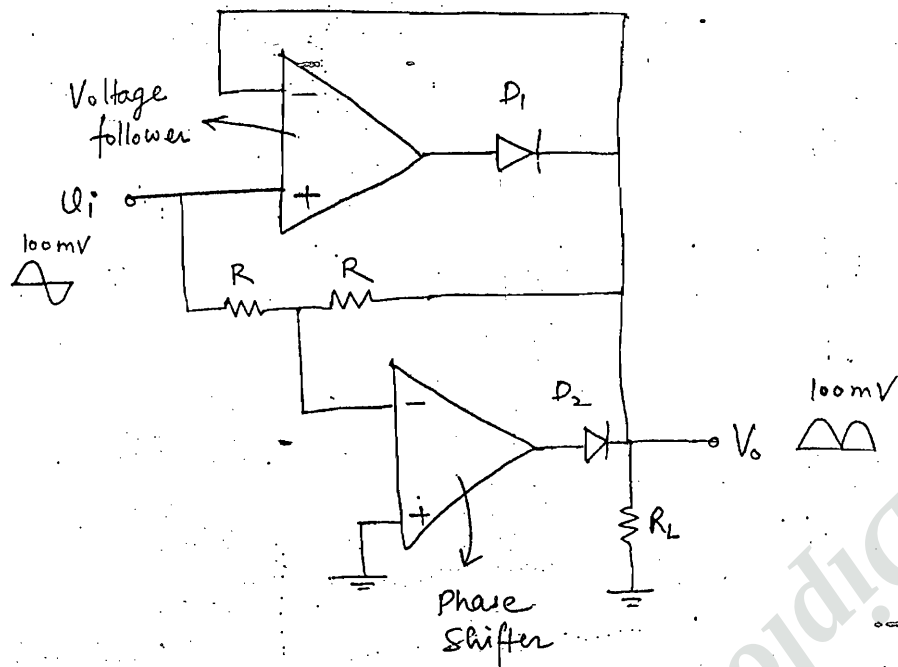
-ve Half cycle

$D_2 \rightarrow OFF, D_1 \rightarrow ON$

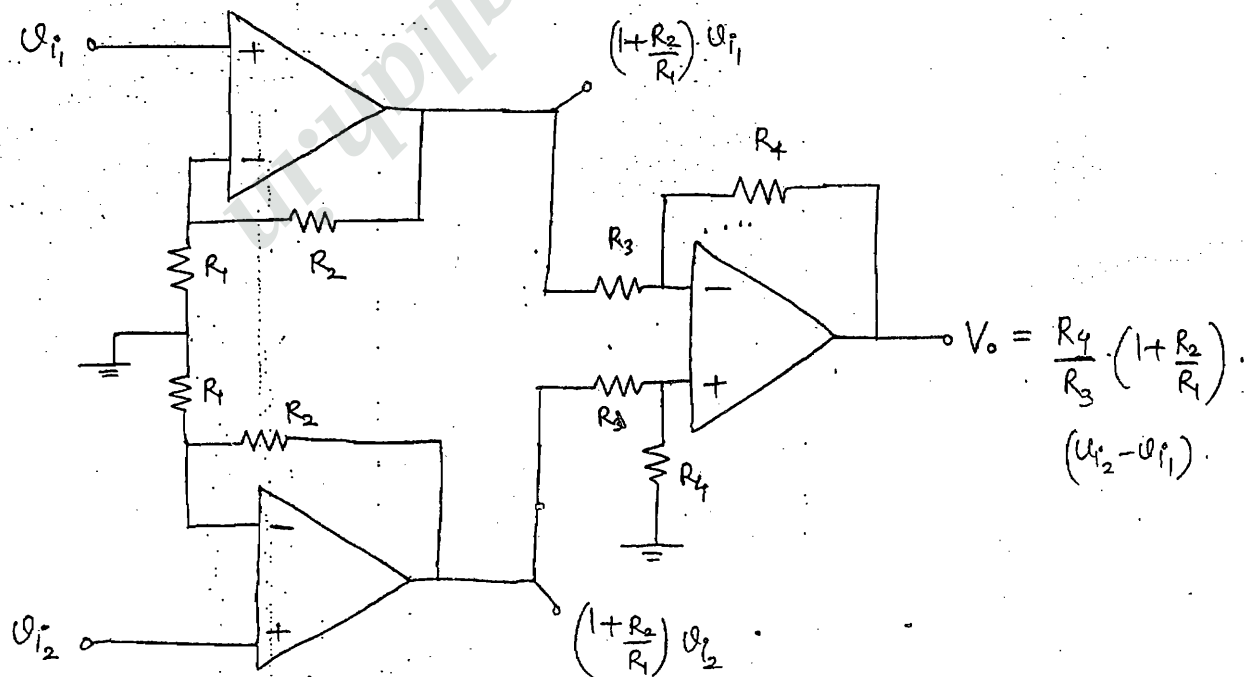
$$V_o = -V_i \text{ (Phase shifter)}$$



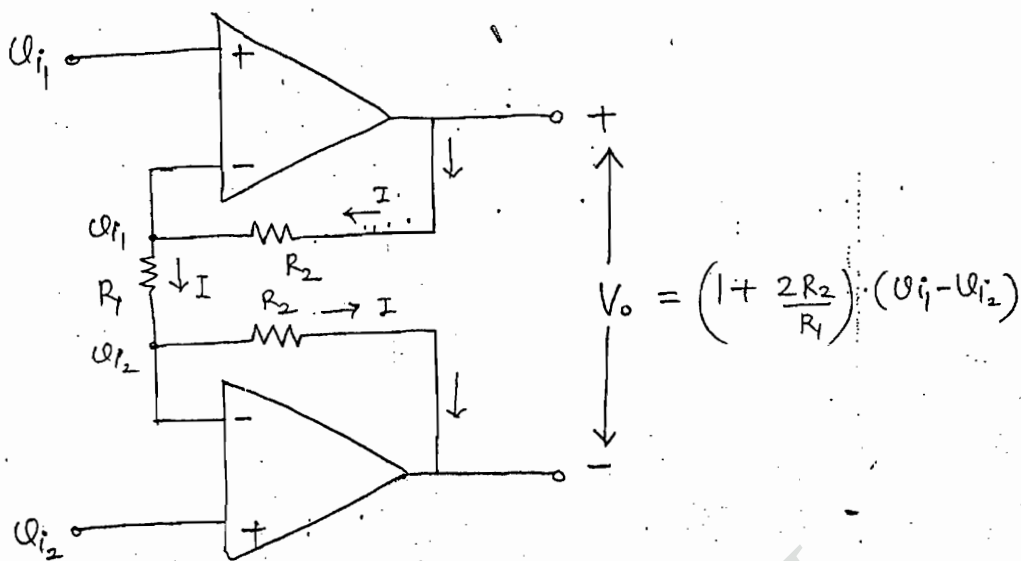
full wave rectifier:



(15) Instrumentational amplifier :

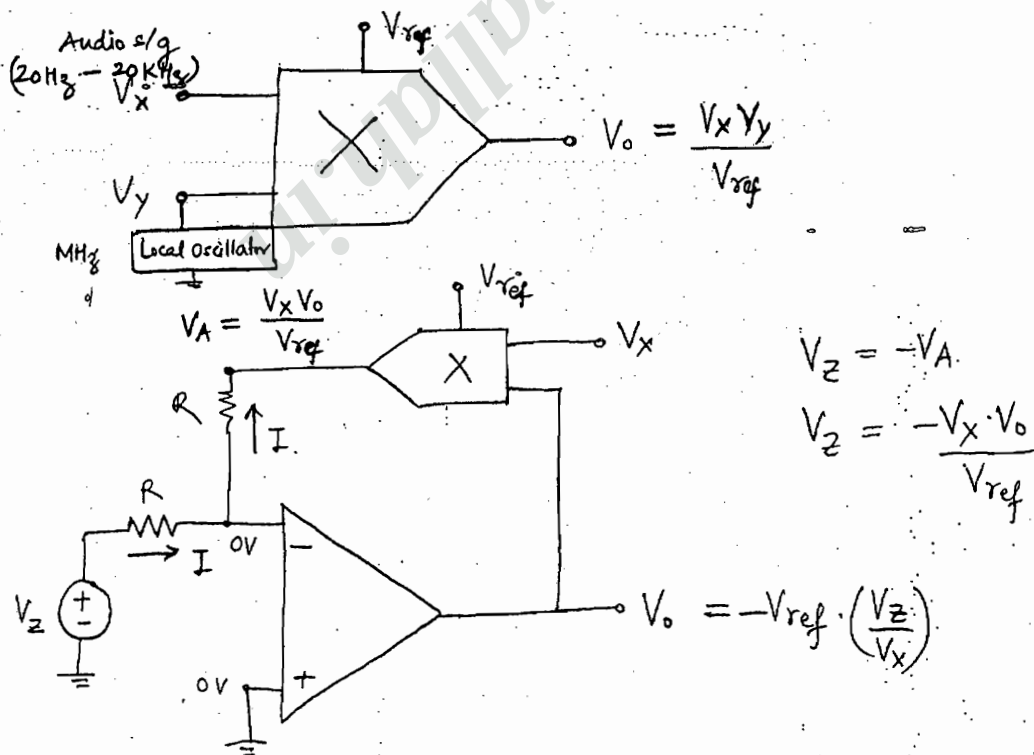


$$A_d = \frac{V_o}{U_d} = \frac{R_4}{R_3} \left(1 + \frac{R_2}{R_1} \right)$$

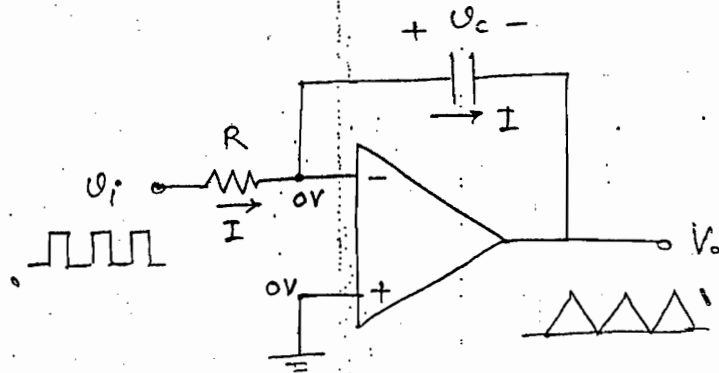


$$A_d = \frac{V_o}{V_d} = \frac{V_o}{(V_{i1} - V_{i2})} = \frac{I(R_1 + 2R_2)}{I(R_1)} = 1 + \frac{2R_2}{R_1} \quad (\text{High})$$

(16) Modulator : (Analog Multiplier)



(17) Demodulator : (Analog Divider)

(18). Integrator : (LPF)

$$V_c = \frac{1}{C} \int I dt$$

$$V_o = -V_c$$

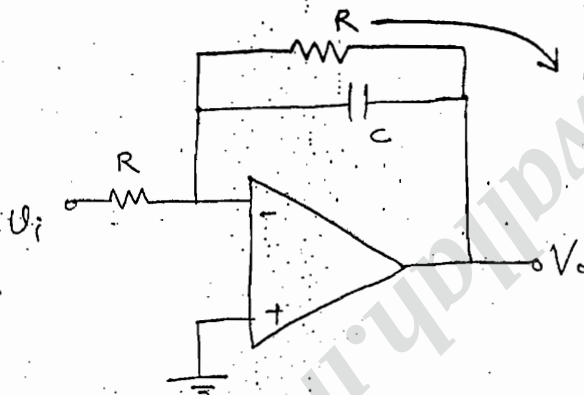
$$= -\frac{1}{C} \int I dt$$

$$V_o = -\frac{1}{RC} \int V_i dt$$

Transfer function:

$$TF = \frac{V_o(s)}{V_i(s)} = \frac{-1}{sRC} \Rightarrow \frac{1}{sRC} = \frac{1}{j\omega RC} = \frac{-j}{\omega RC}$$

$\downarrow$ phase shift $\downarrow$ phase = -90°
(Phase lag design)

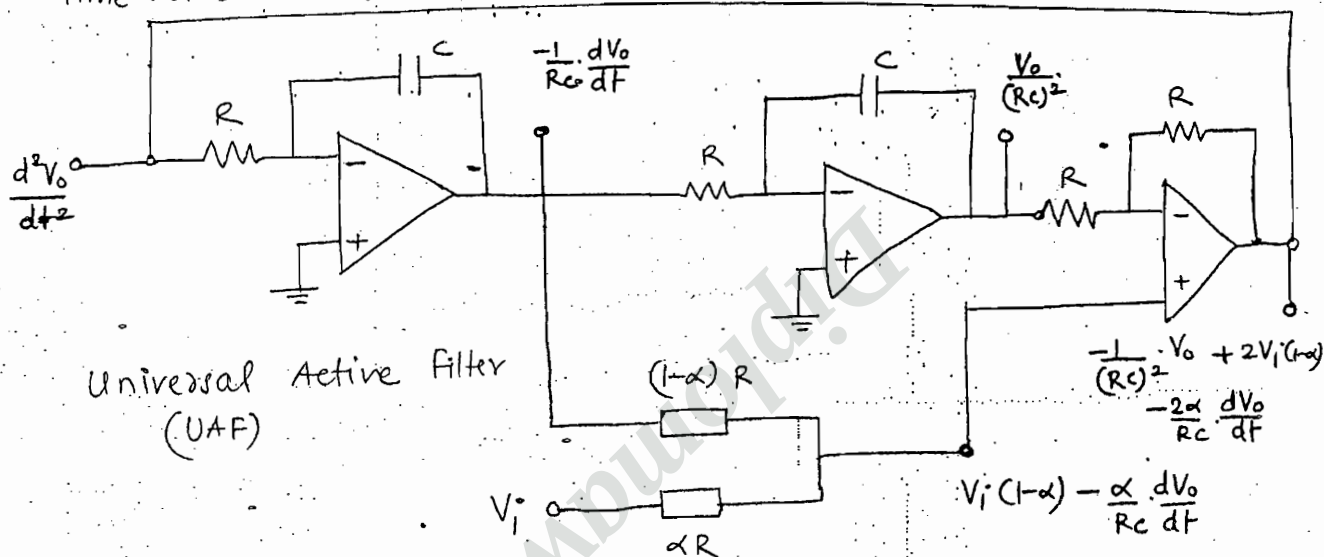
(19). Differentiator :

Simulate a 2nd order differential eqn using op-amps.

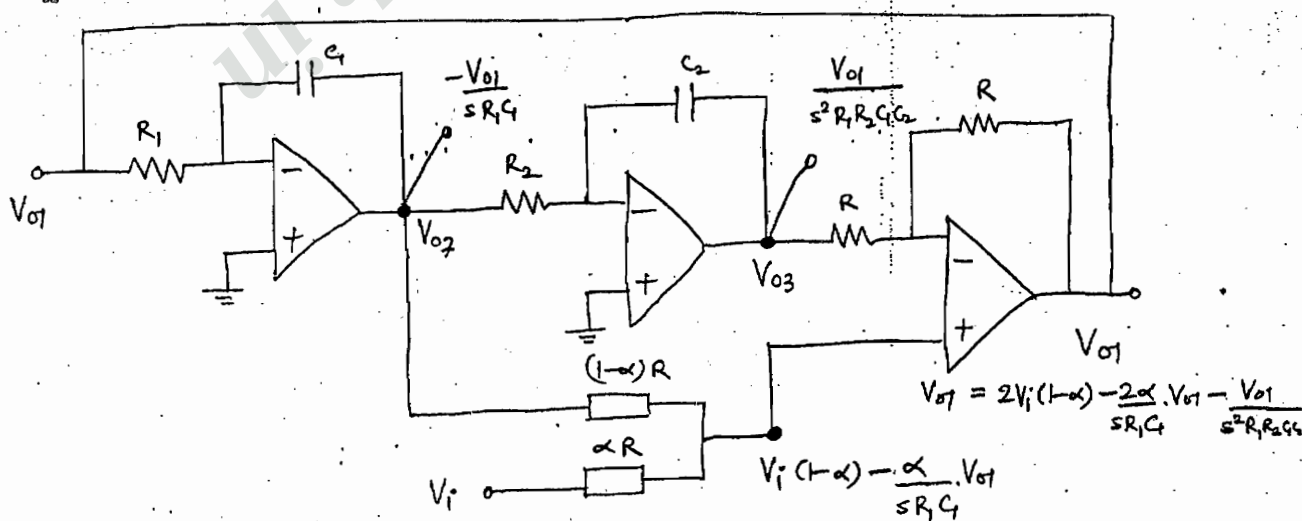
$$\frac{d^2 V_o}{dt^2} + \alpha \frac{dV_o}{dt} + \gamma V_o = V_i$$

$$\frac{d^2 V_o}{dt^2} = V_i - \alpha \frac{dV_o}{dt} - \gamma V_o$$

Time domain analysis



Frequency domain analysis :



$$V_{o1} \left(1 + \frac{2\alpha}{sR_1C_1} + \frac{1}{s^2 R_1 R_2 C_1 C_2} \right) = 2V_i(1-\alpha)$$

$$\frac{V_{o1}}{V_i} = \frac{2(1-\alpha) \cdot s^2 (R_1 R_2 C_1 C_2)}{s^2 R_1 R_2 C_1 C_2 + 2\alpha s R_2 C_2 + 1} \quad (\text{HPF})$$

$$\frac{V_{02}}{V_i} = \frac{-2(1-\alpha) \cdot s R_2 C_2}{s^2 R_1 R_2 C_2 + 2\alpha s R_2 C_2 + 1} \quad (\text{BPF})$$

$$\frac{V_{03}}{V_i} = \frac{2(1-\alpha)}{s^2 R_1 R_2 C_2 + 2\alpha s R_2 C_2 + 1} \quad (\text{LPF})$$

LP + HP $\Rightarrow$ BRF (Notch filter)

$$\frac{V_{04}}{V_i} = \frac{2(1-\alpha) (s^2 R_1 R_2 C_2 + 1)}{s^2 R_1 R_2 C_2 + 2\alpha s R_2 C_2 + 1}$$

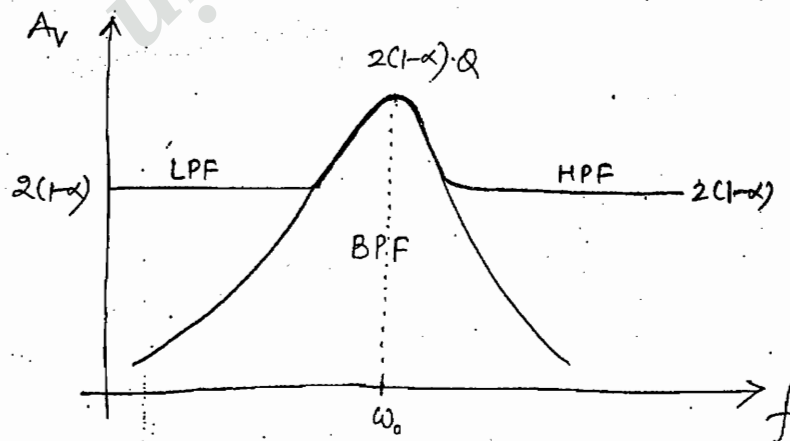
Band pass:

$$\left| \frac{V_{02}}{V_i} \right| = \frac{2(1-\alpha) \cdot s R_2 C_2}{s^2 R_1 R_2 C_2 + 2\alpha s R_2 C_2 + 1}$$

Resonant frequency, $\omega_0 = \frac{1}{\sqrt{R_1 R_2 C_2}}$

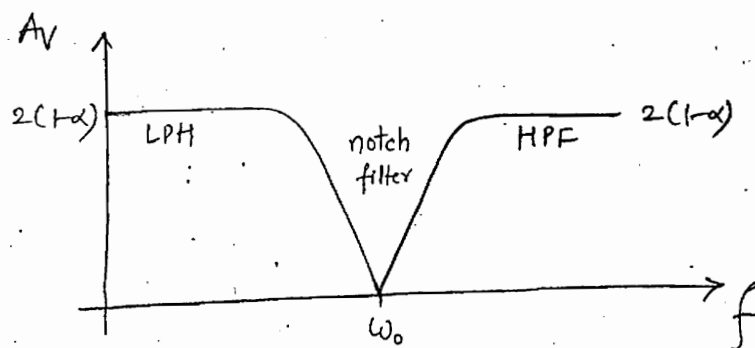
At ω_0 , gain is highest,

$$\left| \frac{V_{02}}{V_i} \right| = \frac{2(1-\alpha)}{2\alpha} = 2(1-\alpha) \cdot Q$$



Band Reject filter:

$$\frac{V_{04}}{V_i} = \frac{2(1-\alpha) (s^2 R_1 R_2 C_2 + 1)}{s^2 R_1 R_2 C_2 + 2\alpha s R_2 C_2 + 1}$$



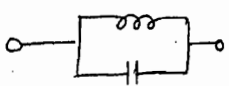
$$\left| \frac{V_{O4}}{V_i} \right| = \frac{2(1-\alpha)}{2\alpha} \leq R_1 C \quad \downarrow \text{zero}$$

$$= 2(1-\alpha) \leq R_1 C \cdot Q$$

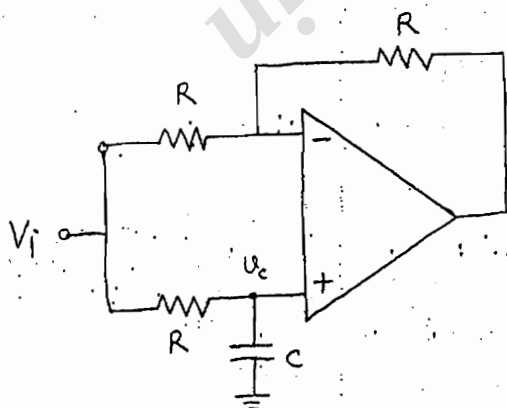
* LC filters are best due to high value Q 1000's multiple
RC filters have Q value in the range of 10's multiple.

for loads:

(1) R_L load $\rightarrow A_v$ may be 100's and 1000's.

(2)  $\rightarrow A_v = \frac{A_i \cdot Z_L}{Z_i}$ at ω_0 , $Z_L = \infty$
 $\therefore A_v = \infty$

All pass filter: (Phase corrector)



By superposition,

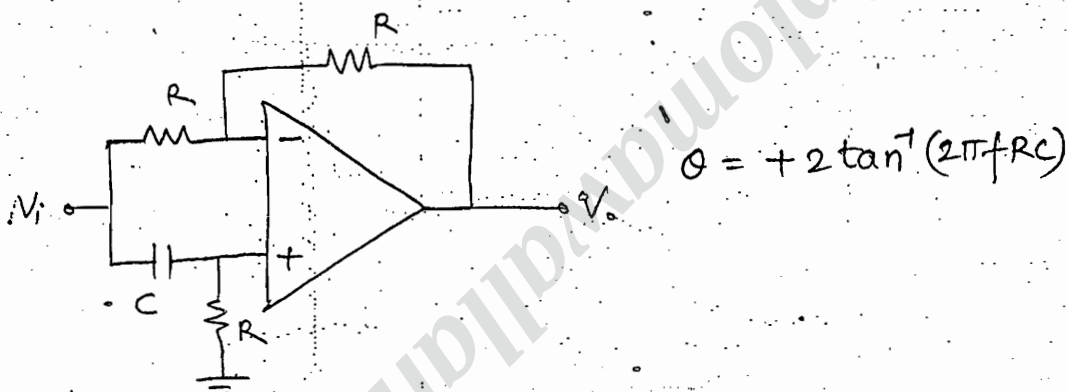
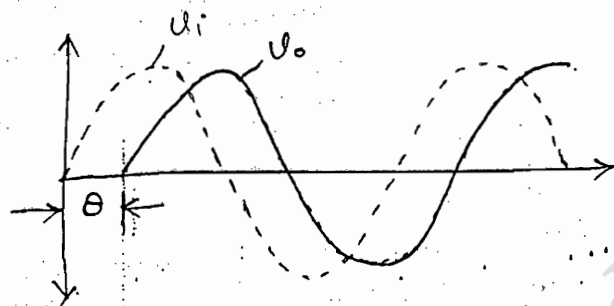
$$\begin{aligned} V_o &= -\frac{R}{R} \cdot V_i + \left(1 + \frac{R}{R}\right) V_c \\ &= -V_i + 2V_c \\ &= -V_i + 2V_i \left(\frac{-jX_c}{R - jX_c} \right) \\ &= V_i \left[-1 + \frac{2}{1 + j2\pi fRC} \right] \\ &= V_i \cdot \left(\frac{1 - j2\pi fRC}{1 + j2\pi fRC} \right) \end{aligned}$$

$$\frac{V_o}{V_i} = \frac{1 - j2\pi fRC}{1 + j2\pi fRC}$$

$$\frac{V_o}{V_i} = \left| \frac{V_o}{V_i} \right| \angle \theta = 1 \cdot \angle -2 \tan^{-1}(2\pi fRC)$$

↓
phase lag.

$$\therefore \theta = -2 \tan^{-1}(2\pi fRC)$$

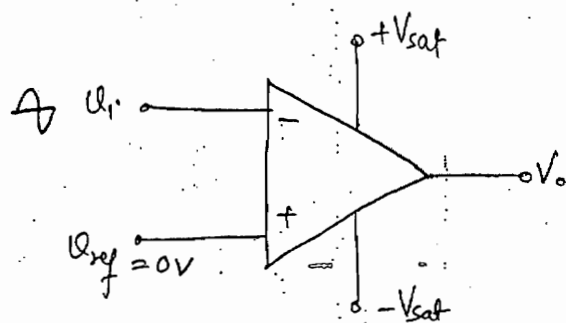


1st order APF $\rightarrow$ can correct $\pm 180^\circ$.

2nd order APF $\rightarrow$ can correct $\pm 360^\circ$.

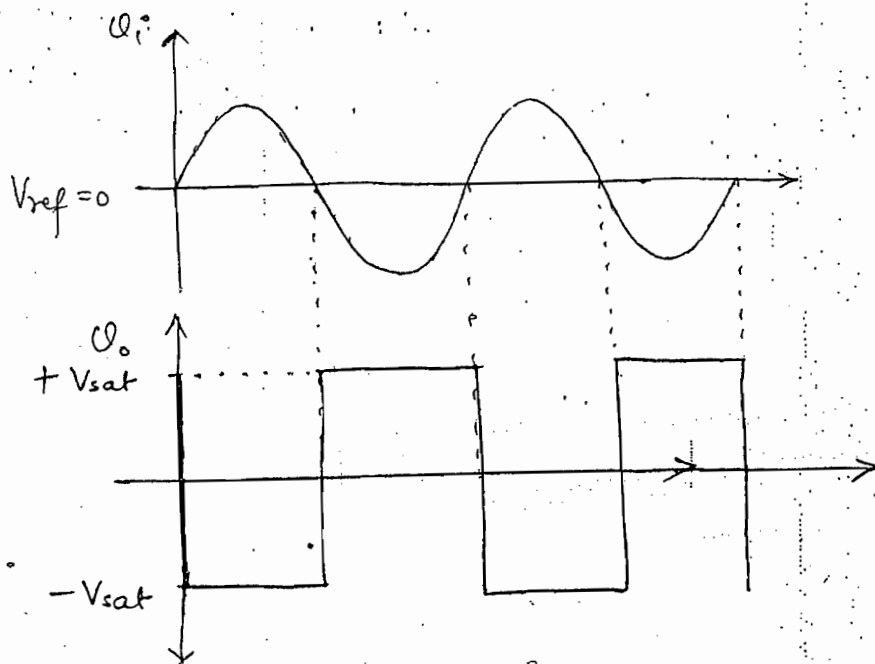
Non-linear applications:

Comparator: Zero crossing detector (ZCD)

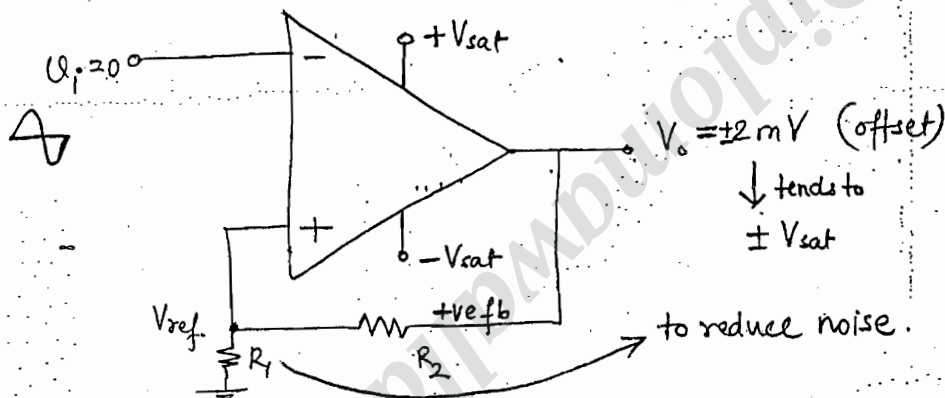


$$u_i > 0 \Rightarrow u_d = -ve \Rightarrow V_o = -V_{sat}$$

$$u_i < 0 \Rightarrow u_d = +ve \Rightarrow V_o = +V_{sat}$$



when V_{ref} is not given : Schmitt trigger



$V_{i0} \rightarrow$ i/p offset voltage "nV"

$I_{i0} \rightarrow$ i/p offset current "nA"

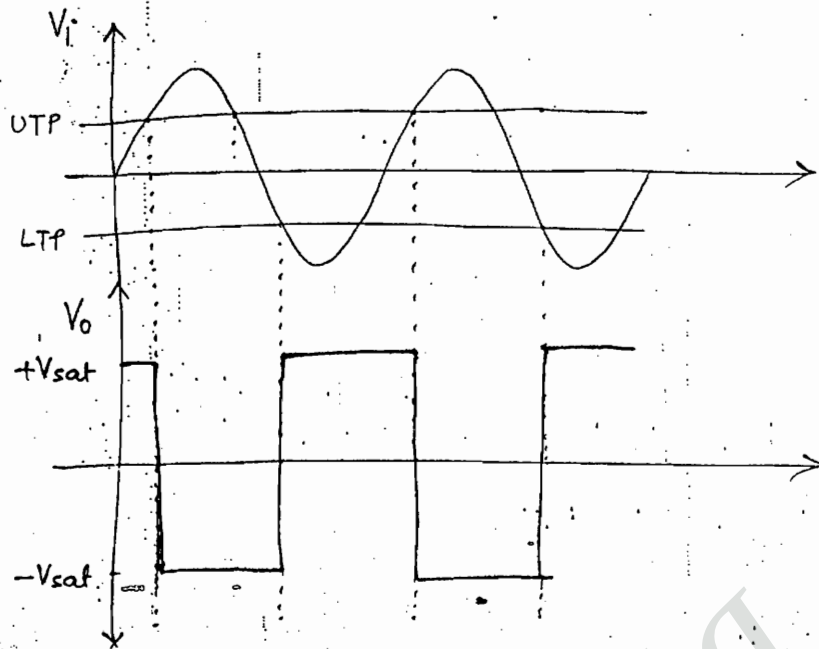
At $V_i = 0$, $V_{i0} \times 10^6 = V_o = \text{nV} \times 10^6 = \text{mV}$.

$$\text{When, } V_o = +V_{sat} \Rightarrow V_{ref1} = \frac{R_1}{R_1 + R_2} V_{sat}$$

$$\text{When, } \underline{V_o = -V_{sat}} \Rightarrow V_{ref2} = \frac{R_1}{R_1 + R_2} \cdot -V_{sat}$$

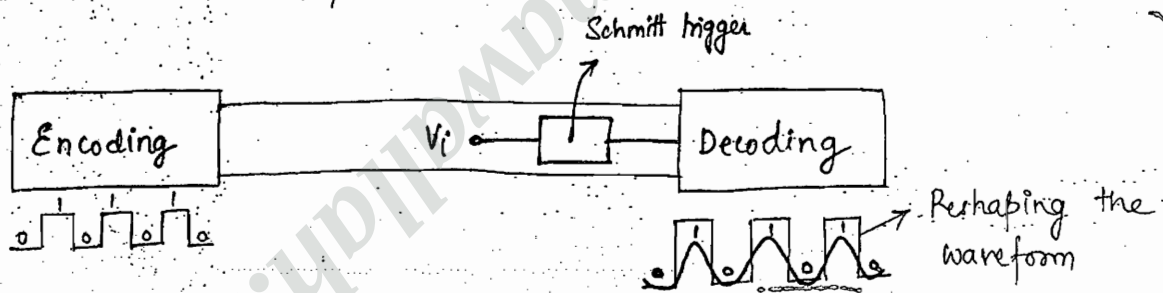
$$\text{Upper trigger point, UTP} = +V_{sat} \cdot \frac{R_1}{R_1 + R_2}$$

$$\text{Lower trigger point, LTP} = -V_{sat} \cdot \frac{R_1}{R_1 + R_2}$$

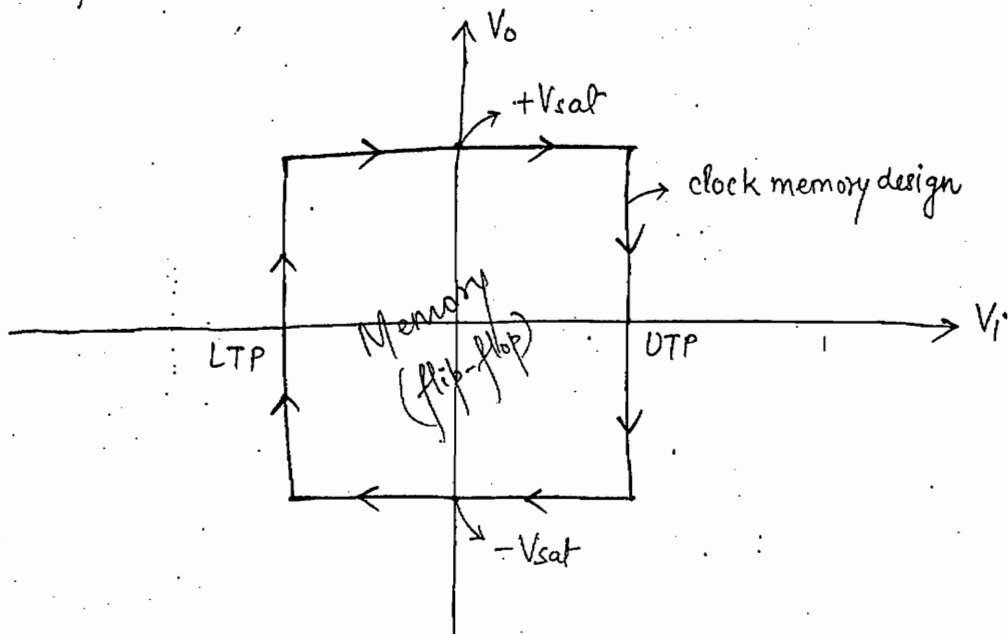


Applications of Schmitt trigger :

Communication system :

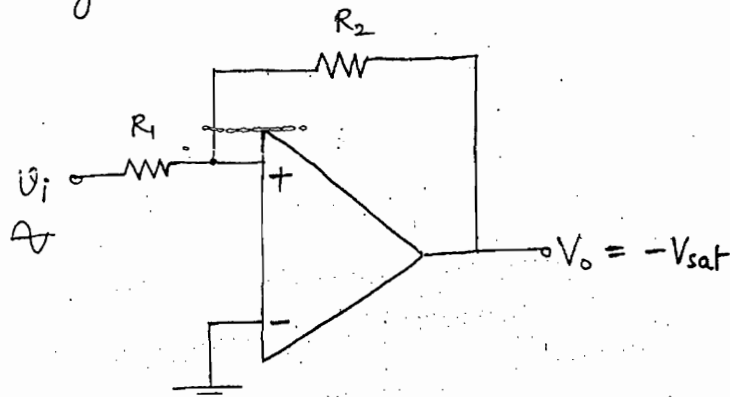


Transfer characteristics :



Q. Design a schmitt trigger which gives anticlock memory design.

Sol.



When $V_o = -V_{sat}$

$$V_i \cdot \frac{R_2}{R_1 + R_2} + V_o \cdot \frac{R_1}{R_1 + R_2} = 0$$

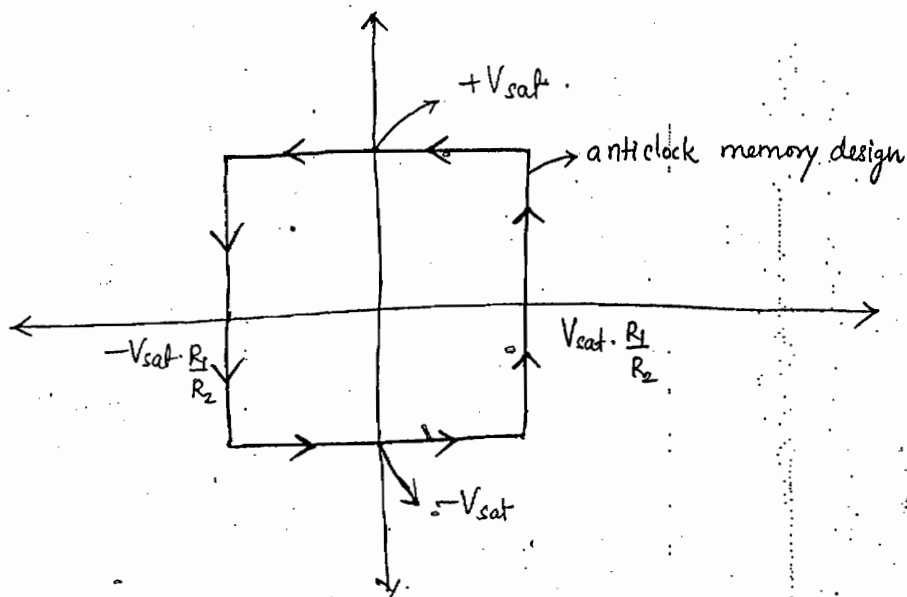
$$V_i \cdot \frac{R_2}{R_1 + R_2} - V_{sat} \cdot \frac{R_1}{R_1 + R_2} = 0$$

$$V_i = +V_{sat} \cdot \frac{R_1}{R_2}$$

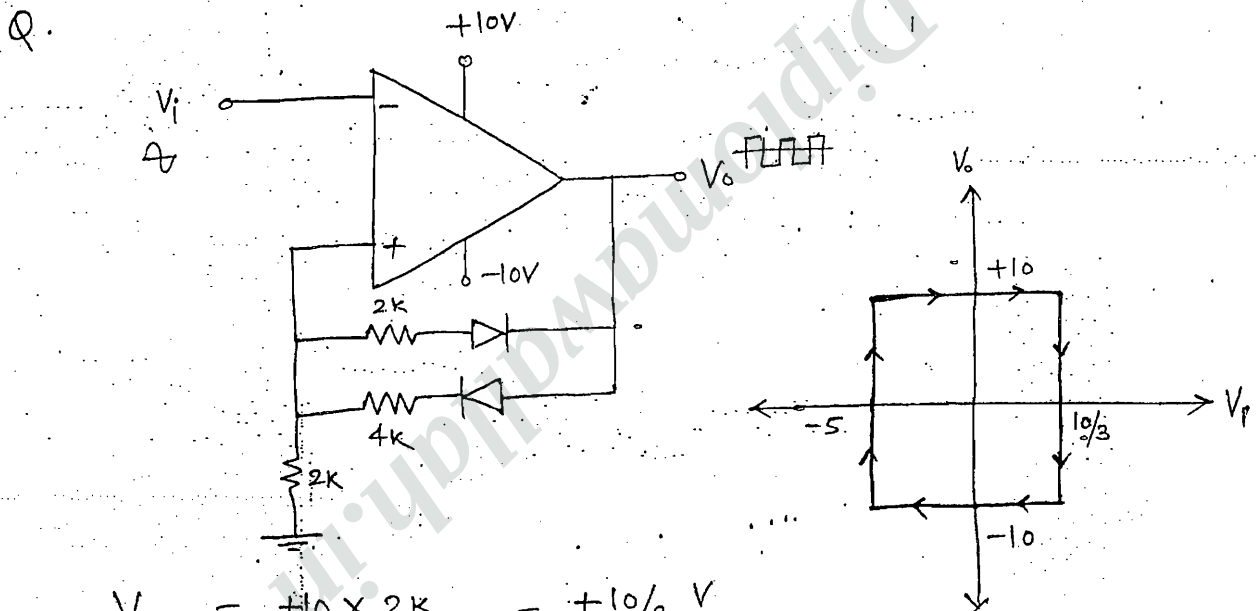
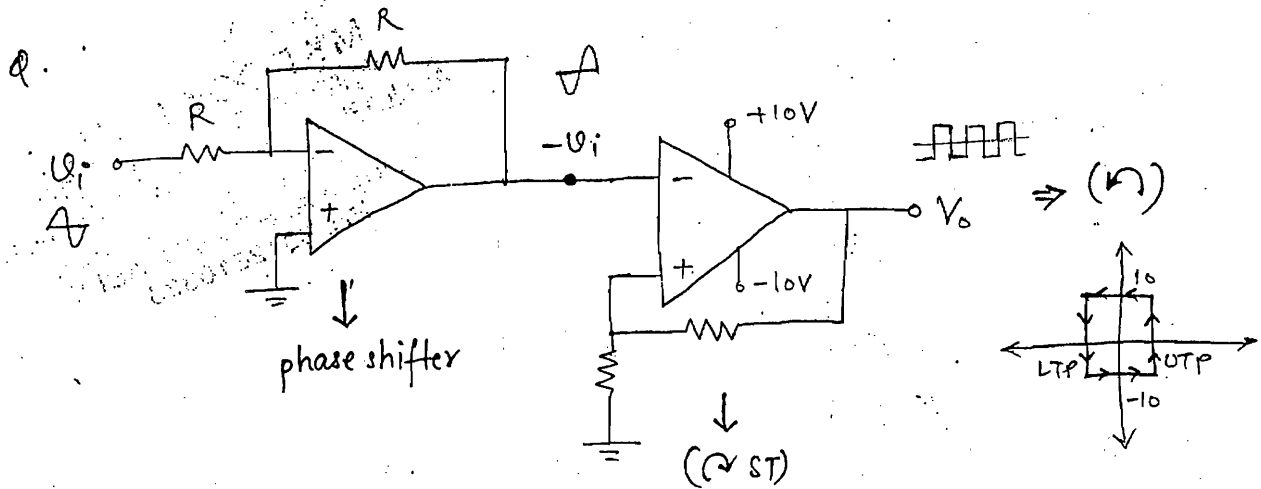
When $V_o = +V_{sat}$

$$V_i \cdot \frac{R_2}{R_1 + R_2} + V_o \cdot \frac{R_1}{R_1 + R_2} = 0$$

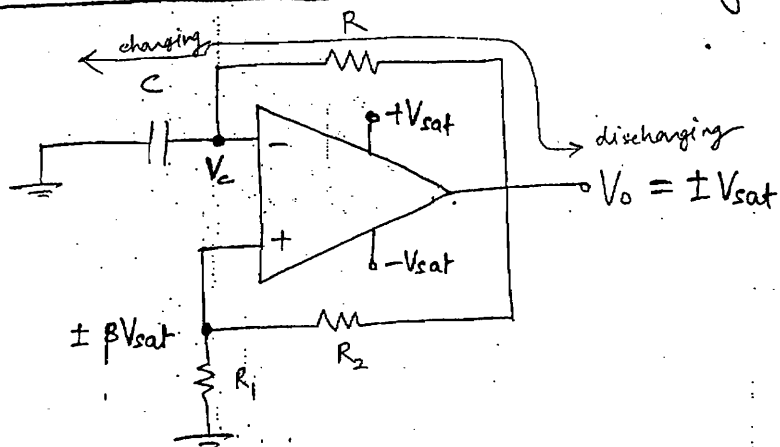
$$V_i = -V_{sat} \cdot \frac{R_1}{R_2}$$



Problems:

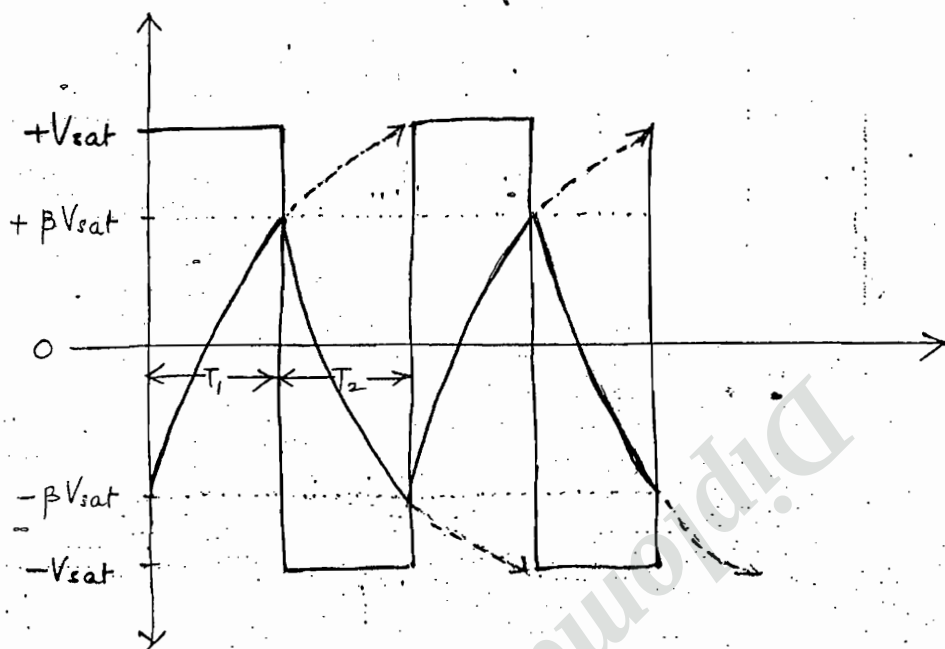


Astable Multivibrator (or) free running oscillator:



$$V_{UTP} = +V_{sat} \cdot \frac{R_1}{R_1 + R_2} = \beta V_{sat}$$

$$V_{LTP} = -V_{sat} \cdot \frac{R_1}{R_1 + R_2} = -\beta V_{sat}$$



$$V_c = V_{final} - (V_{final} - V_{initial}) e^{-t/RC}$$

At $t = T_2$,

$$V_c = -\beta V_{sat} ; V_{final} = -V_{sat} ; V_{initial} = +\beta V_{sat}$$

$$-\beta V_{sat} = -V_{sat} + (1 + \beta) V_{sat} \cdot e^{-T_2/RC}$$

$$(1 - \beta) V_{sat} = (1 + \beta) V_{sat} e^{-T_2/RC}$$

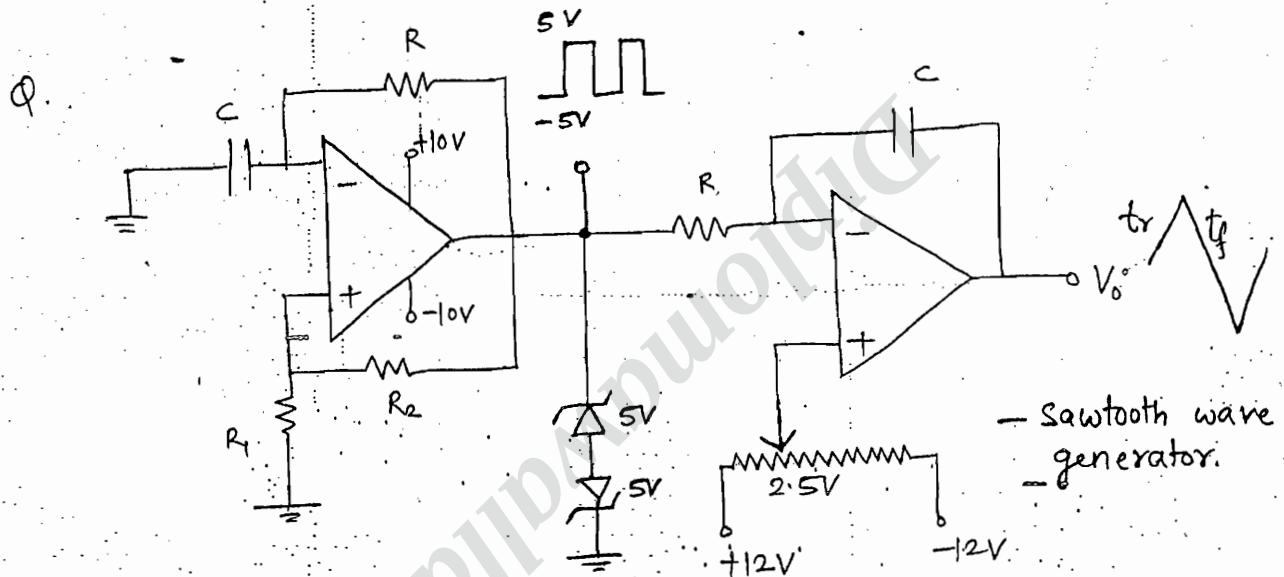
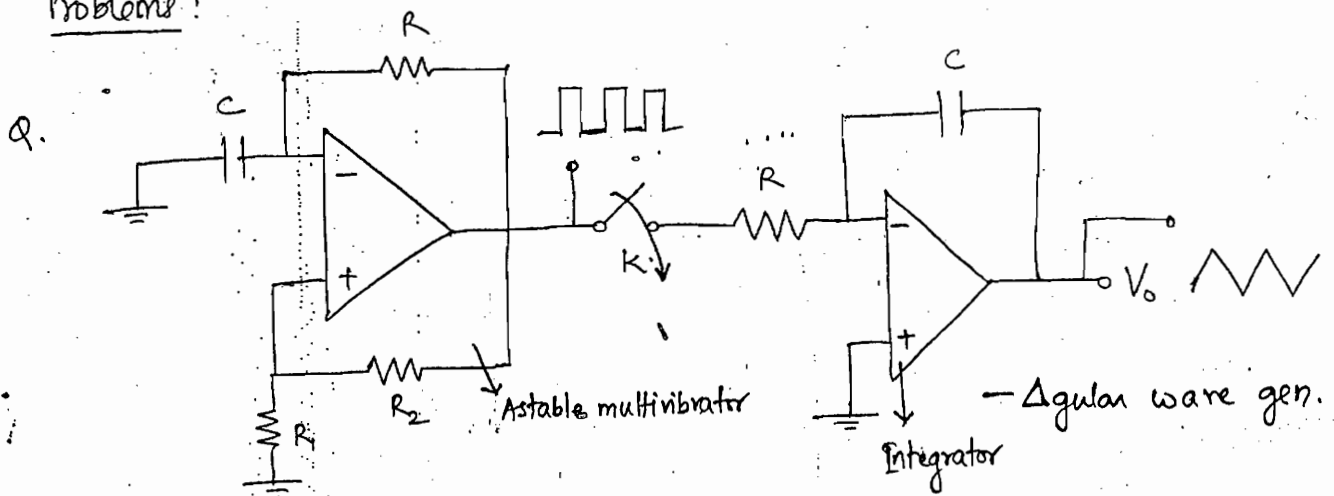
$$T_2 = RC \ln \left(\frac{1 + \beta}{1 - \beta} \right)$$

$$T_1 = RC \ln \left(\frac{1 + \beta}{1 - \beta} \right)$$

$$T = T_1 + T_2 = 2RC \ln \left(\frac{1 + \beta}{1 - \beta} \right)$$

$$f = \frac{1}{2RC \ln \left(\frac{1 + \beta}{1 - \beta} \right)}$$

Problems:

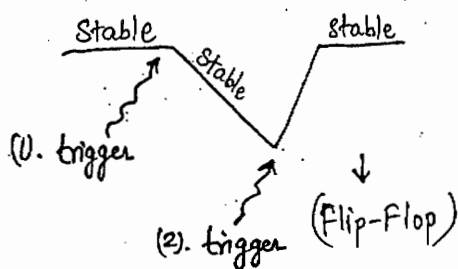


When the wiper moves towards +ve side. $t_f > t_r$
 When the wiper moves towards -ve side. $t_r > t_f$

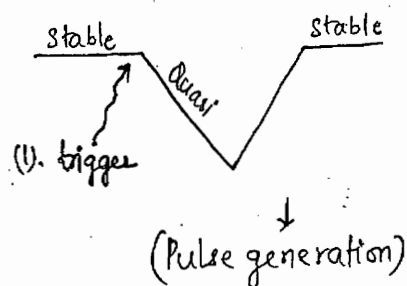
Mono stable multivibrator:

Without capacitor, there is no multivibrator.

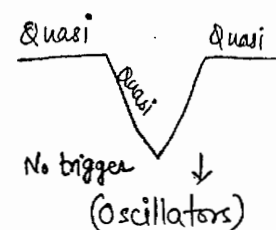
Note: Bistable multivibrator

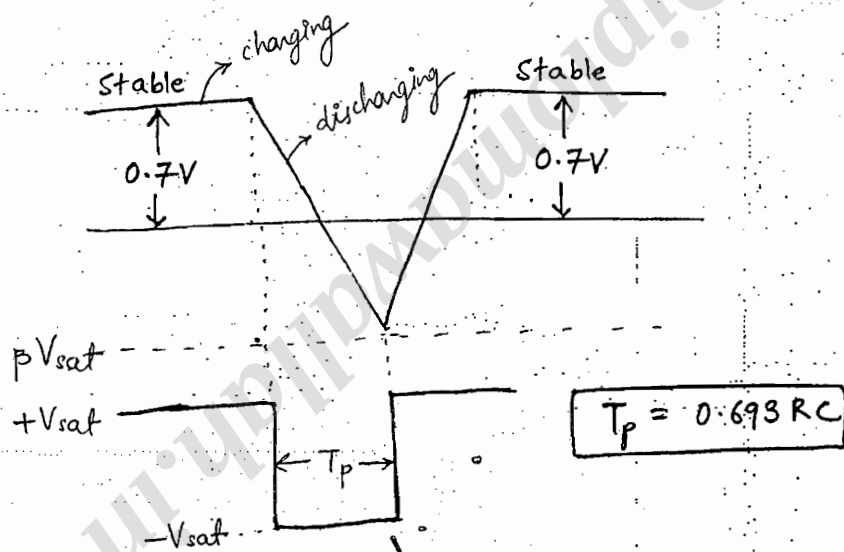
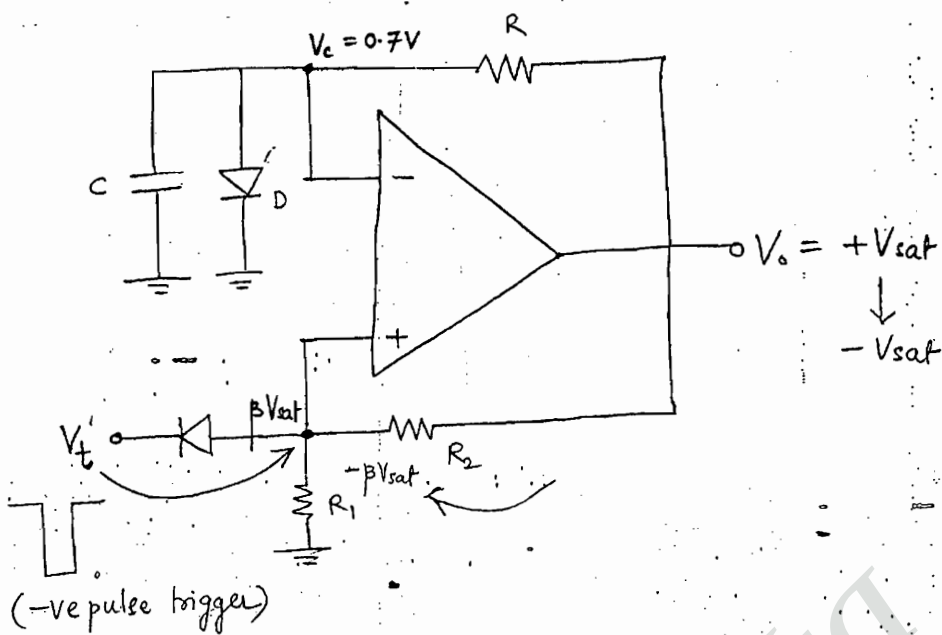


Monostable multivibrator



Astable multivibrator

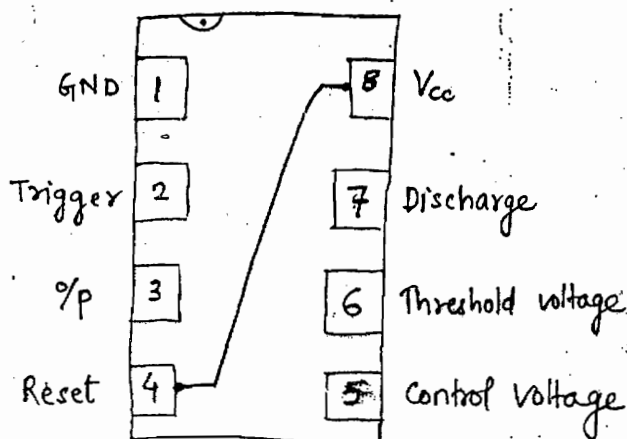


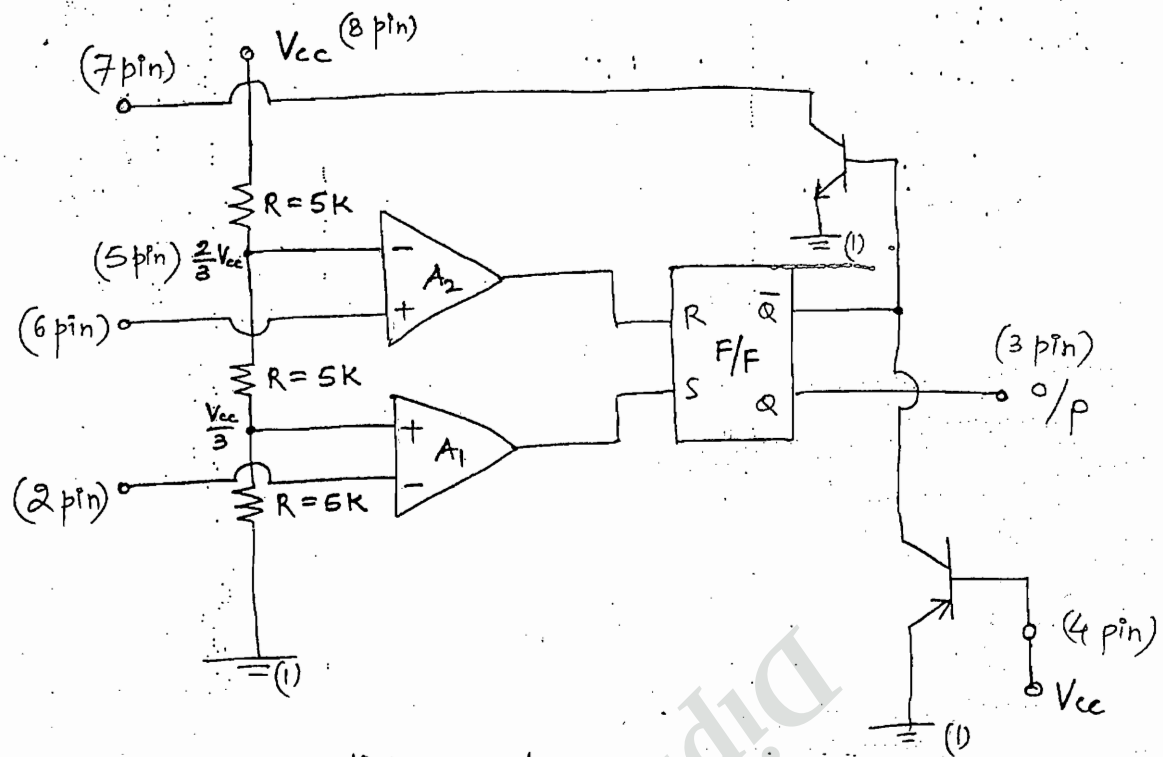


$$T_p = 0.693 RC$$

555 timer

Pin diagram →



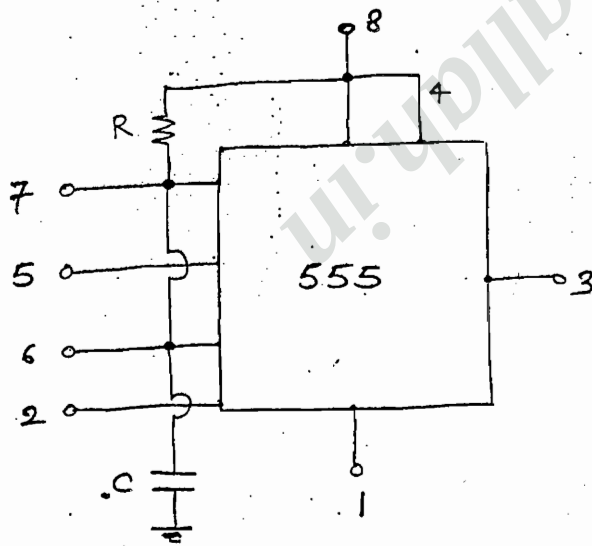


555 timer operating modes :

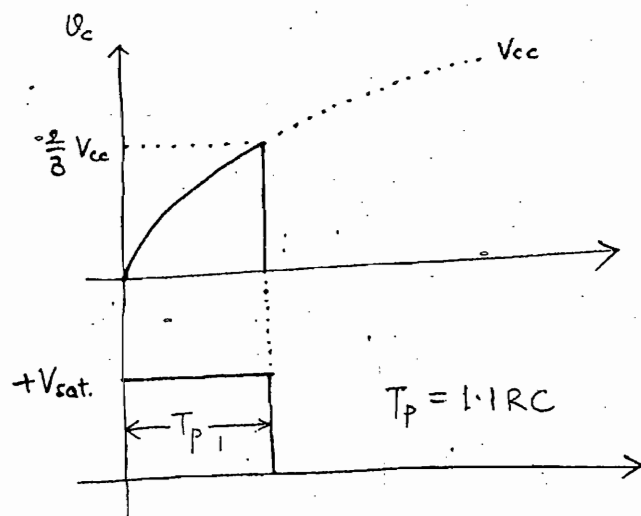
→ Monostable mode

→ Astable mode

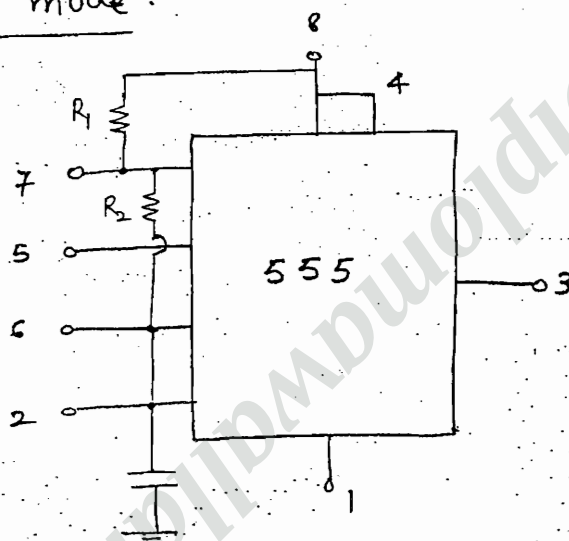
(1). Monostable 555 timer:



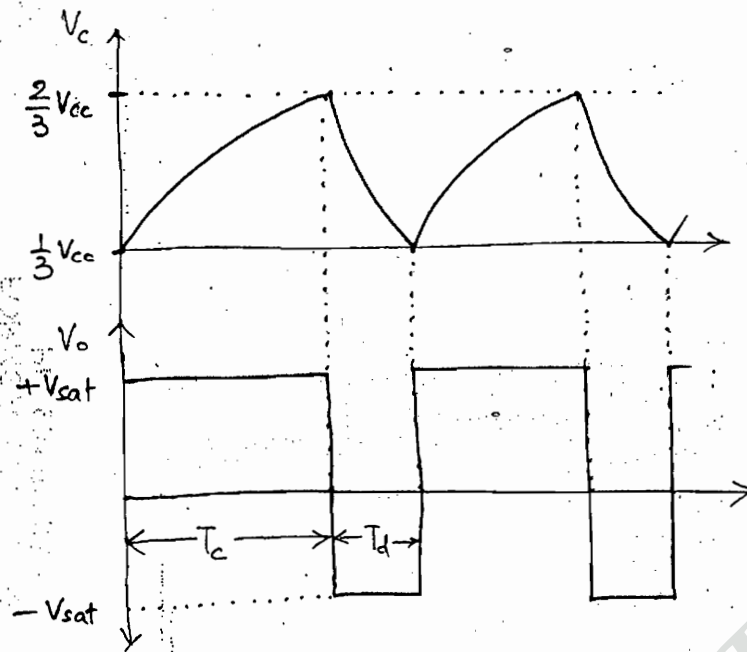
Condition	Q	$\bar{Q}$	0/p	Capacitor position
Stand by	0	1	0	discharging - stable ($V_c = 0$)
trigger $< \frac{1}{3} V_{cc}$	1	0	1	charging - quasi ($V_c = \frac{2}{3} V_{cc}$)
$V_c > \frac{2}{3} V_{cc}$	0	1	0	discharging - stable ($V_c = 0$)



(2). Astable mode :



Condition	Q	$\bar{Q}$	%p	Capacitor position
Stand by	0	1	0	discharging, $V_c = \frac{1}{3} V_{cc}$
$V_c < \frac{1}{3} V_{cc}$	1	0	1	charging, $V_c = \frac{2}{3} V_{cc}$
$V_c > \frac{2}{3} V_{cc}$	0	1	0	discharging, $V_c = \frac{1}{3} V_{cc}$



$$T = T_c + T_d$$

$$T_c = 0.693 (R_1 + R_2) C$$

$$T_d = 0.693 \cdot R_2 C$$

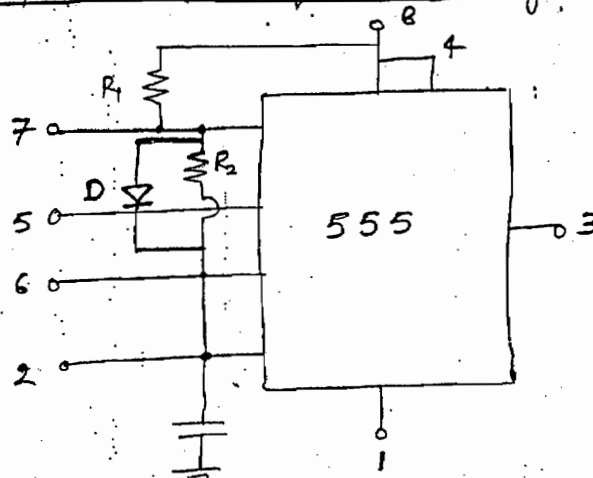
$$T = 0.693 (R_1 + 2R_2) C$$

$$\text{Duty cycle} = \frac{T_c}{T_c + T_d}$$

$$DC = \frac{0.693 (R_1 + R_2) C}{0.693 (R_1 + 2R_2) C}$$

$$DC = \frac{R_1 + R_2}{R_1 + 2R_2}$$

Modification → Square wave generator



$$T_c = 0.693 R_1 C$$

$$DC = \frac{R_1}{R_1 + R_2}$$

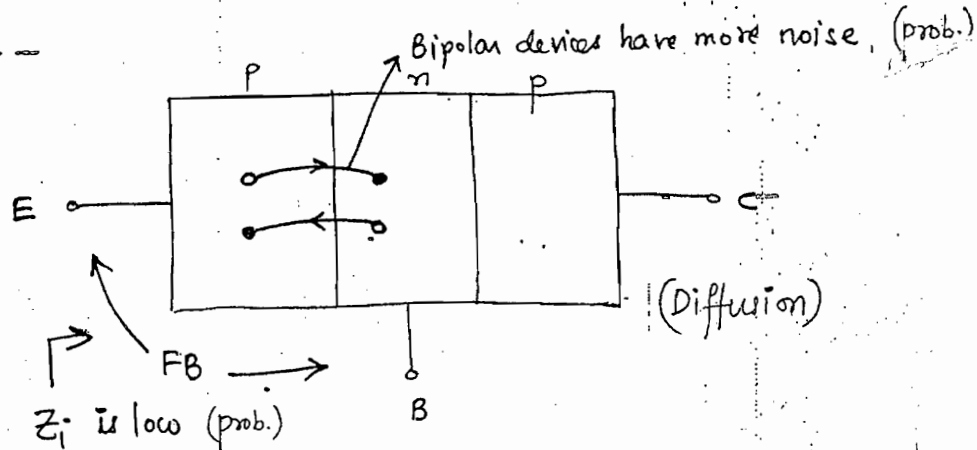
$$\text{if } R_1 = R_2 = R$$

$$DC = \frac{R}{R + R} = 0.5$$

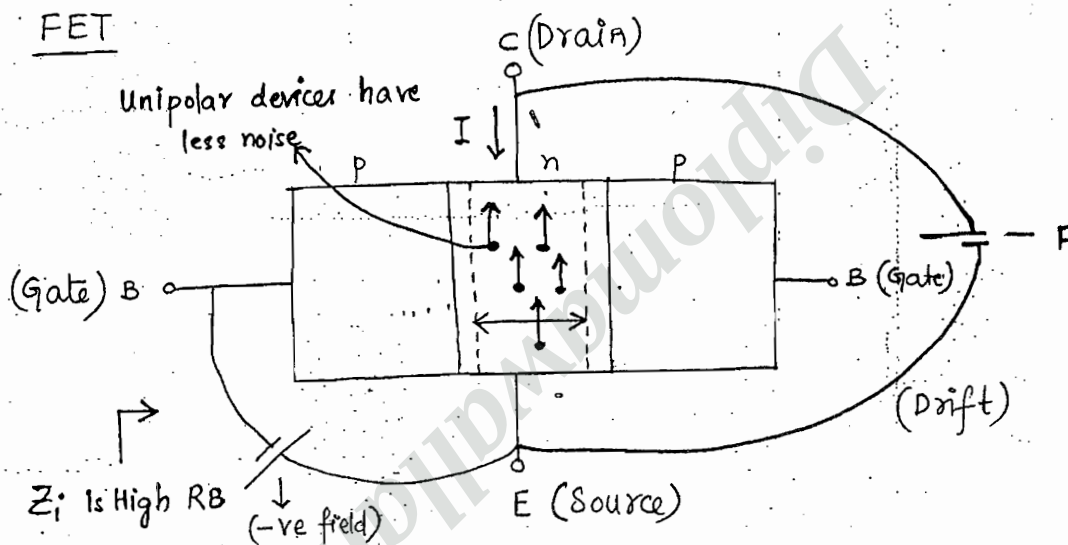
↓
Square wave

Introduction :

BJT



FET



FET is a replacement of BJT. Both problems in BJT are solved in FET.

Bipolar jxn transistor is having following disadvantages:

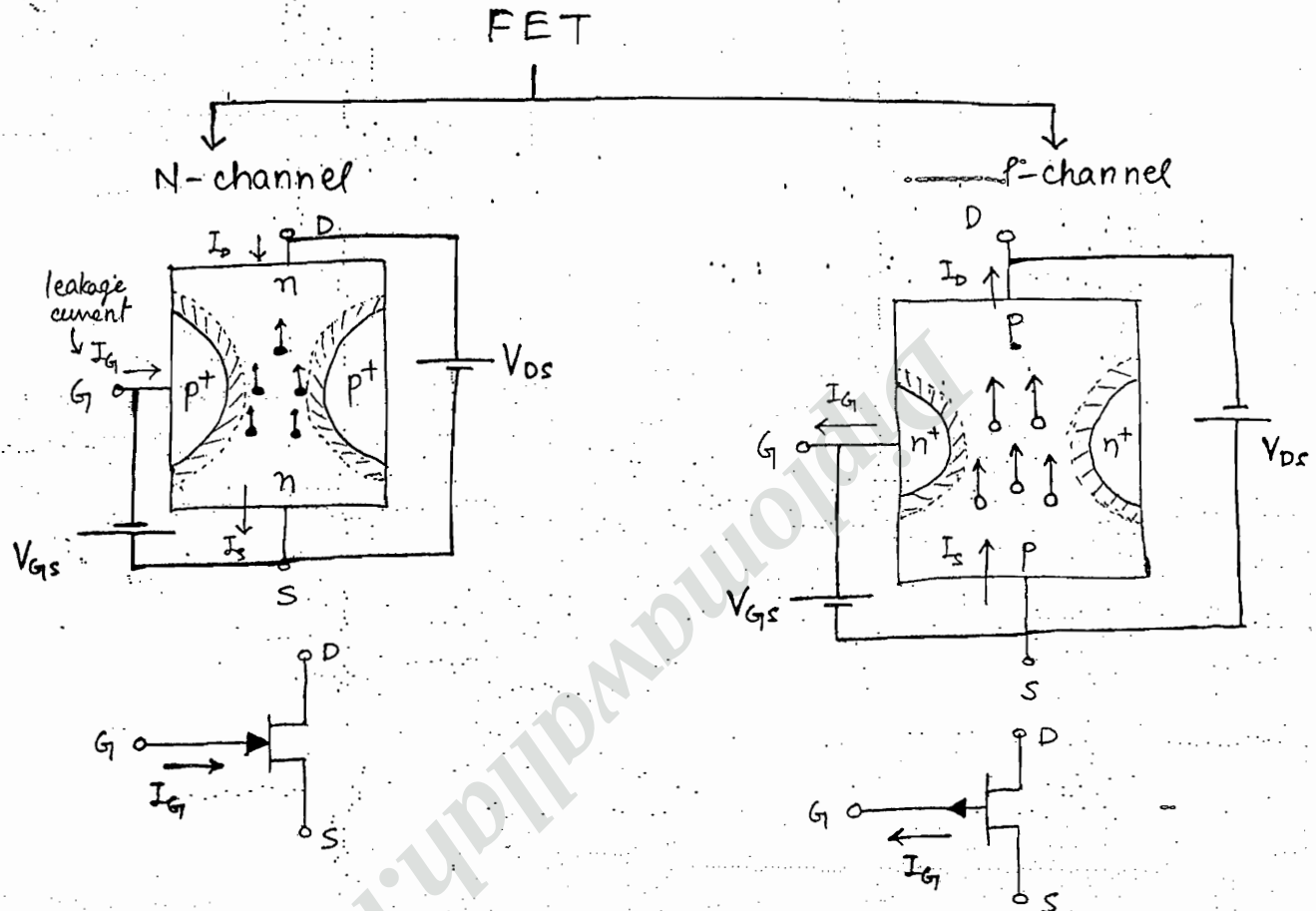
- (1). the i/p impedance is low.
- (2). the noise level is high.

Field Effect transistor is having following advantages compared to BJT :

- (1). the i/p impedance is high becoz the i/p is always R_B
- (2). the noise level is comparatively low becoz it is a unipolar device

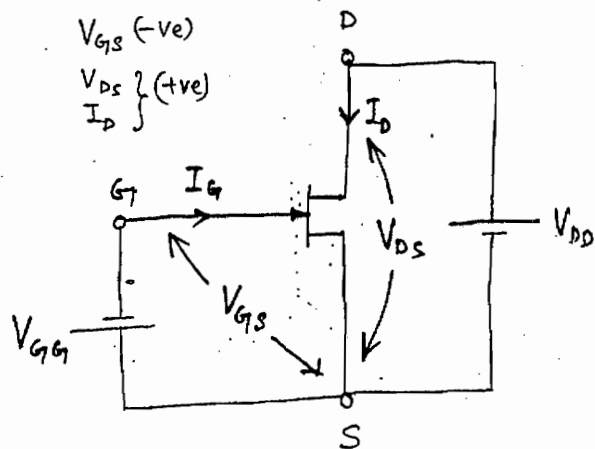
Note: BJT is a current controlled device whereas FET is a voltage controlled device.

Types of FET are :



Important points in FET :

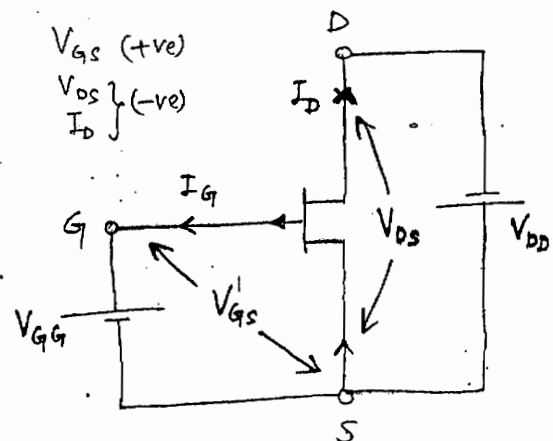
n-channel FET



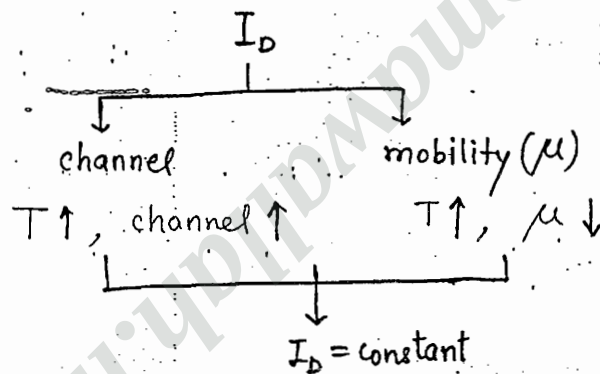
i/p parameters $\rightarrow V_{GS}, I_G$

o/p parameters $\rightarrow V_{DS}, I_D$

p-channel FET



- (1). Gate current I_G is called as leakage current or reverse saturation current. In FET analysis, we are assuming $I_G \approx 0$.
- (2). There will be no i/p characteristic study of FET.
- (3). - There are 2 types of graphs :
 - (i). Drain characteristics (V_{DS} Vs I_D at constant V_{GS})
 - (ii). Transfer characteristics (V_{GS} Vs I_D at constant V_{DS})
- (4). For FET analysis, transfer characteristics study became dominant than drain characteristics.
- (5). The Q point in FET is defined as $Q [(I_D)_Q, (V_{GS})_Q]$.
- (6). FET is always thermally stable.



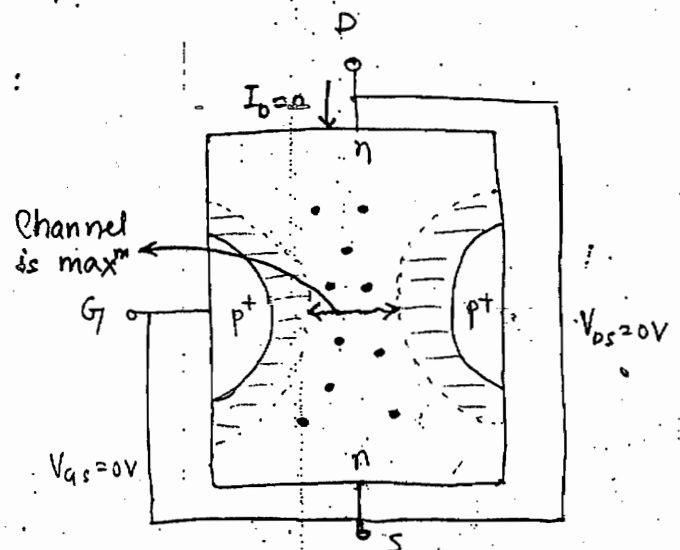
- (7). For FET biasing, we have to fix V_{GS} voltage but not drain current I_D .

Working principle of FET :

Case - (1) :

$$V_{GS} = 0V$$

$$V_{DS} = 0V$$



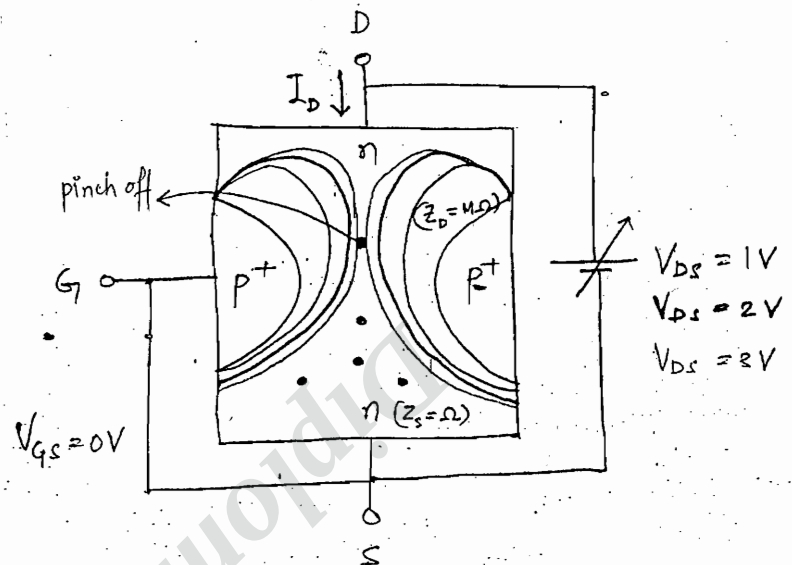
Conclusions:

- (1). When $V_{GS} = 0V$, $\max^m$ channel can be achieved
- (2). When $V_{DS} = 0V$, the drain current I_D becomes zero.

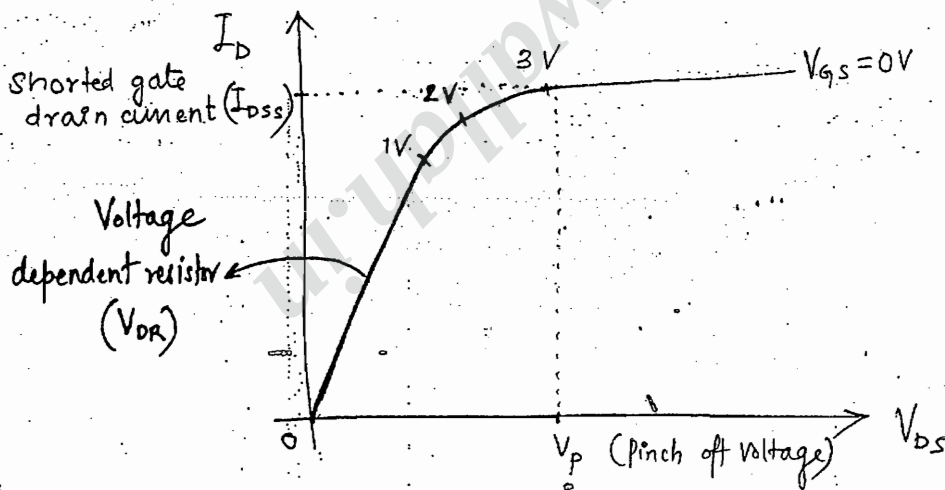
Case - (2):

$$V_{GS} = 0V$$

$$V_{DS} \neq 0V$$



Characteristics (V_{DS} Vs I_D):

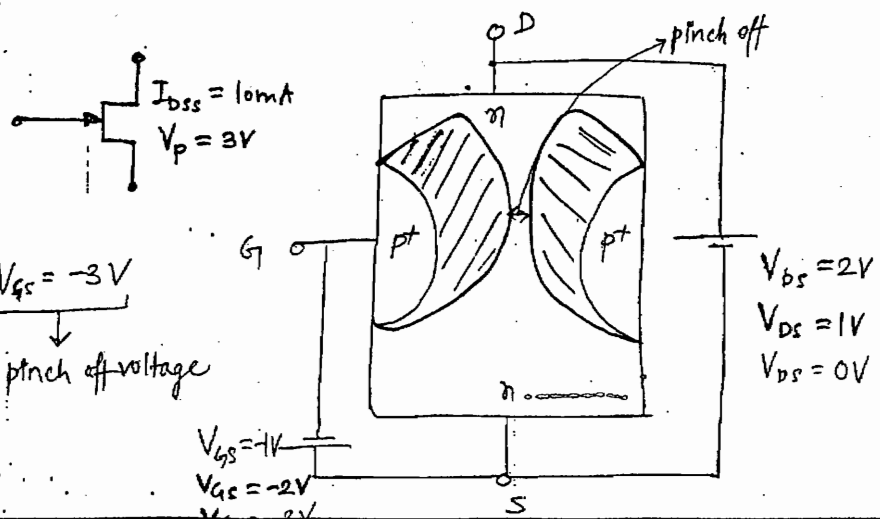


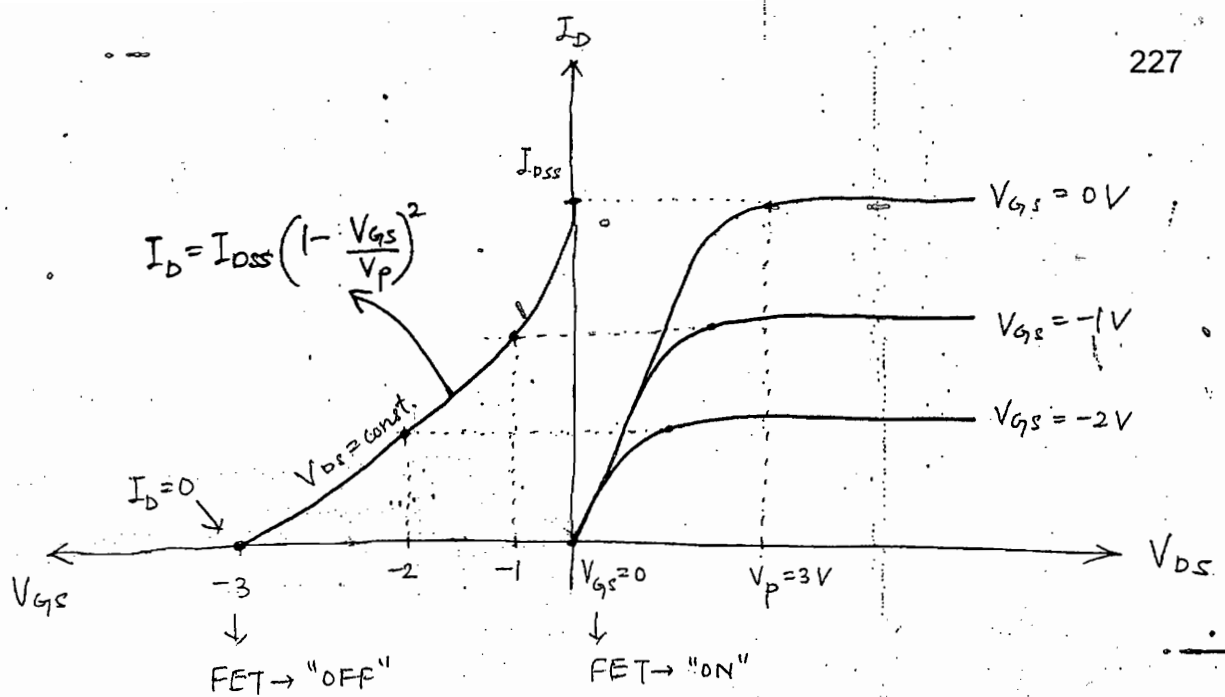
Case - (3):

$$V_{GS} \neq 0V$$

$$V_{DS} \neq 0V$$

When $V_{DS} = 0V$, $V_{GS} = -3V$
 pinch off voltage



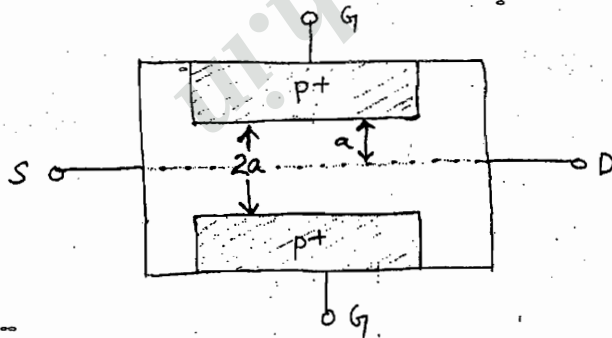


(1). Transfer characteristic eqn is

$$I_D = I_{DSS} \left(1 - \frac{V_{GS}}{V_P}\right)^2$$

(2). $|V_{GS}|_{off} = |V_P|$

(3). $V_P = \frac{q^2 q_f N_A}{2 \epsilon}$ (p-channel) (or) $\frac{q^2 q_f N_D}{2 \epsilon}$ (n-channel)



$2a \rightarrow$ full channel Height
 $a \rightarrow$ Half channel Height.

* On increasing doping, penetration decreases and Hence pinch off voltage increases.

FET parameters:

- (1). Ac drain resistance, $r_d = \left. \frac{\Delta V_{DS}}{\Delta I_D} \right|_{V_{GS} = \text{const.}}$
- (2). Trans conductance, $g_m = \left. \frac{\Delta I_D}{\Delta V_{GS}} \right|_{V_{DS} = \text{const.}}$
- (3). Amplification factor, $\mu = \left. \frac{\Delta V_{DS}}{\Delta V_{GS}} \right|_{I_D = \text{const.}}$

$$\mu = g_m \cdot r_d$$

Expression for g_m :

$$I_D = I_{DSS} \left(1 - \frac{V_{GS}}{V_p}\right)^2$$

$$\frac{dI_D}{dV_{GS}} = -2 \frac{I_{DSS}}{V_p} \left(1 - \frac{V_{GS}}{V_p}\right)$$

$$g_m = \frac{\partial I_D}{\partial V_{GS}} = -\frac{2 I_{DSS}}{V_p} \left[1 - \frac{(V_{GS})_Q}{V_p}\right]$$

$$g_m = g_{m0} \left[1 - \frac{(V_{GS})_Q}{V_p}\right]$$

where, $g_{m0} = \frac{-2I_{DSS}}{V_p}$
 $\downarrow$
 (max^m transconductance)

And,

$$I_D = I_{DSS} \left(1 - \frac{V_{GS}}{V_p}\right)^2 \Rightarrow \left(1 - \frac{V_{GS}}{V_p}\right) = \sqrt{\frac{I_D}{I_{DSS}}}$$

$$g_m = -\frac{2 I_{DSS}}{V_p} \left(1 - \frac{V_{GS}}{V_p}\right)$$

$$= -\frac{2 I_{DSS}}{V_p} \sqrt{\frac{I_D}{I_{DSS}}}$$

$$\therefore g_m = -\frac{2}{V_p} \sqrt{I_{DSS} \cdot (I_D)_Q} = \frac{2}{|V_p|} \sqrt{I_{DSS} \cdot (I_D)_Q}$$

FET Biasing :

Conditions :

$$(1) \cdot I_G \approx 0$$

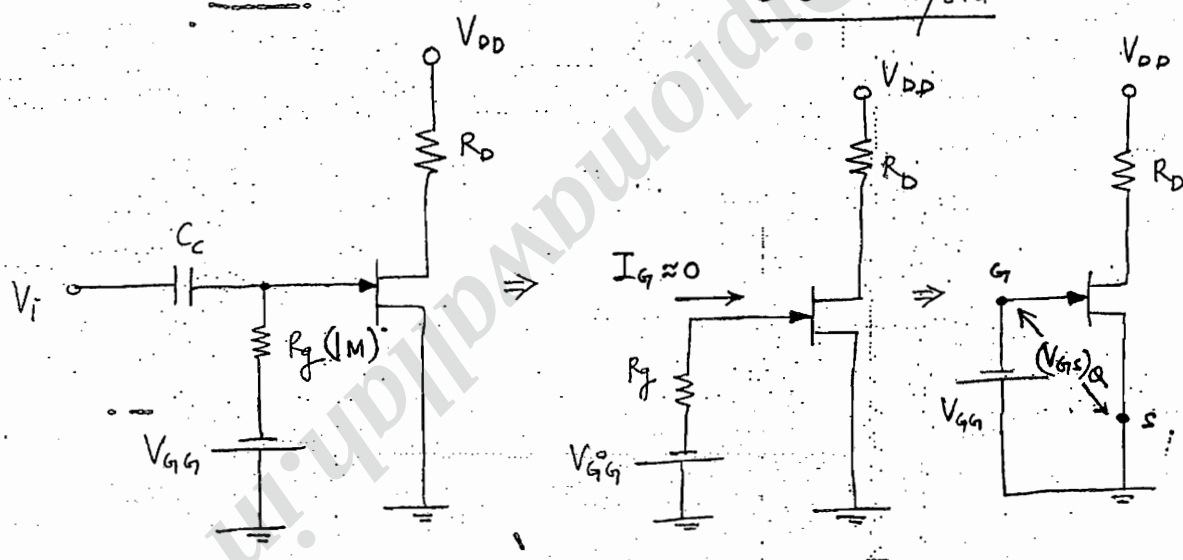
$$(2) \cdot I_D \approx I_S$$

$$(3) \cdot I_D = I_{DSS} \left(1 - \frac{V_{GS}}{V_P}\right)^2$$

$$(4) \cdot Q \text{ point} \rightarrow Q \left[(I_D)_Q, (V_{GS})_Q \right]$$

(1). Fixed Biasing -

Model - (1) : Common source bypass



• R_g are meant for AC s/g only

$$(1) \cdot (V_{GS})_Q = -V_{GS} \text{ (fixed) - Biasing by keeping a battery.}$$

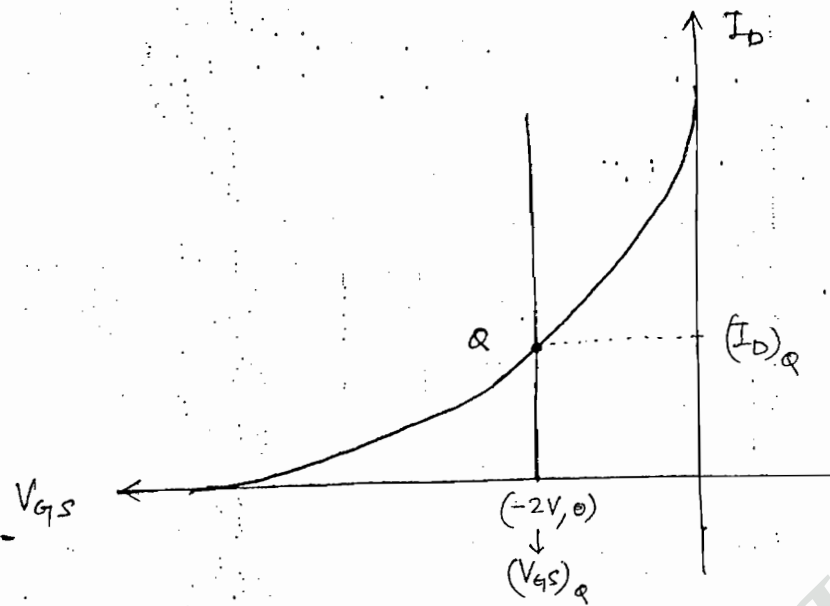
$$(2) \cdot (I_D)_Q = I_{DSS} \left[1 - \frac{(V_{GS})_Q}{V_P}\right]^2$$

$$(3) \cdot (V_{DS})_Q = V_{DD} - (I_D)_Q \cdot R_D$$

$$(4) \cdot (V_D)_Q = (V_{DS})_Q$$

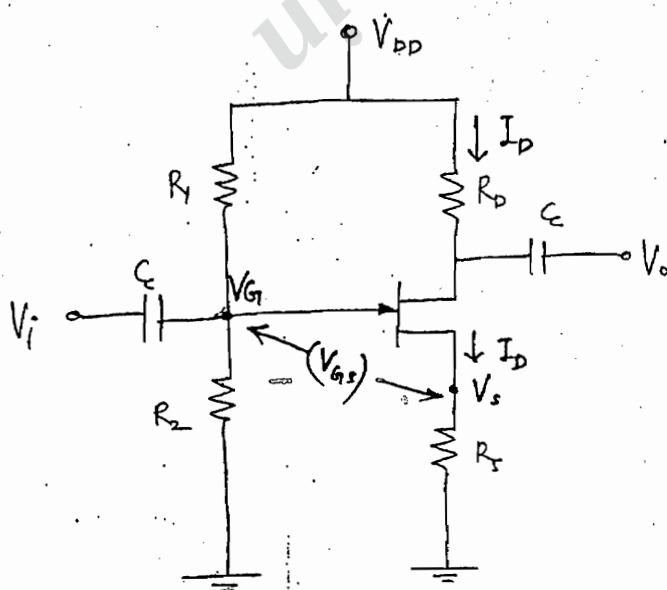
$$(5) \cdot (V_S)_Q = 0$$

$$(6) \cdot (V_G)_Q = (V_{GS})_Q$$

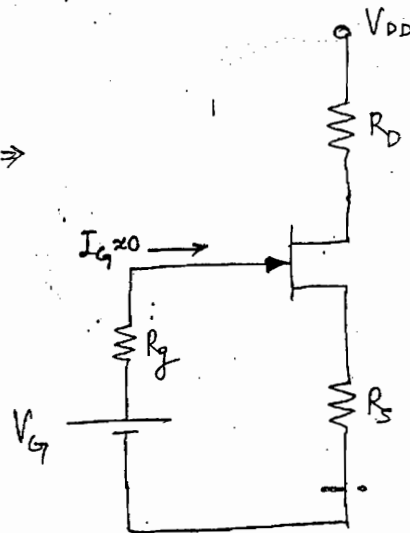


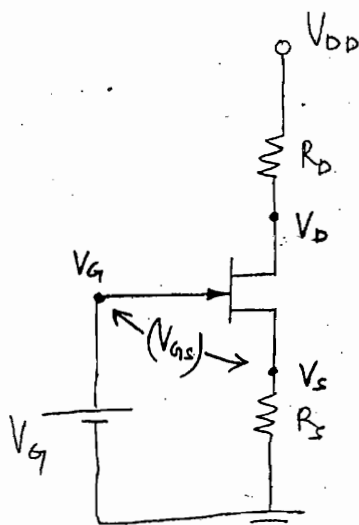
This biasing is not good becoz battery can vary the Q point as there are fluctuations in battery voltage. This is not a stable biasing. Hence, some modifications are required. Bypass amplifiers are always unstable.

(2) Voltage Divider Biasing:



DC analysis



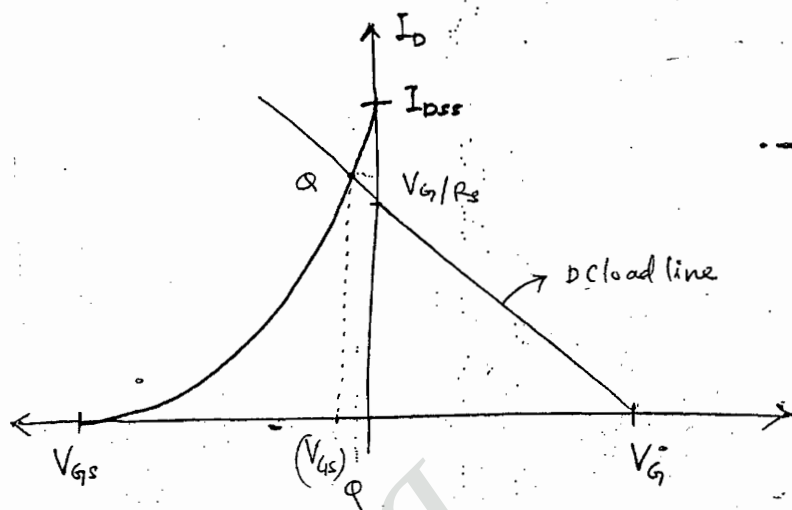


$$V_G = \frac{V_{DD} \times R_2}{R_1 + R_2}$$

$$R_G = R_1 \parallel R_2$$

$$V_G = V_{GS} + I_D \cdot R_S$$

$$V_{GS} = V_G - I_D R_S$$



$$(1) \quad I_D = I_{DSS} \left(1 - \frac{V_{GS}}{V_P}\right)^2 \quad \text{and} \quad V_{GS} = V_G - I_D R_S$$

Solving we get,

$$(I_D)_Q = \frac{-b \pm \sqrt{b^2 - 4ae}}{2a}$$

+ve root (n-channel)

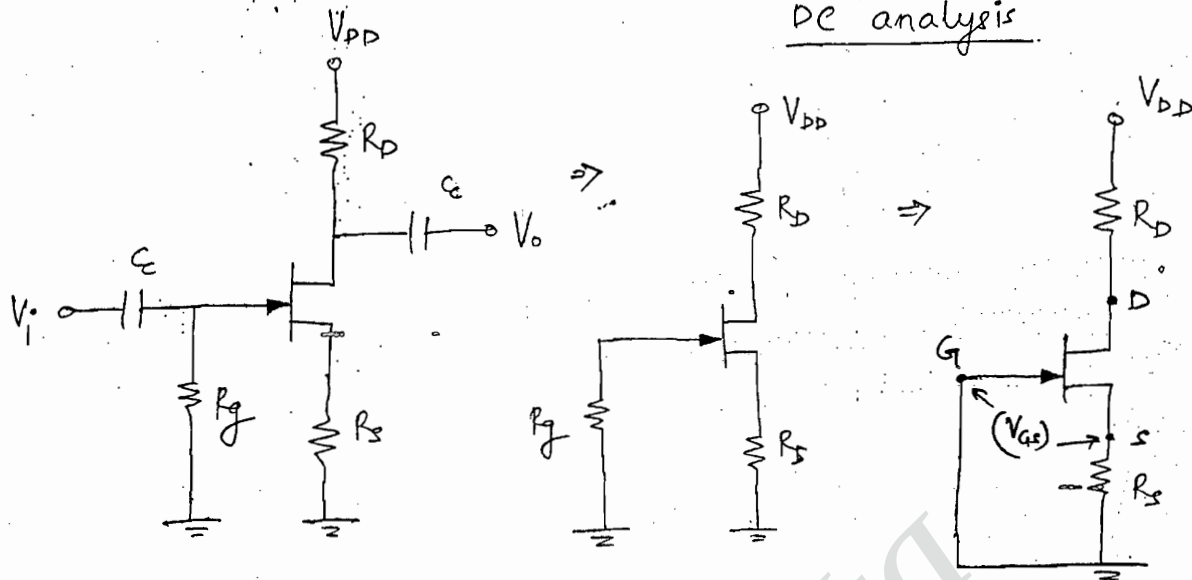
-ve root (p-channel)

$$(2) \quad (V_{GS})_Q = V_G - (I_D)_Q \cdot R_S$$

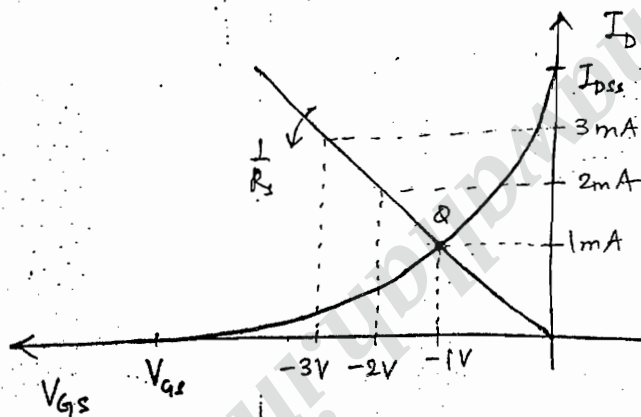
- Fixed biasing uses 2 batteries whereas this biasing is using single battery so it is an improved biasing but due to the presence of battery, it is also not much stable. Hence, more modification is required.

(3). Self Biasing (Stable Biasing): Common source unbypass

DC analysis



$$V_{GS} = -I_D R_S \rightarrow \text{Ohm's law}$$



$$(1) \quad I_D = I_{DSS} \left(1 - \frac{V_{GS}}{V_P}\right)^2 \quad \text{and} \quad V_{GS} = -I_D R_S$$

$$(I_D)_Q = \frac{-b \pm \sqrt{b^2 - 4ac}}{2a} \quad \begin{cases} \text{+ve root (n-channel)} \\ \text{-ve root (p-channel)} \end{cases}$$

$$(2) \quad (V_{GS})_Q = -(I_D)_Q R_S$$

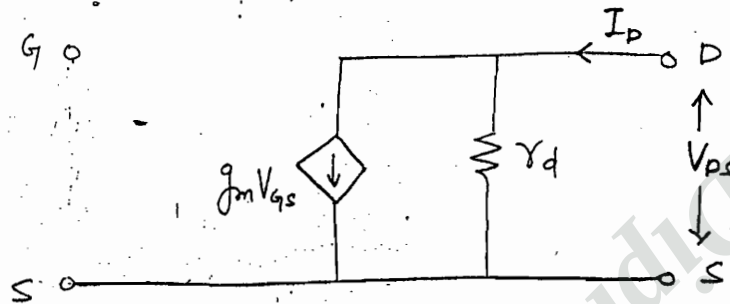
- Self biasing is a stable biasing and improved biasing as it does not depend upon battery voltage.
- Unbypass amplifiers are always stable.

FET model :

$$I_D = f(V_{GS}, V_{DS})$$

$$I_D = \frac{\partial I_D}{\partial V_{GS}} \cdot V_{GS} + \frac{\partial I_D}{\partial V_{DS}} \cdot V_{DS}$$

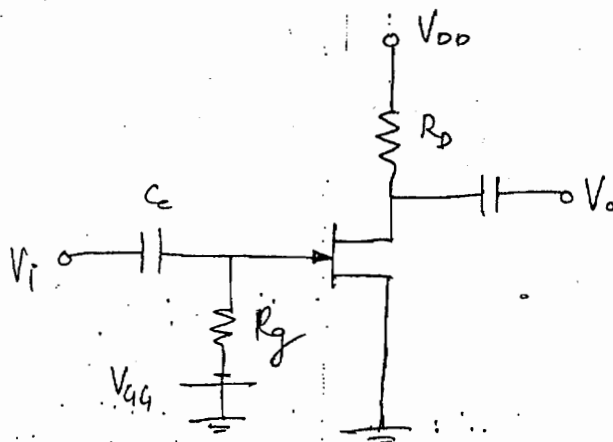
$$I_D = g_m \cdot V_{GS} + \frac{V_{DS}}{r_d}$$

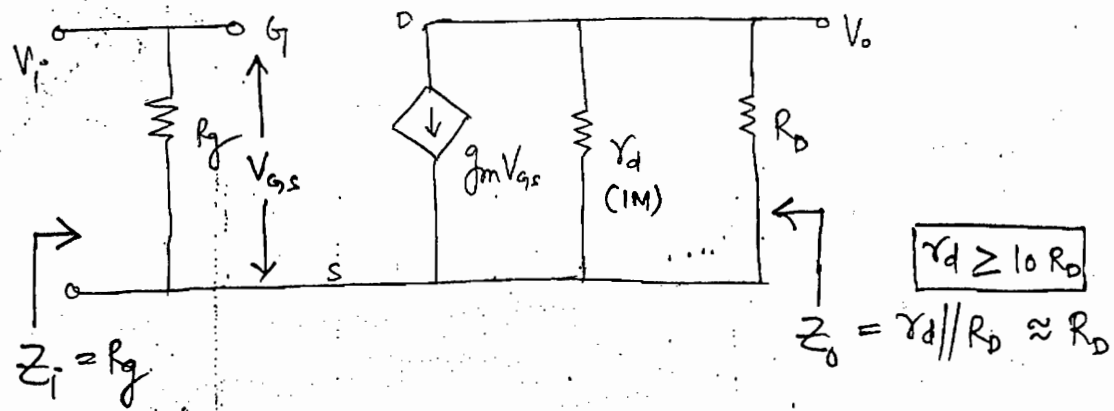


FET amplifiers :

- (1). CS bypass amplifier
- (2).. CS unbypass amplifier
- (3). CD amplifier
- (4). CG amplifier

(1). CS bypass amplifier :





Voltage gain:

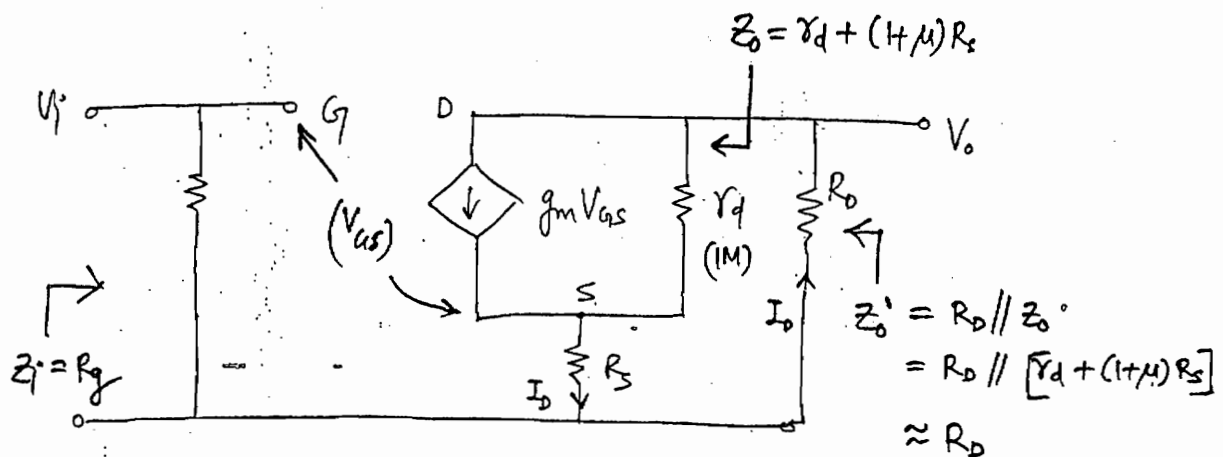
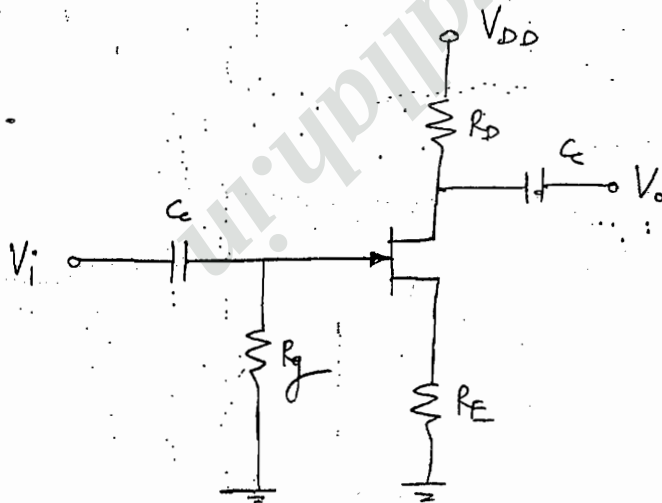
$$V_o = -g_m V_{gs} (R_d \parallel R_L)$$

$$V_{gs} = V_i$$

$$V_o = -g_m V_i R_d$$

$$A_v = \frac{V_o}{V_i} = -g_m R_d$$

CS unbypass amplifier:



Voltage gain:

$$V_o = -I_D R_D \quad \text{--- (1)}$$

$$I_D R_D + (I_D - g_m V_{gs}) r_d + I_D R_S = 0 \quad \text{--- (2)}$$

$$V_i = V_{gs} + I_D R_S \quad \text{--- (3)}$$

$$V_{gs} = V_i - I_D R_S$$

$$I_D R_D + [I_D - g_m (V_i - I_D R_S)] r_d + I_D R_S = 0$$

$$I_D R_D + I_D r_d - g_m r_d (V_i - I_D R_S) + I_D R_S = 0$$

$$I_D (R_D + r_d + R_S + g_m r_d R_S) = g_m r_d V_i$$

$$I_D = \frac{g_m r_d V_i}{(R_D + R_S + r_d + g_m r_d R_S)}$$

$$V_o = -I_D R_D$$

$$V_o = \frac{-g_m r_d R_D V_i}{(R_D + R_S + r_d + g_m r_d R_S)}$$

$$\frac{V_o}{V_i} = \frac{-g_m r_d R_D}{(R_D + R_S + r_d + g_m r_d R_S)}$$

$$\approx \frac{-g_m R_D}{\left(\frac{R_D}{r_d} + \frac{R_S}{r_d} + 1 + g_m R_S \right)}$$

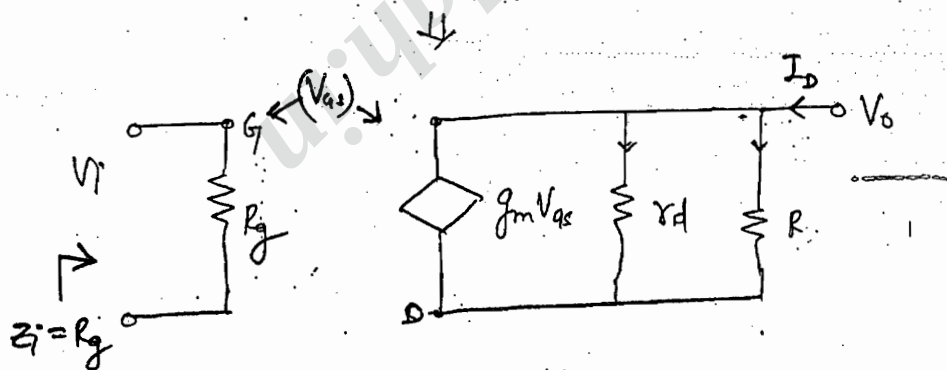
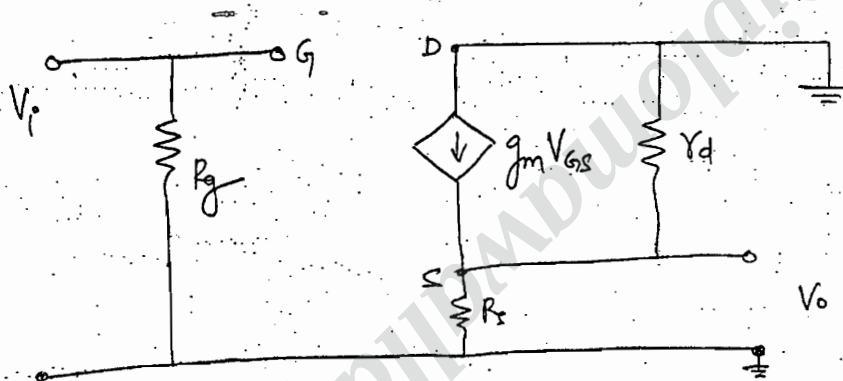
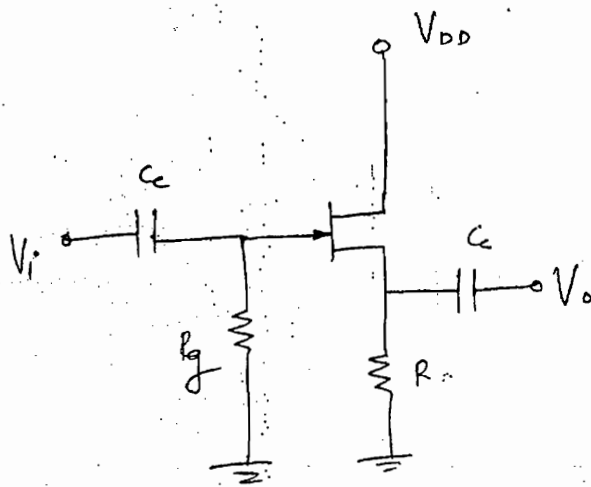
↓ ↓
neglected neglected.

$$A_v = \frac{V_o}{V_i} = \frac{-g_m R_D}{(1 + g_m R_S)}$$

$$A_v = \frac{-g_m R_D}{1 + g_m R_S}$$

if $g_m R_S \gg 1$, then

$$A_v = \frac{-R_D}{R_S} \rightarrow \text{Stable}$$

(3) Common Drain amplifier :

$$I_D + g_m V_{GS} = \frac{V_o}{r_d} + \frac{V_o}{R}$$

$$V_i = V_{GS} + V_o \Rightarrow V_o = -V_{GS} \Rightarrow V_{GS} = -V_o$$

$$I_D - g_m V_o = \frac{V_o}{r_d} + \frac{V_o}{R}$$

$$I_D = \left(g_m + \frac{1}{r_d} + \frac{1}{R} \right) V_o$$

o/p Impedance :

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$$Z_o = \frac{V_o}{I_D} = \frac{1}{\frac{1}{r_d} + \frac{1}{R} + g_m} \approx \frac{1}{\frac{1}{R} + g_m} \approx \frac{1}{g_m} \quad (\text{Impedance matching}).$$

$\downarrow$ neglect $\downarrow$ neglect

$$\approx \left(R_s // \frac{1}{g_m} \right)$$

Voltage gain :

$$V_o = g_m V_{gs} (r_d // R_s)$$

$$V_i = V_{gs} + V_o$$

$$V_i = V_{gs} + g_m V_{gs} (r_d // R_s)$$

$$V_{gs} = \frac{V_i}{1 + g_m (r_d // R_s)}$$

$$V_o = \frac{g_m \cdot V_i (r_d // R_s)}{1 + g_m (r_d // R_s)}$$

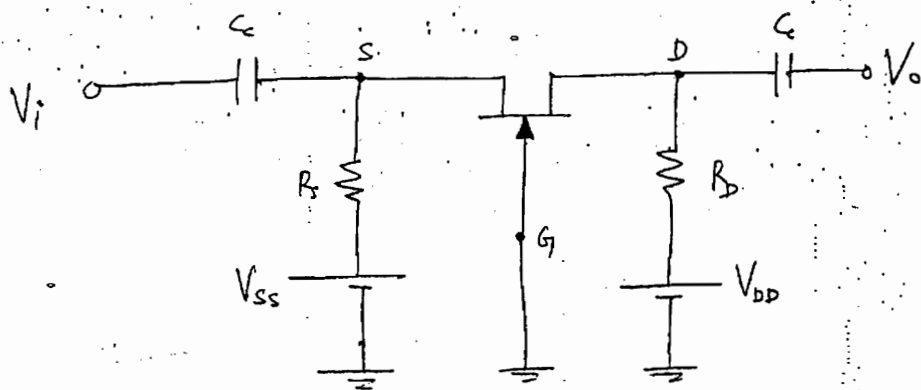
$$A_v = \frac{V_o}{V_i} = \frac{g_m R_s}{(1 + g_m R_s)}$$

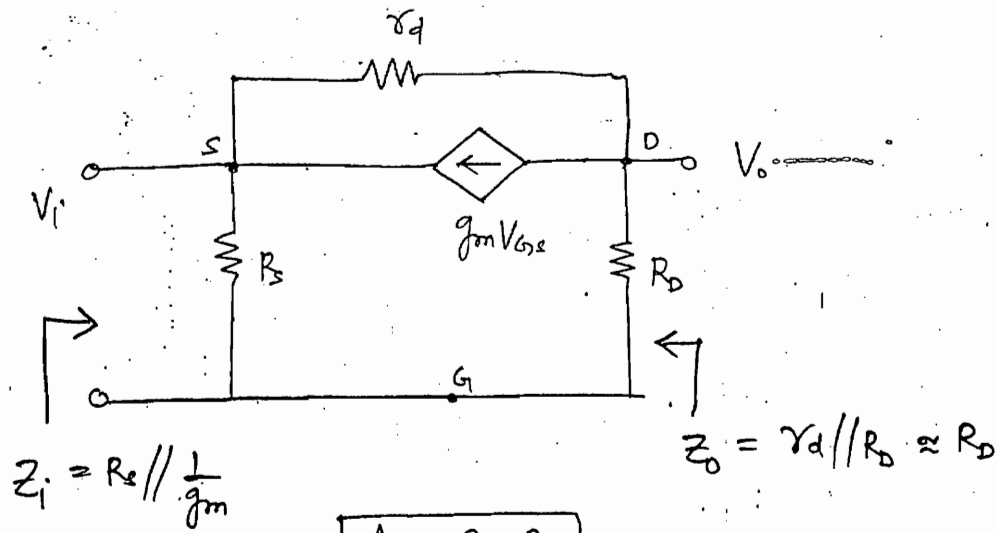
If $g_m R_s \gg 1$, then

$A_v = \frac{V_o}{V_i} \approx 1$

→ Voltage Buffer

(4). Common gate amplifier :





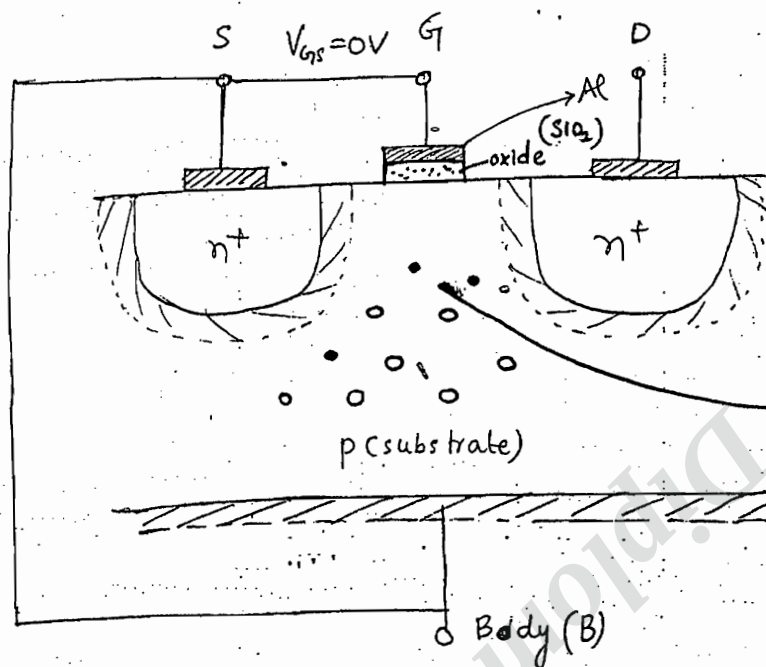
$$A_v = g_m R_D$$

Conclusions:

- (1). CS bypass
- (2). CS unbypass
- (3). CD
- (4). CG

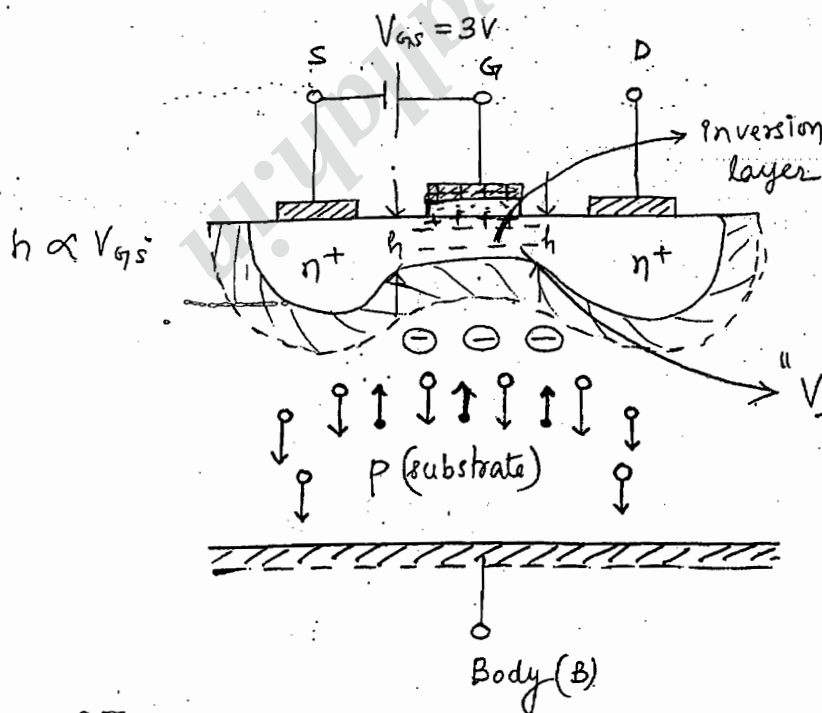
Z_i	Z_o	A_v
R_g	$r_d \parallel R_D \leftarrow$	$-g_m R_D$
R_g	$r_d + (1+\mu) R_s \parallel R_D$	$-\frac{g_m R_D}{(1+g_m R_s)}$
R_g	$R_s \parallel \frac{1}{g_m}$	$\frac{g_m R_s}{(1+g_m R_s)}$
$R_s \parallel \frac{1}{g_m} \leftarrow$	$r_d \parallel R_D \leftarrow$	$+g_m R_D$

n-channel Enhancement MOSFET (NMOS) :



Holes $\rightarrow$ majority charge carrier
Electrons $\rightarrow$ minority charge carrier

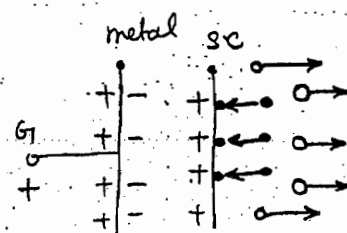
channel is not existing at $V_{gs} = 0V$
 $\therefore I_D = 0$



$h \propto V_{gs}$

" V_t " $\rightarrow$ threshold voltage (0.5V to 1V)

Mos capacitor



$(V_{gs} - V_t) \rightarrow$ Effective voltage (or) over drive voltage

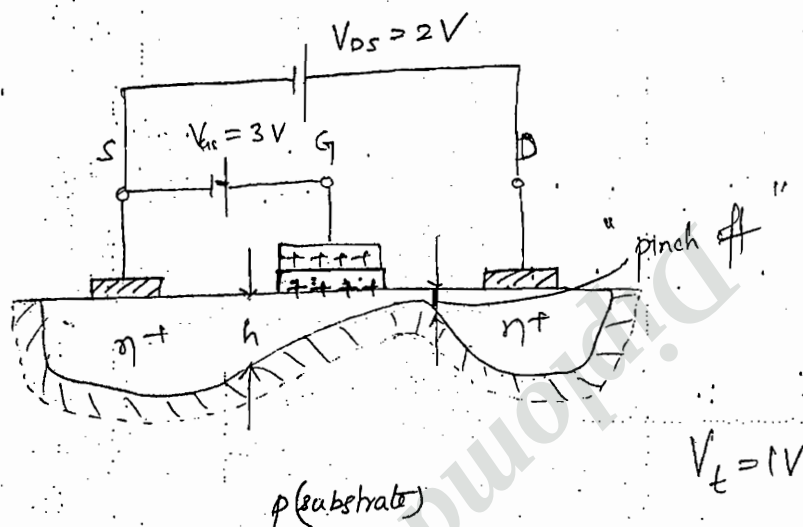
- V_t is the min^m V_{gs} voltage at which conducting channel will be formed.

$$V_{DG} = V_{DS} + V_{SG}$$

$$= V_{DS} - V_{GS}$$

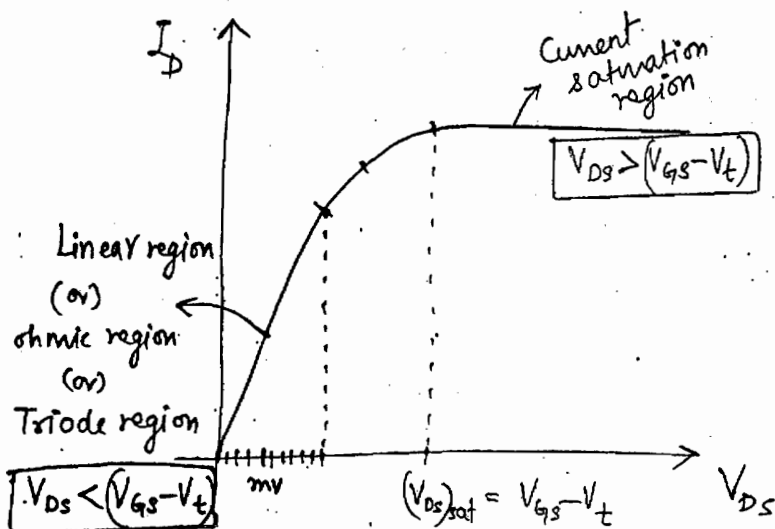
$$V_{GD} = V_{GS} - V_{DS}$$

$$\text{Here, } V_{DS} = 0 \Rightarrow V_{GD} = V_{GS}$$



$$\begin{aligned} V_{GD} &= V_{GS} - V_{DS} \\ &= 3V - 2V \\ &= 1V = V_t \end{aligned}$$

$$(V_{DS})_{\text{sat.}} = V_{GS} - V_t$$

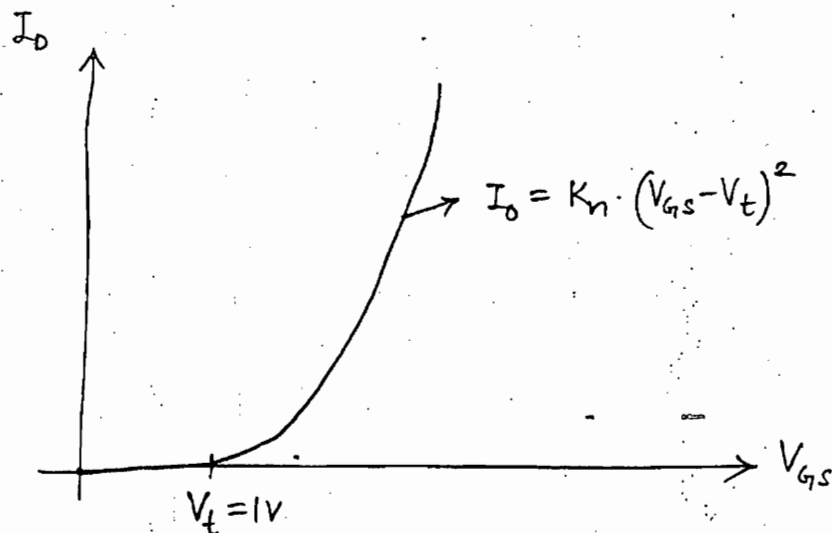


Similarity

Triode $\rightarrow$ vacuum

voltage control

FET, MOSFET



V-I characteristic eqn of MOSFET :

"NMOS"

$$I_D = \mu_n C_{ox} \left(\frac{W}{L}\right) \left\{ (V_{GS} - V_t) V_{DS} - \frac{1}{2} V_{DS}^2 \right\}$$

where, $\mu_n \rightarrow$ mobility of electron
 $C_{ox} \rightarrow$ capacitance of oxide layer

$$C_{ox} = \epsilon_{ox} / t_{ox}$$

$$\& \epsilon_{ox} = 3.9 \epsilon_0$$

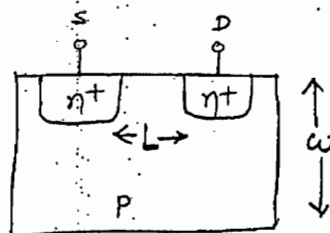
$$= 3.9 \times 8.85 \times 10^{-12} \text{ F/m}$$

$$= 3.45 \times 10^{-11} \text{ F/m}$$

A diagram showing a cross-section of a MOSFET gate. It consists of a gate oxide layer of thickness t_{ox} on a substrate. The area of the gate is A . The equation $C_{ox} = \frac{A \epsilon_{ox}}{t_{ox}}$ is written next to it, with $(A=1)$ noted.

$\left(\frac{W}{L}\right) \rightarrow$ aspect ratio

$\therefore \left(\frac{W}{L}\right)$ is very less for modern mosfets.



$(V_{GS} - V_t) \rightarrow$ Effective voltage (v) over drive voltage

$V_{DS} \rightarrow$ drain to source voltage

Triode region -

$$V_{DS} < V_{GS} - V_t, \text{ if } V_{DS} \text{ (mV)}$$

$$I_D = \mu_n C_{ox} \left(\frac{\omega}{L} \right) \cdot (V_{GS} - V_t) \cdot V_{DS}$$

$$R_{DS} = \frac{V_{DS}}{I_D} = \frac{1}{\mu_n \left(\frac{\omega}{L} \right) (V_{GS} - V_t)}$$

Current Saturation:

$$V_{DS} \geq V_{GS} - V_t$$

$$(V_{DS})_{sat} = V_{GS} - V_t$$

$$I_D = \frac{1}{2} \mu_n C_{ox} \left(\frac{\omega}{L} \right) \cdot (V_{GS} - V_t)^2$$

$\downarrow$
 K_n (Transconductance parameter) mA/V^2

* g_m should be more, so K_n should be more,

$\mu_n \rightarrow$ faster, gain $\rightarrow$ higher

* A good MOSFET should have high value of K_n

$$I_D = K_n (V_{GS} - V_t)^2$$

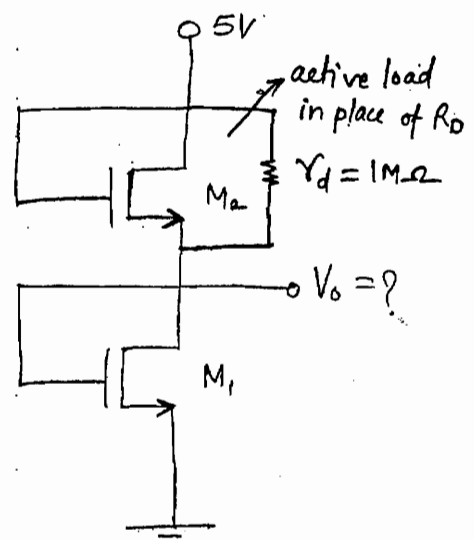
$$\therefore g_m = \frac{\partial I_D}{\partial V_{GS}} = 2K_n (V_{GS} - V_t)$$

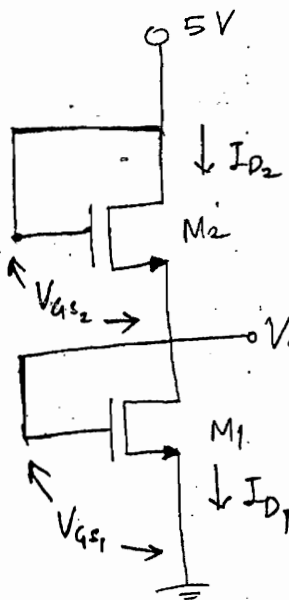
Active load MOSFET problems:

Q. given : $K_1 = 9 \text{ mA/V}^2$

$K_2 = 36 \text{ mA/V}^2$

$V_t = 1 \text{ V}$





$$I_{D1} = I_{D2}$$

$$K_1 (V_{GS1} - V_t)^2 = K_2 (V_{GS2} - V_t)^2$$

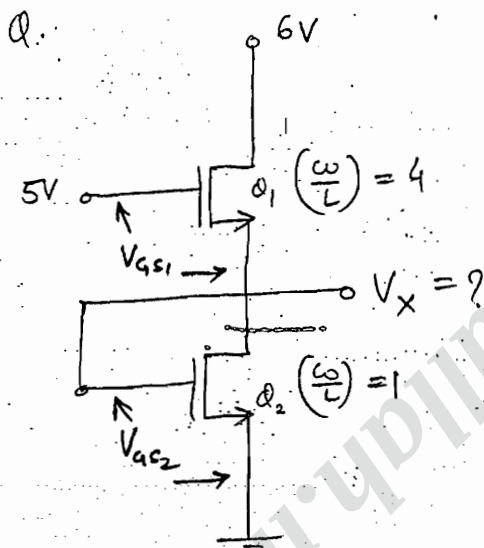
$$9 (V_0 - 1)^2 = 36 (5 - V_0 - 1)^2$$

$$V_0^2 + 1 - 2V_0 = 4 (V_0^2 + 16 - 8V_0)$$

$$V_0^2 - 2V_0 + 1 = 4V_0^2 - 32V_0 + 64$$

$$3V_0^2 - 30V_0 + 63 = 0$$

$$V_0 = 3 \text{ V or } 7 \text{ V (X)}$$



$$Q_1 \text{ \& } Q_2 \Rightarrow \mu_n C_{ox} = 100 \mu\text{A/V}^2$$

$$V_t = 1 \text{ V}$$

$$I_{D1} = I_{D2}$$

$$\frac{1}{2} \mu_n C_{ox} \left(\frac{W}{L}\right)_1 (V_{GS1} - V_t)^2 =$$

$$\frac{1}{2} \mu_n C_{ox} \left(\frac{W}{L}\right)_2 (V_{GS2} - V_t)^2$$

$$4 (5 - V_x - 1)^2 = (V_x - 1)^2$$

$$4 (V_x - 4)^2 = V_x^2 + 1 - 2V_x$$

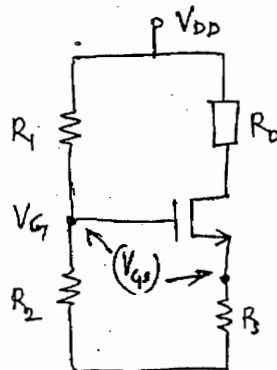
$$4V_x^2 + 64 - 32V_x = V_x^2 - 2V_x + 1$$

$$3V_x^2 - 30V_x + 63 = 0$$

$$\therefore V_x = 3 \text{ volts}$$

Biasing:

MOSFET Biasing:



$$I_D = K_n (V_{GS} - V_t)^2$$

↓
should be independent of
 K_n and V_t

$$V_G = V_{GS} + I_D R_S \Rightarrow V_G \approx I_D R_S$$

small $\rightarrow$ neglected.

$$\Rightarrow (I_D)_Q = \frac{V_G}{R_S}$$

$$(V_{GS})_Q = \sqrt{\frac{(I_D)_Q}{K_n}} + V_t$$

* This is the idea behind good biasing for MOSFET i.e. $(V_{GS} \approx 0)$.

* A good MOSFET requirement is high value of K_n and low value of V_t .

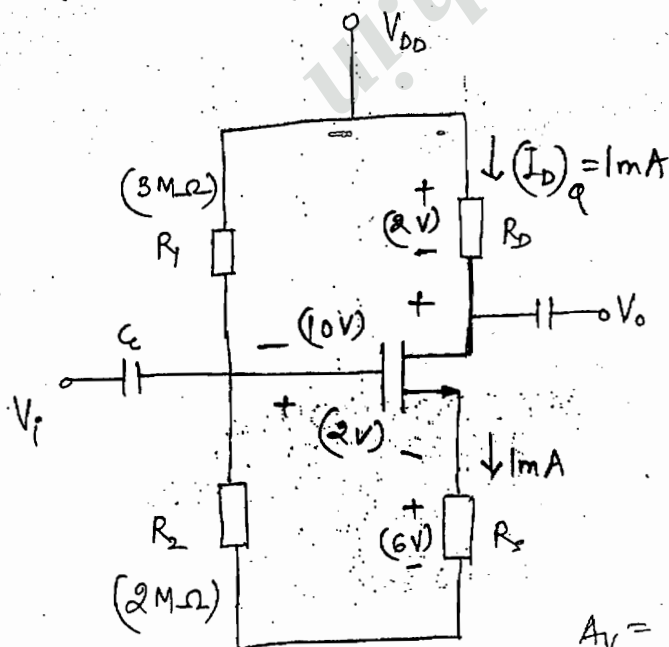
Q. Design an NMOS amplifier with given specifications:

$$V_{DD} = 20V, (I_D)_Q = 1mA, K_n = 1mA/V^2, V_t = 1V$$

$$(V_{DQ})_Q = 10V$$

Calculate the voltage gain of NMOS amplifier.

Sol.



$$(V_{GS})_Q = \sqrt{\frac{I_D}{K_n}} + V_t = 2V$$

$$R_S = \frac{6V}{1mA} = 6k\Omega$$

$$R_D = \frac{2V}{1mA} = 2k\Omega$$

$$V_G = V_{DD} \cdot \frac{R_2}{R_1 + R_2}$$

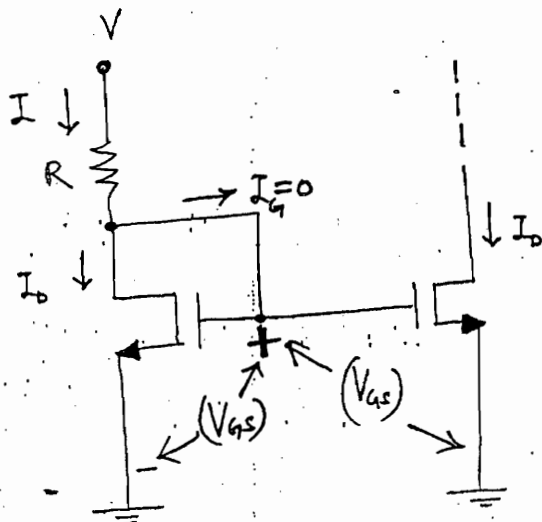
$$\frac{R_2}{R_1 + R_2} = \frac{8}{20} = \frac{2}{5}$$

$$A_v = -g_m R_D$$

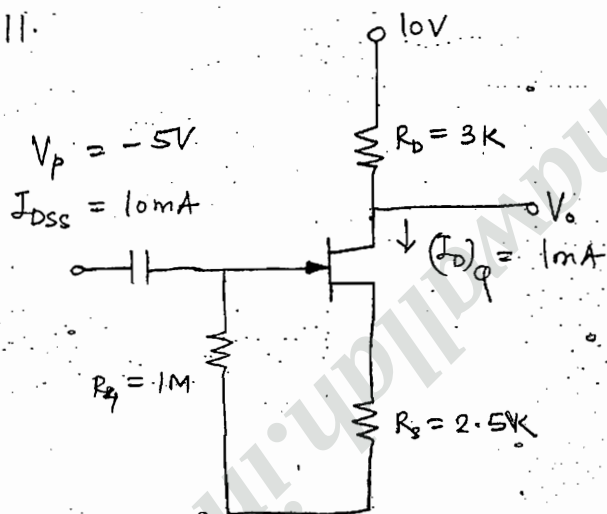
$$g_m = 2K_n (V_{GS} - V_t)^2 = 2(1)(2-1)^2 = 2mS$$

$$A_v = -2 \times 2 = -4$$

Current mirror biasing:



Q11.

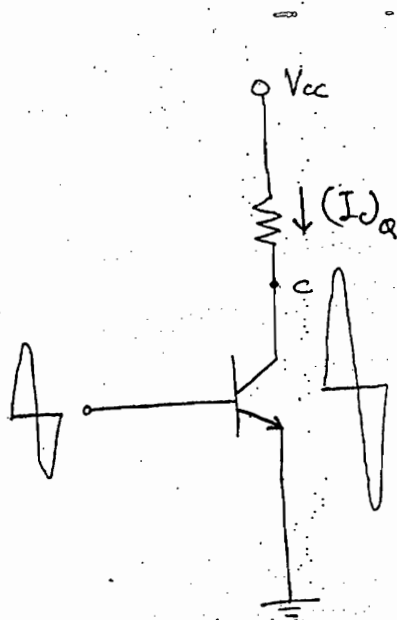


$$\begin{aligned} (V_{GS})_Q &= -(I_D)_Q \cdot R_S \\ &= -1\text{mA} \cdot 2.5\text{k}\Omega \\ &= -2.5 \end{aligned}$$

$$\begin{aligned} g_m &= \frac{-2I_{DSS}}{V_P} \left(1 - \frac{(V_{GS})_Q}{V_P} \right) \\ &= \frac{-2 \times 10}{-5} \left[1 + \frac{2.5}{5} \right] \\ &= 2 \end{aligned}$$

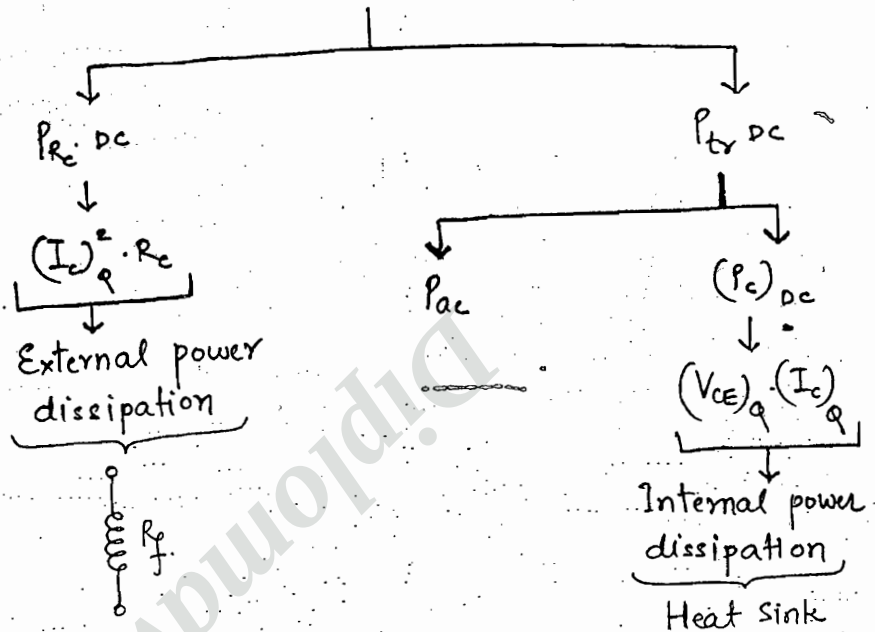
$$A_V = -g_m R_D = -2 \times 3\text{k}\Omega = -6$$

Power Amplifiers



Power diagram

$$P_{DC} = V_{CC} \cdot (I_C)_Q$$



$$\% \eta = \frac{P_{ac}}{P_{DC}}$$

$$P_{DC} = P_{ac} + \text{losses}$$

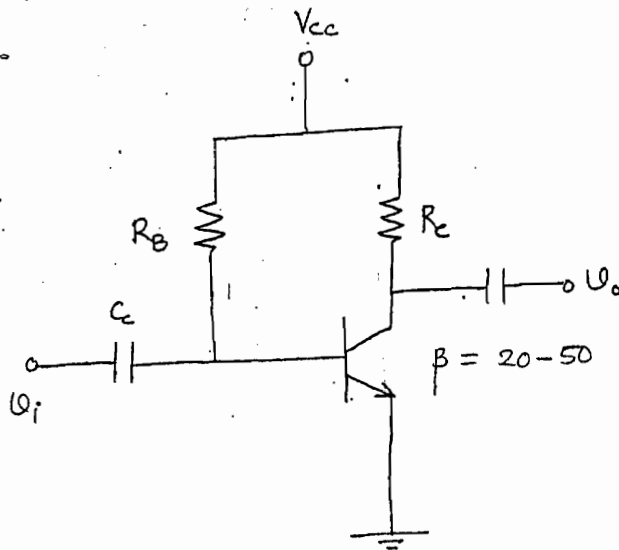
$$P_{DC} = P_{ac} + P_D$$

$$\text{If } P_D \rightarrow 0 \Rightarrow \eta \rightarrow 100\%$$

Power amplifier is a device which converts DC power to ac power and whose action is controlled by ac e/p s/g.

Class A amplifier:

- (1). Class A series fed power amplifier
- (2). Class A X^r coupled power amplifier

(1) Class A series fed power amplifier :

Base width is high, so recombination high, I_B is high
so β is less.

DC i/p $\rightarrow P_{DC} = V_{CC} \cdot (I_C)_Q$

Ac o/p $\rightarrow P_{ac} = V_{rms} \cdot I_{rms}$
 $= \frac{V_m}{\sqrt{2}} \cdot \frac{I_m}{\sqrt{2}}$

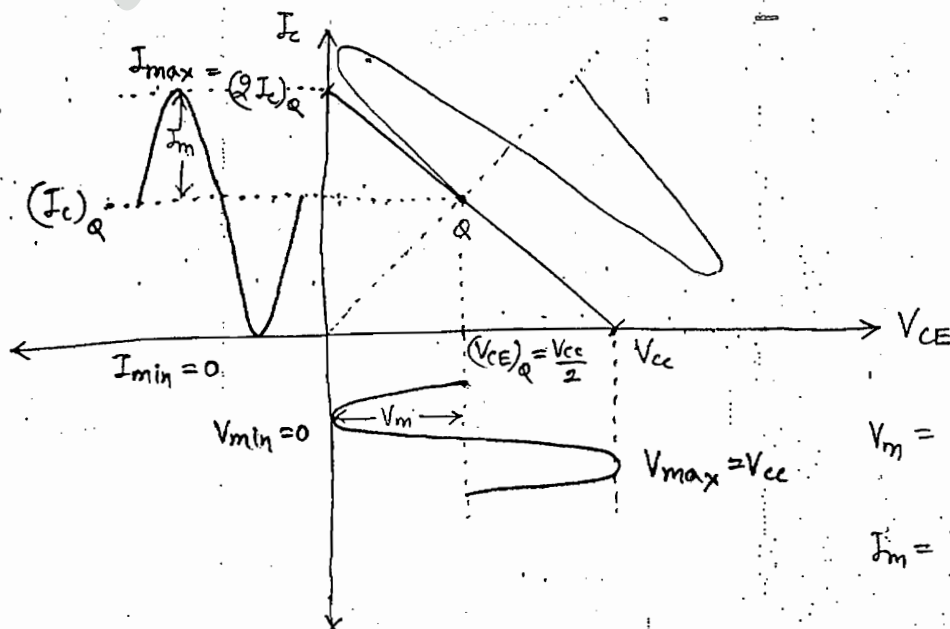
DC conditions :

$$(I_B)_Q = \frac{V_{CC} - V_{BE}}{R_B}$$

$$(I_C)_Q = \beta (I_B)_Q$$

$$(V_{CE})_Q = V_{CC} - (I_C)_Q \cdot R_c$$

$$= \frac{V_m I_m}{2}$$

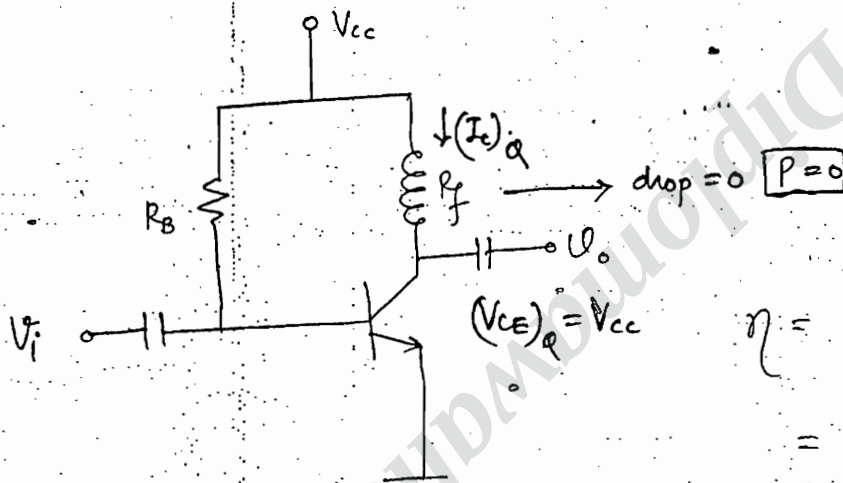


$$V_m = \frac{V_{max} - V_{min}}{2}$$

$$I_m = \frac{I_{max} - I_{min}}{2}$$

$$\begin{aligned}
 \eta &= \frac{P_{ac}}{P_{DC}} = \frac{1}{8} \frac{(V_{max} - V_{min})(I_{max} - I_{min})}{V_{CC} \cdot (I_C)_Q} \\
 &= \frac{1}{8} \frac{(V_{CC} - 0)[(2I_C)_Q - 0]}{V_{CC} \cdot (I_C)_Q} \\
 &= \frac{1}{8} \cdot 2 = \frac{1}{4} = 25\%
 \end{aligned}$$

(2). X^r coupled :



$$\begin{aligned}
 \eta &= \frac{2V_{CC} \times 2(I_C)_Q}{8 \times V_{CC} \cdot (I_C)_Q} \\
 &= \frac{1}{2} = 50\%
 \end{aligned}$$

Conclusion :

In class A series fed power amplifier the efficiency η_{max} is 25%. To improve this efficiency, replace the resistor R_C with an inductor L_f where the efficiency goes to 50%.

Power dissipation -

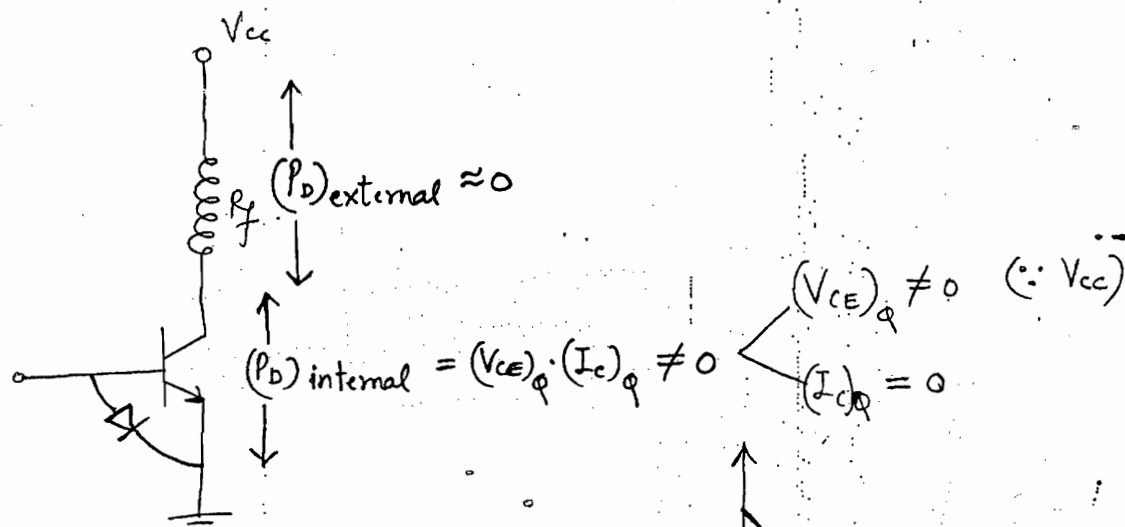
$$P_{DC} = P_{AC} + P_D$$

$$P_D = P_{DC} - P_{AC}$$

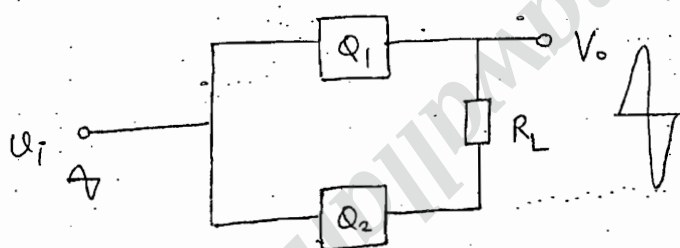
$$= V_{CC} \cdot (I_C)_Q - \frac{V_m I_m}{2}$$

At zero ω conditions, $P_D = P_{DC}$ ($\because P_{ac} = 0$)

Class B power amplifiers :



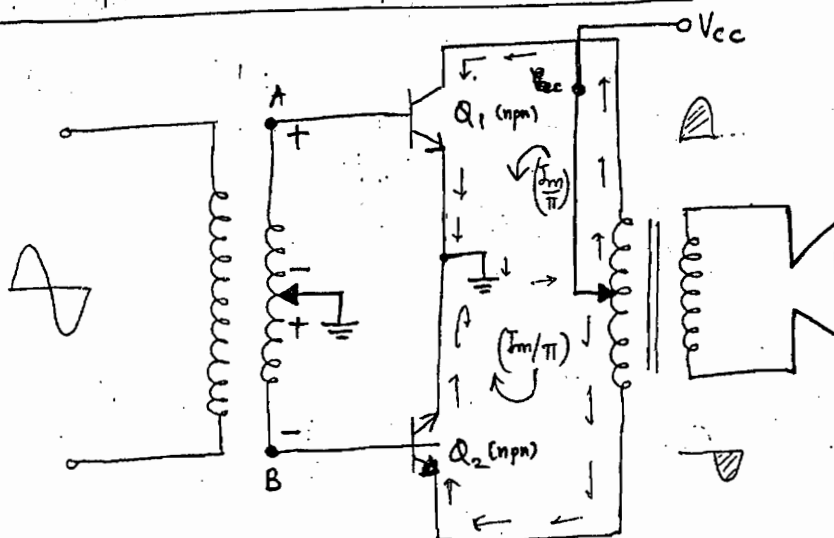
Device should sleep when there is no s/g.



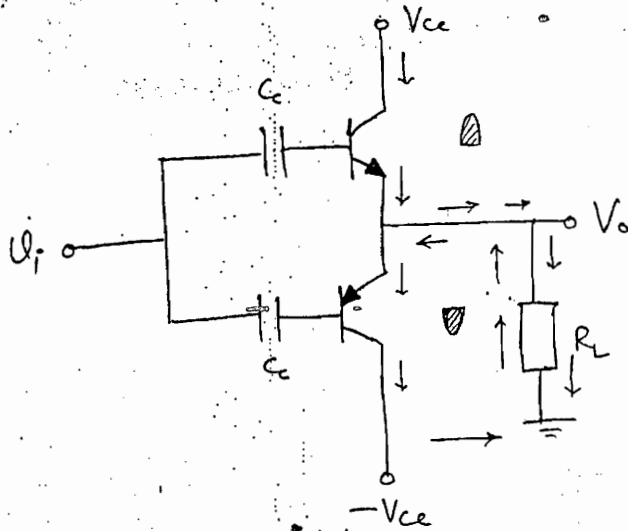
Respond to only +ve s/g

The diagram shows a sine wave and its positive half-cycle, indicating that the output is only the positive half-cycle of the input signal.

Push pull class B power amplifier : (single Battery)



Complementary symmetry class B : (2 Batteries)



$$(I_c)_Q = 0$$

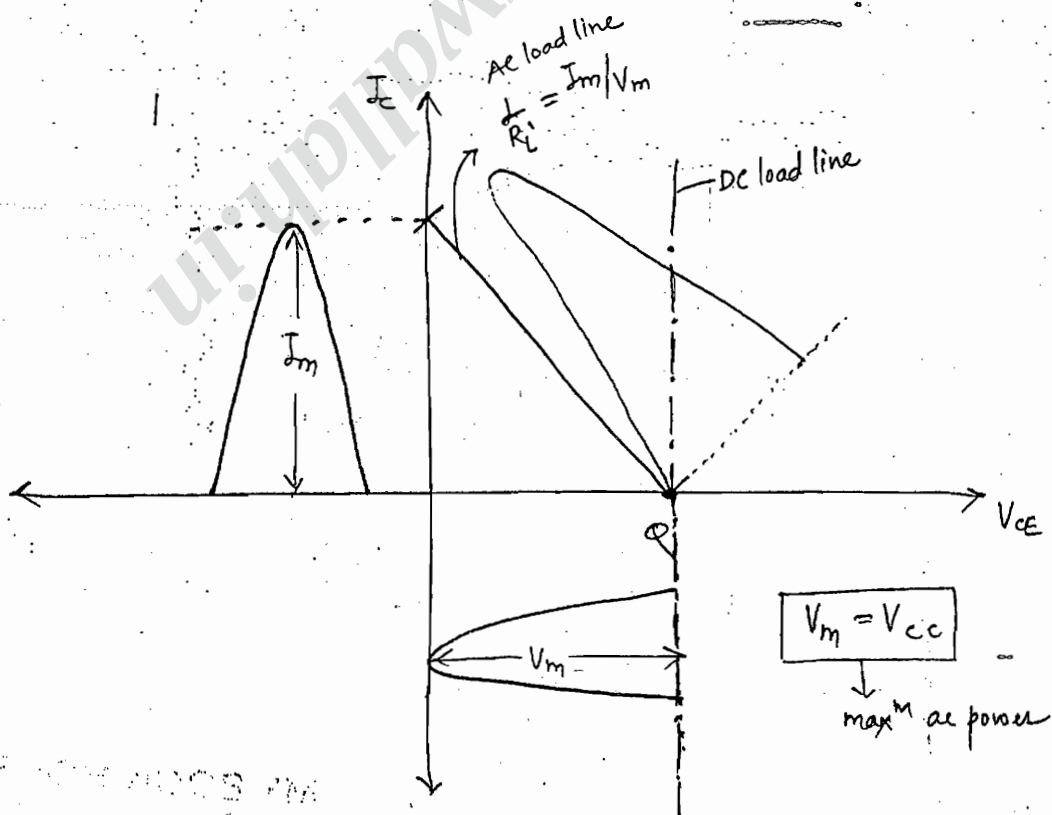
$$P_{DC} = V_{ce} \cdot I_{dc}$$

$$= V_{ce} \cdot \left(\frac{2 I_m}{\pi} \right)$$

Efficiency

$$P_{ac} = \frac{V_m I_m}{2}$$

$$\% \eta = \frac{P_{ac}}{P_{DC}} = \frac{V_m I_m / 2}{V_{ce} \cdot 2 I_m / \pi} = \frac{V_m \pi}{4 V_{ce}}$$



$$\% \eta = \frac{V_{ce} \cdot \pi}{4 V_{ce}} = \frac{\pi}{4} = 78.5\%$$

Power dissipation in class B :

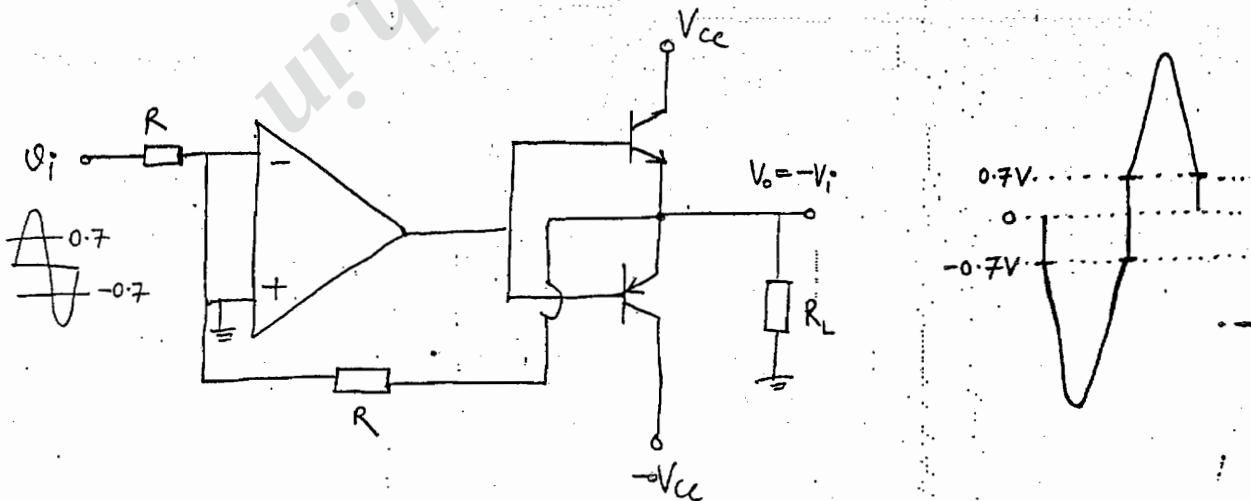
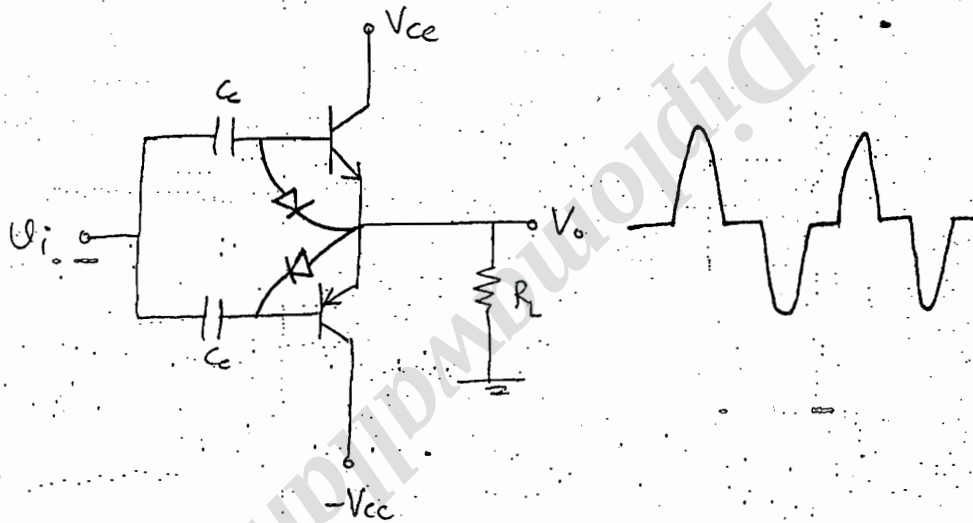
$$P_D = P_{DC} - P_{ac}$$

$$= V_{ce} \cdot \frac{2I_m}{\pi} - \frac{V_m I_m}{2}$$

At ^{zero} s/g conditions, $V_m = 0$, $I_m = 0$.

$$P_D = 0 \quad (\text{Sleeping mode})$$

Cross over distortion:



Diplomawallah.in

Oscillators

$$\frac{V_{O1}}{V_i} = \frac{2(1-\alpha) S^2 R^2 C^2}{S^2 R^2 C^2 + 2\alpha SRC + 1} \quad (\text{H.P.})$$

$$\frac{V_{O2}}{V_i} = \frac{-2(1-\alpha) SRC}{S^2 R^2 C^2 + 2\alpha SRC + 1} \quad (\text{B.P.})$$

$$\frac{V_{O3}}{V_i} = \frac{2(1-\alpha)}{S^2 R^2 C^2 + 2\alpha SRC + 1} \quad (\text{L.P.})$$

Band-pass $\Rightarrow$

$$\frac{V_{O2}}{V_i} = \frac{-2(1-\alpha) SRC}{S^2 R^2 C^2 + 2\alpha SRC + 1}$$

$$= \frac{-2(1-\alpha) SRC}{-\omega^2 R^2 C^2 + 2\alpha SRC + 1}$$

$$\boxed{\omega_0 = \frac{1}{RC}}$$

$$= \frac{-2(1-\alpha) SRC}{2\alpha SRC}$$

$$= \frac{1-\alpha}{\alpha} = -2(1-\alpha) Q$$

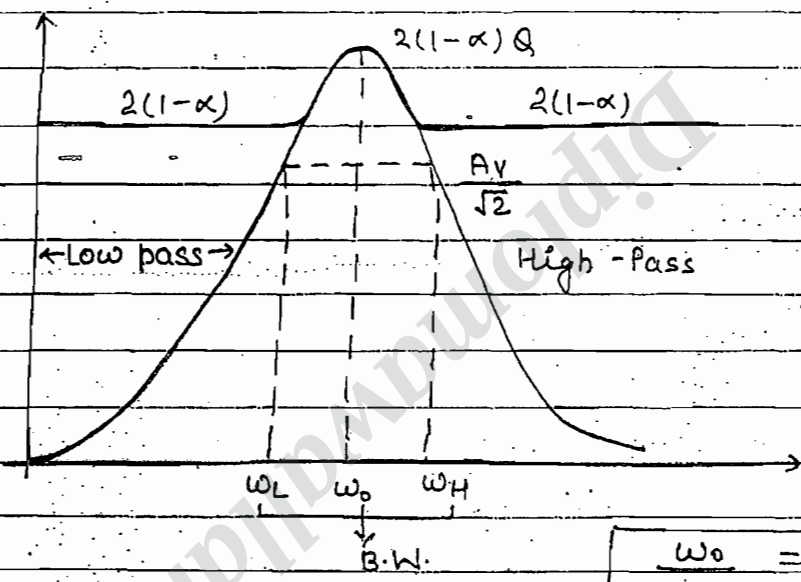
$Q \rightarrow$ Quality factor ;

$$\left\{ Q = \frac{1}{2\alpha} \right\} \text{ pole } Q$$

$\omega_0 = \frac{1}{RC}$... Pole frequency.

$$\frac{V_{o1}}{V_i} = \frac{2(1-\alpha) s^2}{\omega_0^2 \left(\frac{s^2}{\omega_0^2} + \frac{s}{\omega_0 Q} + 1 \right)}$$

$$\frac{V_{o3}}{V_i} = \frac{2(1-\alpha)}{\frac{s^2}{\omega_0^2} + \frac{s}{\omega_0 Q} + 1}$$



$\frac{\omega_0}{B.W.} = Q$

Pole-Equation $\Rightarrow$

$$\frac{s^2}{\omega_0^2} + \frac{s}{\omega_0 Q} + 1 = 0 \quad \text{Assume } \frac{s}{\omega_0} = x$$

$$\rightarrow x^2 + \frac{x}{Q} + 1 = 0$$

$$\frac{s_{1,2}}{\omega_0} = \frac{-1}{Q} \pm \sqrt{\left(\frac{1}{Q}\right)^2 - 4(1)(1)}$$

$$\frac{s_{1,2}}{\omega_0} = \frac{-1}{Q} \pm \sqrt{\frac{1}{Q^2} - 4}$$

Real part $\Rightarrow \frac{1}{Q^2} > 4$, $Q < \frac{1}{\sqrt{4}}$

$Q < \frac{1}{2}$

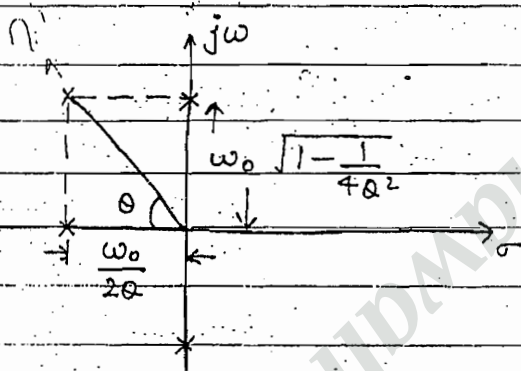
Imaginary part $\Rightarrow$

$Q > 1/2$

$$\Rightarrow \frac{S_{1,2}}{\omega_0} = \frac{-1}{Q} \pm j \frac{\sqrt{4 - \frac{1}{Q^2}}}{2}$$

$$\Rightarrow \frac{S_{1,2}}{\omega_0} = \frac{-1}{2Q} \pm j \frac{\sqrt{1 - \frac{1}{4Q^2}}}{2Q}$$

$$\Rightarrow \frac{S_{1,2}}{\omega_0} = \frac{-\omega_0}{2Q} \pm j \frac{\omega_0 \sqrt{1 - \frac{1}{4Q^2}}}{2Q}$$



$$\theta = \tan^{-1} \frac{\omega_0 \sqrt{1 - \frac{1}{4Q^2}}}{\omega_0 / 2Q}$$

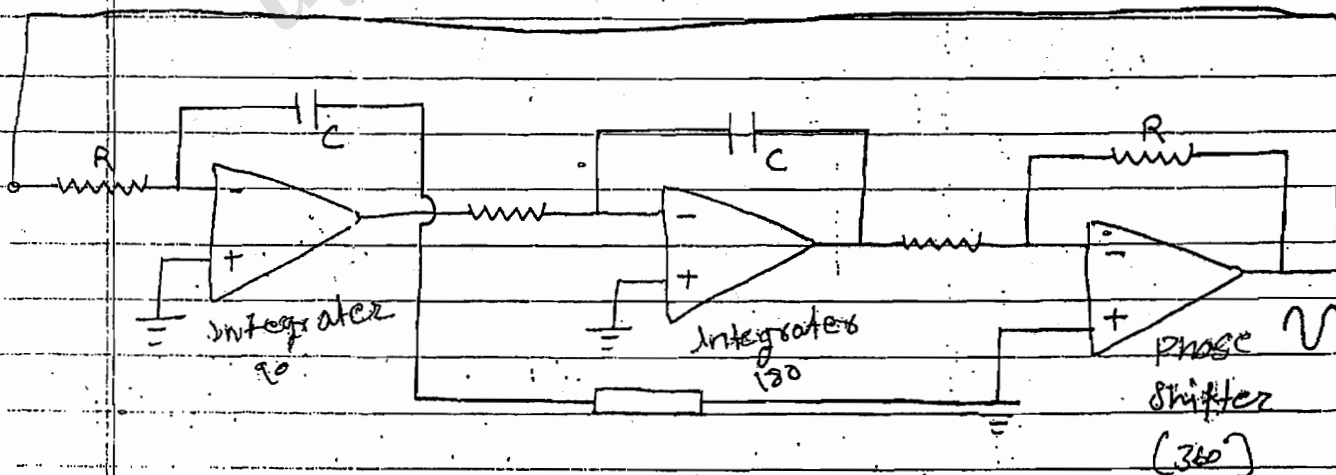
$$\theta = \tan^{-1} 2Q \sqrt{1 - \frac{1}{4Q^2}}$$

$$Q \rightarrow \infty$$

$$\theta = \tan^{-1} \infty$$

$$\theta = 90^\circ$$

$$Q = \frac{1}{2\alpha} = \infty , \alpha = 0$$



Harmonic Oscillator
(or)

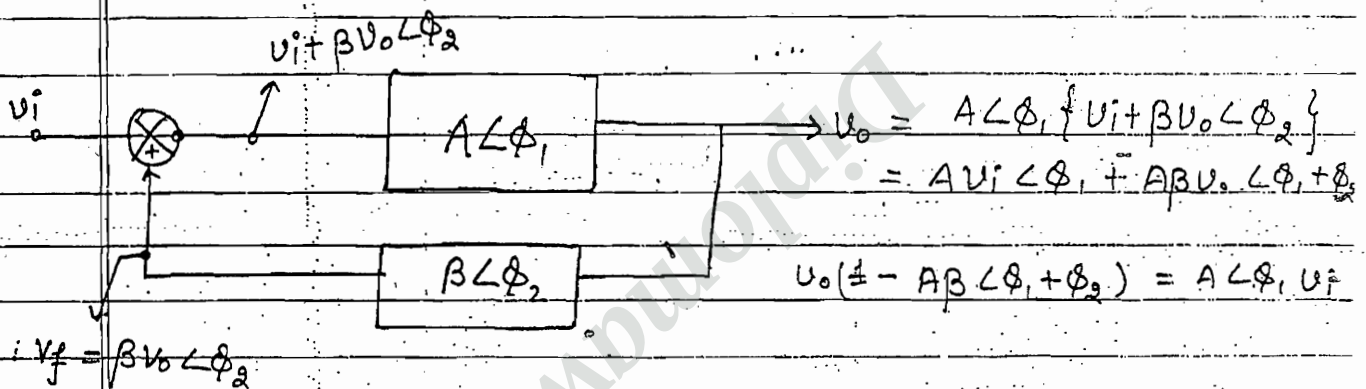
Quadrature Oscillator [* should be zero then pole will come on imag axis i quadrature]

$$\frac{\partial^2 v_o}{\partial t^2} + \alpha \frac{\partial v_o}{\partial t} + \gamma v_o = \underbrace{v_i}_{\downarrow 0}$$

$$\boxed{\frac{\partial^2 v_o}{\partial t^2} + \gamma v_o = 0}$$

harmonic oscillators:

* Positive Feedback Concept $\Rightarrow$



$$v_o = \frac{A \angle \phi_1}{1 - \beta A \angle \phi_1 + \phi_2} v_i \quad \dots \quad 1 - \beta A \angle \phi_1 + \phi_2 = 0$$

Conditions $\Rightarrow |A\beta| = 1$

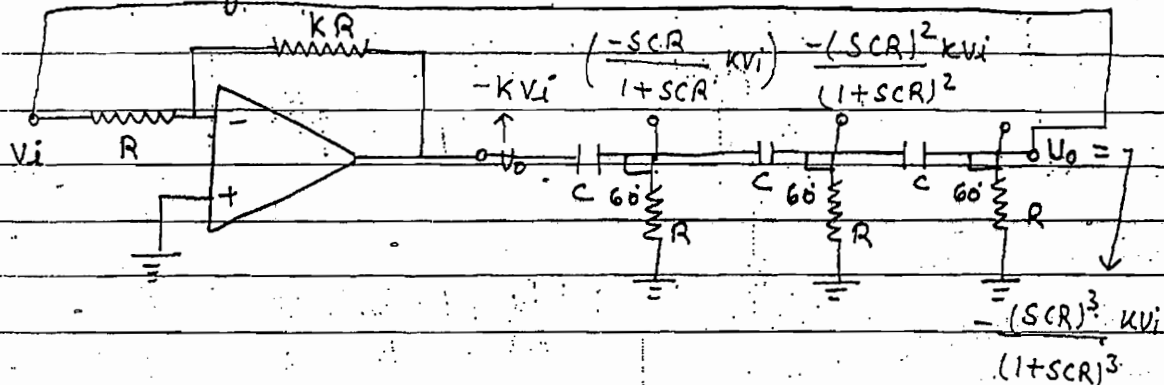
$$A = \frac{v_o}{v_i}, \quad \beta = \frac{v_f}{v_o}$$

$$A\beta = \frac{v_o}{v_i} \cdot \frac{v_f}{v_o} = \frac{v_f}{v_i} = 1 \quad \boxed{v_f = v_i}$$

$\angle \phi_1 + \phi_2 = 0^\circ$ (or) multiples of 2π .

Barkhausen criteria.

* RC-Phase Shift Oscillators $\Rightarrow$



$$-\frac{(SCR)^3}{(1+SCR)^3} KV_i = V_i$$

$$\rightarrow (1+SCR)^3 + (SCR)^3 K = 0$$

$$\rightarrow 1 + 3s^2C^2R^2 + 3sCR + (SCR)^3(K+1) = 0$$

$$\rightarrow 1 - 3\omega^2C^2R^2 + 3j\omega CR - j\omega 3R^3C^3(K+1) = 0$$

$$1 - 3\omega^2C^2R^2 = 0 \quad (\text{Real part})$$

$$\left\{ \omega_0 = \frac{1}{\sqrt{3}RC} \right\}$$

$$3\omega CR - \omega^3 R^3 C^3 (K+1) = 0$$

$$\rightarrow 1 - 3\omega^2 R^2 C^2 (K+1) = 0 \quad \leftarrow \text{at gain = 8 we'll get on imag. axis poles}$$

$$\rightarrow \omega_0 = \frac{1}{\sqrt{3}RC}$$

$$3 - 1(K+1) = 0$$

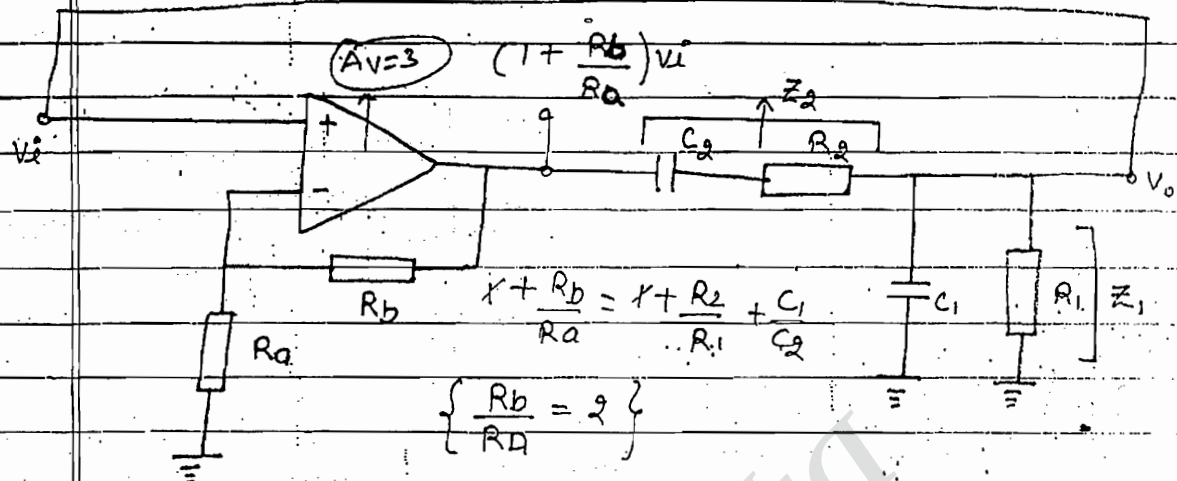
$$K = 8$$

Conclusions $\Rightarrow$

- RC-phase shift osci. are used for audio freq. ranges (20 Hz - 20 KHz).
- Quality factor for RC-n/w will be in multiples of 10.
- RC-phase shift Oscill. can't be design for dynamic range of frequency.

* Wein-Bridge Oscillator $\Rightarrow$

$$\left(1 + \frac{R_b}{R_a}\right) \left(1 + \frac{R_2}{R_1} + \frac{C_1}{C_2}\right) = 1$$



$$V_o = \frac{\left(1 + \frac{R_b}{R_a}\right) V_i \cdot Z_1}{Z_1 + Z_2} \Rightarrow \frac{\left(1 + \frac{R_b}{R_a}\right) V_i}{1 + Z_2 Y_1}$$

$$V_o = \frac{\left(1 + \frac{R_b}{R_a}\right) V_i}{1 + \left(\frac{R_2 + \frac{1}{sC_2}}{R_1}\right) \left(\frac{1 + sC_1 R_1}{1 + sC_1 R_2}\right)} \Rightarrow \frac{\left(1 + \frac{R_b}{R_a}\right) V_i}{1 + \frac{R_2}{R_1} + \frac{C_1}{C_2} + \frac{1}{sC_2 R_1} + sC_1 R_2}$$

$$V_o = \frac{\left(1 + \frac{R_b}{R_a}\right) V_i}{1 + \frac{R_2}{R_1} + \frac{C_1}{C_2} + j \left[\frac{-1}{\omega C_2 R_1} + \omega C_1 R_2 \right]}$$

j-term $\Rightarrow$

$$\frac{-1}{\omega C_2 R_1} + \omega C_1 R_2 = 0$$

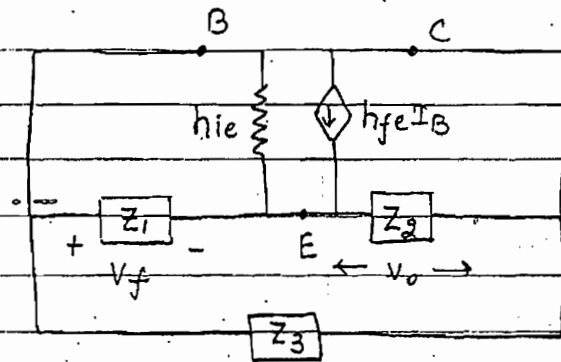
$$\omega_o = \frac{1}{\sqrt{R_1 R_2 C_1 C_2}}$$

$$R_1 = R_2 = R, \quad C_1 = C_2 = C$$

$$\omega_o = \frac{1}{RC}$$

$$f_o = \frac{1}{2\pi RC}$$

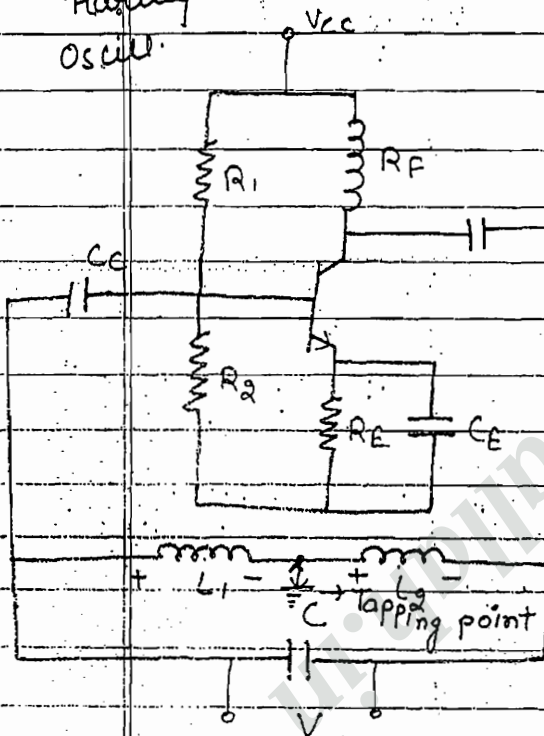
3). LC - Oscillators $\Rightarrow$



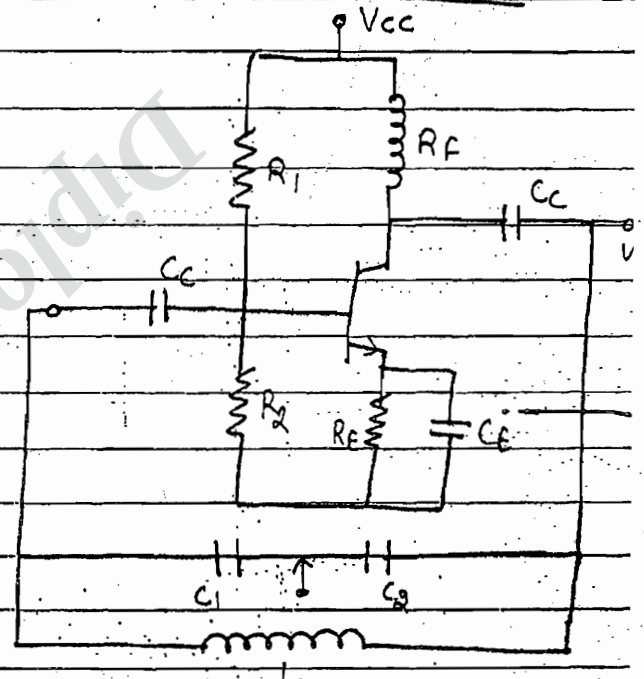
Condition

Z_1 & Z_2 are same reactive components. Z_3 is opposite reactive compo. (reactive only).

Hartley
Oscill.



Colpitts Oscill.



Analysis of LC - Oscillators $\Rightarrow$

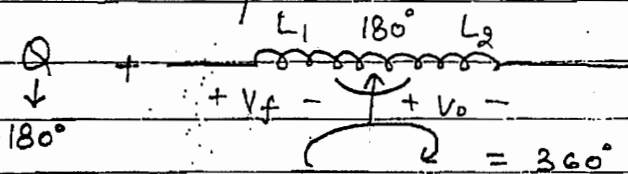
Q:1 what is the imp. of inductor (R_F) in LC oscill. ckt?

Ans: Inductor R_F is used as an isolation b/w dc - supply & ac - signal.

Q:2 Explain phase relation in LC oscill.?

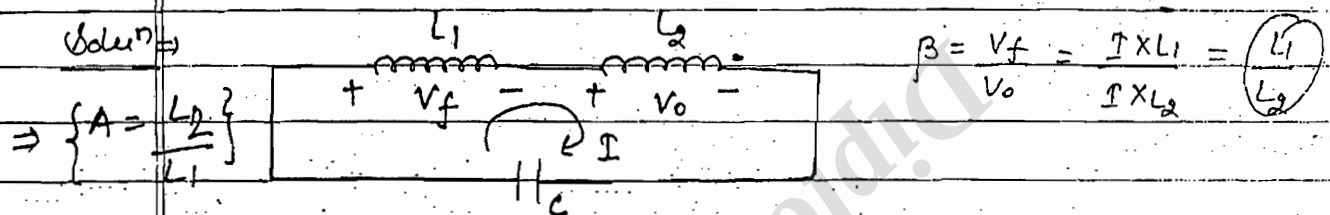
Ans:

Solnⁿ ⇒ Hartley ⇒

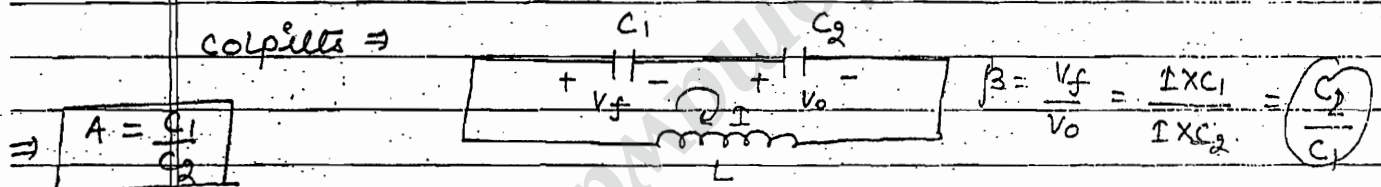


Ques:3 Explain conditions for gain A and β for Hartley osci.

Solnⁿ ⇒



colpitts ⇒



Ques:3 Explain expressions for freq. of oscilla. in LC ckt?

$$\text{Soln}^n \Rightarrow f = \frac{1}{2\pi\sqrt{LC}} = \frac{1}{2\pi\sqrt{L_T C}}$$

$$L_T = L_1 + L_2 + 2m, \quad m \rightarrow \text{negligible}$$

$$L_T = L_1 + L_2$$

$$\text{colpitts} \Rightarrow f = \frac{1}{2\pi\sqrt{LC_T}}, \quad \frac{1}{C_T} = \frac{1}{C_1} + \frac{1}{C_2}$$

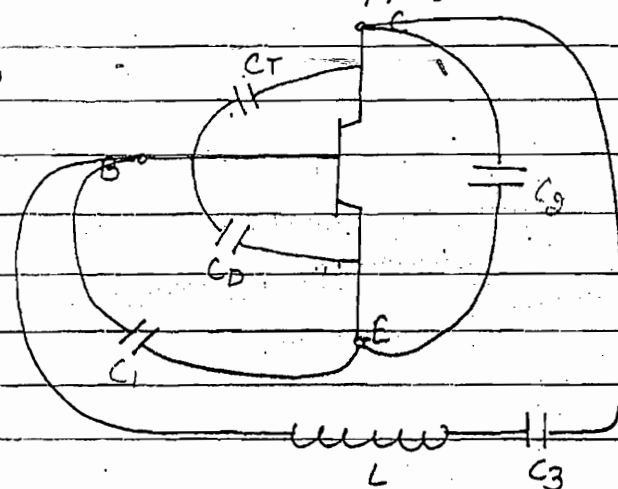
Ques: why LC oscilla. can't be use for low freq. of oscilla.

$$\text{Soln}^n \Rightarrow f \downarrow = \frac{1}{2\pi\sqrt{LC}} \uparrow; \quad f \rightarrow \text{less}, \quad \text{LC compo. become high.}$$

* RC never use for high freq.

Ques Explain about clapp oscillator design ?

Soln $\Rightarrow$



$$f = \frac{1}{2\pi\sqrt{LC_T}}$$

$$\frac{1}{C_T} = \frac{1}{C_1} + \frac{1}{C_2} + \frac{1}{C_3}$$

$$C_1 \gg C_3, C_2 \gg C_3$$

$$\frac{1}{C_T} = \frac{1}{C_3}$$

C_1, C_2 are getting effect by high

freq [C_D and C_T Problem]

but C_3 is free from them

$$\left\{ f = \frac{1}{2\pi\sqrt{LC_3}} \right\}$$

• A quality factor of an LC-circuit is in multiples of 100. This LC oscill. can stabilize the freq. upto 5 MHz.

• Modified version of Colpitts is called Clapp oscillator which can stabilize freq. upto 10 MHz.

• To stabilize freq. above 10 MHz crystal design will play a role which is known as piezoelectric effect principle. When a mechanical force is applied on one face of crystal, an electrical signal will be generated on other face of crystal and vice versa. Typically Q (quality factor) of crystal will be around 20,000 (multiples of 1000).

FET / MOSFET

- FET Biasing
- FET amplifier

O/P RAMP

- differential Amplifier
- op amp application